

Global Chip Handler in Semiconductor Market Research Report 2022 Professional Edition

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Abstracts

The global Chip Handler in Semiconductor market was valued at 768.8 Million USD in 2021 and will grow with a CAGR of 4.34% from 2021 to 2027, based on HNY Research newly published report.

The prime objective of this report is to provide the insights on the post COVID-19 impact which will help market players in this field evaluate their business approaches. Also, this report covers market segmentation by major market vendors, types, applications/end users and geography(North America, East Asia, Europe, South Asia, Southeast Asia, Middle East, Africa, Oceania, South America).

Semiconductor Chip Handlers are primarily used to for sorting, testing and transferring chips with the simple electrical interface diagram: ATE ? ITA ? DUT (package) ? Handler. And these machines are capable of processing thousands of chips in an hour. Normally, packaged parts use a handler to place the device on a customized interface board, then IC chip handler transfers finished chips to the inspection equipment for final visual and electrical performance test and then sort the chips into good, bad or other bins based on test results. Note: ATE: Automatic Test Equipment; ITA: Interface Test Adapter; DUT: Device Under Test. The OSAT segment is the largest of the chip handler market and is expected to garner close to 63% of market share by the end of the forecast period. The growth of the semiconductor industry and rising costs of packaging and testing equipment will create a high demand for automated test equipment along with chip handlers from these OSATs. They are also investing in precision and high-speed IC test handlers so that they can speed up production and deliver high-performance products to market faster. The global semiconductor chip handler market is consolidated with the presence of just a few suppliers. The manufacturing process requires high capital investment. Manufacturers are looking for

ways to expand their production capacity and would like to get products to the market as fast as possible. Vendors who can offer cheaper and technologically advanced products will be able to improve their market share during the forecast period. Key vendors in this market are -ASM Pacific Technology, Advantest, Boston Semi Equipment, Seiko Epson Corporation.

By Market Vendors:

Advantest

ASM Pacific Technology

Cohu

MCT

Boston Semi Equipment

Seiko Epson Corporation

TESEC Corporation

Hon Precision

Chroma

SRM Integration

SYNAX

CST

ChangChuan Technology

By Types:

Logic Chip Handler

Memory Chip Handler

By Applications:

OSATs

IDMs

Key Indicators Analysed

Market Players & Competitor Analysis: The report covers the key players of the industry including Company Profile, Product Specifications, Production Capacity/Sales, Revenue, Price and Gross Margin 2016-2027 & Sales with a thorough analysis of the market's competitive landscape and detailed information on vendors and comprehensive details of factors that will challenge the growth of major market vendors.

Global and Regional Market Analysis: The report includes Global & Regional market status and outlook 2016-2027. Further the report provides break down details about each region & countries covered in the report. Identifying its sales, sales volume & revenue forecast. With detailed analysis by types and applications.

Market Trends: Market key trends which include Increased Competition and Continuous Innovations.

Opportunities and Drivers: Identifying the Growing Demands and New Technology

Porters Five Force Analysis: The report provides with the state of competition in industry depending on five basic forces: threat of new entrants, bargaining power of suppliers, bargaining power of buyers, threat of substitute products or services, and existing industry rivalry.

Key Reasons to Purchase

To gain insightful analyses of the market and have comprehensive understanding of the global market and its commercial landscape.

Assess the production processes, major issues, and solutions to mitigate the development risk.

To understand the most affecting driving and restraining forces in the market and its

impact in the global market.

Learn about the market strategies that are being adopted by leading respective organizations.

To understand the future outlook and prospects for the market.

Besides the standard structure reports, we also provide custom research according to specific requirements

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