

# Global Advanced Packaging Market Research Report 2012-2024

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# **Abstracts**

#### **SUMMARY**

During the final stages of semiconductor development, a tiny block of materials (the silicon wafer, logic, and memory) is wrapped in a supporting case that prevents physical damage and corrosion and allows the chip to be connected to a circuit board. Typical packaging configurations have included the leadless chip carriers and pin-grid arrays of the 1980s, the system-in-package and package-on package setups of the 2000s, and, most recently, 2-D integrated-circuit technologies such as wafer-level, flip-chip, and through silicon via setups.

The global Advanced Packaging market will reach xxx Million USD in 2019 with CAGR xx% 2019-2024. The objective of report is to define, segment, and project the market on the basis of product type, application, and region, and to describe the content about the factors influencing market dynamics, policy, economic, technology and market entry etc.

Based on products type, the report describes major products type share of regional market. Products mentioned as follows:

3.0 DIC

FO SIP

FO WLP

3D WLP

**WLCSP** 

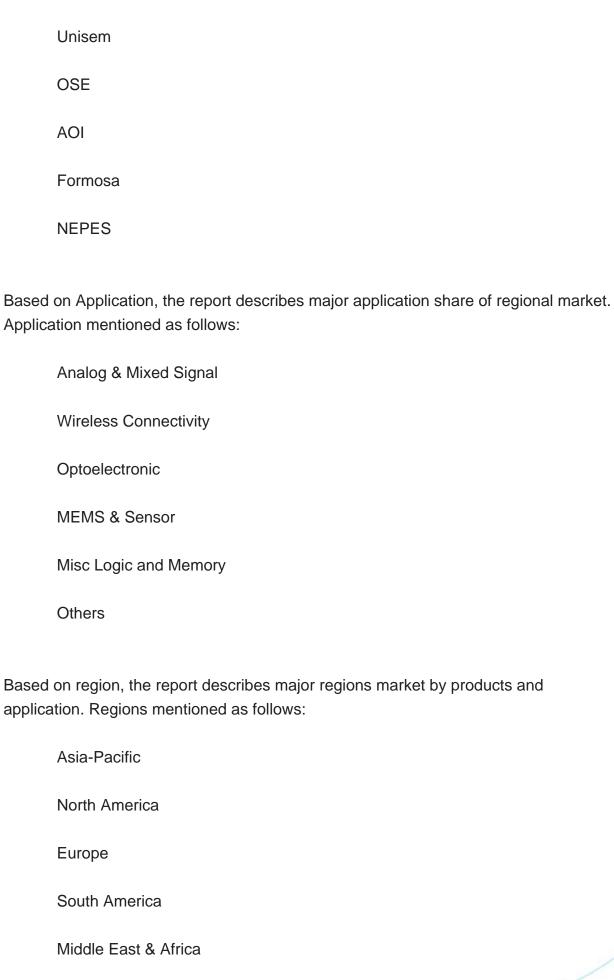


2.5D

Filp Chip
Leading vendors in the market are included based on profile, business performance etc Vendors mentioned as follows:
ASE
Amkor
SPIL
Stats Chippac
PTI
JCET
J-Devices
UTAC
Chipmos
Chipbond
STS
Huatian
NFM
Carsem

Walton











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