

# Global Semiconductor Chip Packaging Test Socket Market 2025 by Manufacturers, Regions, Type and Application, Forecast to 2031

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## Abstracts

According to our (Global Info Research) latest study, the global Semiconductor Chip Packaging Test Socket market size was valued at US\$ 1486 million in 2024 and is forecast to a readjusted size of USD 2364 million by 2031 with a CAGR of 6.9% during review period.

A test socket is a custom-designed electro-mechanical interface that delivers extremely clean electrical signal paths to connect the chip to the ATE.

The global market for semiconductor was estimated at US\$ 579 billion in the year 2022, is projected to US\$ 790 billion by 2029, growing at a CAGR of 6% during the forecast period. Although some major categories are still double-digit year-over-year growth in 2022, led by Analog with 20.76%, Sensor with 16.31%, and Logic with 14.46% growth, Memory declined with 12.64% year over year. The microprocessor (MPU) and microcontroller (MCU) segments will experience stagnant growth due to weak shipments and investment in notebooks, computers, and standard desktops. In the current market scenario, the growing popularity of IoT-based electronics is stimulating the need for powerful processors and controllers. Hybrid MPUs and MCUs provide real-time embedded processing and control for the topmost IoT-based applications, resulting in significant market growth. The Analog IC segment is expected to grow gradually, while demand from the networking and communications industries is limited. Few of the emerging trends in the growing demand for Analog integrated circuits include signal conversion, automotive-specific Analog applications, and power management. They drive the growing demand for discrete power devices.

This report is a detailed and comprehensive analysis for global Semiconductor Chip

Packaging Test Socket market. Both quantitative and qualitative analyses are presented by manufacturers, by region & country, by Type and by Application. As the market is constantly changing, this report explores the competition, supply and demand trends, as well as key factors that contribute to its changing demands across many markets. Company profiles and product examples of selected competitors, along with market share estimates of some of the selected leaders for the year 2025, are provided.

**Key Features:**

Global Semiconductor Chip Packaging Test Socket market size and forecasts, in consumption value (\$ Million), sales quantity (K Units), and average selling prices (US\$/Unit), 2020-2031

Global Semiconductor Chip Packaging Test Socket market size and forecasts by region and country, in consumption value (\$ Million), sales quantity (K Units), and average selling prices (US\$/Unit), 2020-2031

Global Semiconductor Chip Packaging Test Socket market size and forecasts, by Type and by Application, in consumption value (\$ Million), sales quantity (K Units), and average selling prices (US\$/Unit), 2020-2031

Global Semiconductor Chip Packaging Test Socket market shares of main players, shipments in revenue (\$ Million), sales quantity (K Units), and ASP (US\$/Unit), 2020-2025

**The Primary Objectives in This Report Are:**

- To determine the size of the total market opportunity of global and key countries
- To assess the growth potential for Semiconductor Chip Packaging Test Socket
- To forecast future growth in each product and end-use market
- To forecast future factors affecting the marketplace

This report profiles key players in the global Semiconductor Chip Packaging Test Socket market based on the following parameters - company overview, sales quantity, revenue, price, gross margin, product portfolio, geographical presence, and key developments. Key companies covered as a part of this study include Yamaichi Electronics, LEENO, Cohu, ISC, Smiths Interconnect, Enplas, Sensata Technologies, Johnstech, Yokowo, WinWay Technology, etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals.

## **Market Segmentation**

Semiconductor Chip Packaging Test Socket market is split by Type and by Application. For the period 2020-2031, the growth among segments provides accurate calculations and forecasts for consumption value by Type, and by Application in terms of volume and value. This analysis can help you expand your business by targeting qualified niche markets.

### **Market segment by Type**

Burn-in Socket

Test Socket

### **Market segment by Application**

Chip Design Factory

IDM Enterprises

Wafer Foundry

Packaging and Testing Plant

Others

### **Major players covered**

Yamaichi Electronics

LEENO

Cohu

ISC

Smiths Interconnect

Enplas

Sensata Technologies

Johnstech

Yokowo

WinWay Technology

Loranger

Plastronics

OKins Electronics

Qualmax

Ironwood Electronics

3M

M Specialties

Aries Electronics

Emulation Technology

Seiken Co., Ltd.

TESPRO

MJC

Essai (Advantest)

Rika Denshi

Robson Technologies

Test Tooling

Exatron

JF Technology

Gold Technologies

Ardent Concepts

Market segment by region, regional analysis covers  
North America (United States, Canada, and Mexico)  
Europe (Germany, France, United Kingdom, Russia, Italy, and Rest of Europe)  
Asia-Pacific (China, Japan, Korea, India, Southeast Asia, and Australia)  
South America (Brazil, Argentina, Colombia, and Rest of South America)  
Middle East & Africa (Saudi Arabia, UAE, Egypt, South Africa, and Rest of Middle East & Africa)

**The content of the study subjects, includes a total of 15 chapters:**

Chapter 1, to describe Semiconductor Chip Packaging Test Socket product scope, market overview, market estimation caveats and base year.

Chapter 2, to profile the top manufacturers of Semiconductor Chip Packaging Test Socket, with price, sales quantity, revenue, and global market share of Semiconductor Chip Packaging Test Socket from 2020 to 2025.

Chapter 3, the Semiconductor Chip Packaging Test Socket competitive situation, sales quantity, revenue, and global market share of top manufacturers are analyzed emphatically by landscape contrast.

Chapter 4, the Semiconductor Chip Packaging Test Socket breakdown data are shown at the regional level, to show the sales quantity, consumption value, and growth by

regions, from 2020 to 2031.

Chapter 5 and 6, to segment the sales by Type and by Application, with sales market share and growth rate by Type, by Application, from 2020 to 2031.

Chapter 7, 8, 9, 10 and 11, to break the sales data at the country level, with sales quantity, consumption value, and market share for key countries in the world, from 2020 to 2025. and Semiconductor Chip Packaging Test Socket market forecast, by regions, by Type, and by Application, with sales and revenue, from 2026 to 2031.

Chapter 12, market dynamics, drivers, restraints, trends, and Porters Five Forces analysis.

Chapter 13, the key raw materials and key suppliers, and industry chain of Semiconductor Chip Packaging Test Socket.

Chapter 14 and 15, to describe Semiconductor Chip Packaging Test Socket sales channel, distributors, customers, research findings and conclusion.

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