

# Global Polishing N-type Silicon Wafer Market 2025 by Manufacturers, Regions, Type and Application, Forecast to 2031

<https://marketpublishers.com/r/P9D0DA7D8125EN.html>

Date: August 2025

Pages: 125

Price: US\$ 3,480.00 (Single User License)

ID: P9D0DA7D8125EN

## Abstracts

According to our (Global Info Research) latest study, the global Polishing N-type Silicon Wafer market size was valued at US\$ 4278 million in 2024 and is forecast to a readjusted size of USD 6138 million by 2031 with a CAGR of 5.3% during review period.

Polishing N-type Silicon Wafer refers to the process of using mechanical grinding and chemical action to remove surface irregularities, flaws and impurities on N-type silicon wafers, making their surfaces flat, smooth and defect-free. This is a crucial surface treatment process in semiconductor manufacturing, usually carried out after the slicing of single-crystalline silicon rods. It is of great significance for subsequent processes such as lithography and thin film deposition. By precisely controlling parameters like polishing pressure, rotation speed and polishing time, the surface roughness of N-type silicon wafers can be reduced to meet the requirements of nanoscale processing technology, thus providing a high-quality substrate for the fabrication of semiconductor devices.

This report is a detailed and comprehensive analysis for global Polishing N-type Silicon Wafer market. Both quantitative and qualitative analyses are presented by manufacturers, by region & country, by Type and by Application. As the market is constantly changing, this report explores the competition, supply and demand trends, as well as key factors that contribute to its changing demands across many markets. Company profiles and product examples of selected competitors, along with market share estimates of some of the selected leaders for the year 2025, are provided.

Key Features:

Global Polishing N-type Silicon Wafer market size and forecasts, in consumption value (\$ Million), sales quantity (K Units), and average selling prices (US\$/Unit), 2020-2031

Global Polishing N-type Silicon Wafer market size and forecasts by region and country, in consumption value (\$ Million), sales quantity (K Units), and average selling prices (US\$/Unit), 2020-2031

Global Polishing N-type Silicon Wafer market size and forecasts, by Type and by Application, in consumption value (\$ Million), sales quantity (K Units), and average selling prices (US\$/Unit), 2020-2031

Global Polishing N-type Silicon Wafer market shares of main players, shipments in revenue (\$ Million), sales quantity (K Units), and ASP (US\$/Unit), 2020-2025

The Primary Objectives in This Report Are:

To determine the size of the total market opportunity of global and key countries

To assess the growth potential for Polishing N-type Silicon Wafer

To forecast future growth in each product and end-use market

To assess competitive factors affecting the marketplace

This report profiles key players in the global Polishing N-type Silicon Wafer market based on the following parameters - company overview, sales quantity, revenue, price, gross margin, product portfolio, geographical presence, and key developments. Key companies covered as a part of this study include Shin-Etsu Chemical, SUMCO CORPORATION, GlobalWafers, SK Siltron Co., Ltd., Siltronic, Okmetic, Ferrotec, JRH, Wafer Works, Simgui, etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals.

## Market Segmentation

Polishing N-type Silicon Wafer market is split by Type and by Application. For the period 2020-2031, the growth among segments provides accurate calculations and forecasts for consumption value by Type, and by Application in terms of volume and value. This analysis can help you expand your business by targeting qualified niche markets.

## Market segment by Type

12 inch Polished Wafers

8 inch Polished Wafers

6 inch Polished Wafers

#### Market segment by Application

Memory

Logic IC

Semiconductor Discrete Devices

Analog IC

Sensor

Others

#### Major players covered

Shin-Etsu Chemical

SUMCO CORPORATION

GlobalWafers

SK Siltron Co., Ltd.

Siltronic

Okmetic

Ferrotec

JRH

Wafer Works

Simgui

Poshing

GRITEK

Zhonghuan Huanou

MCL

Market segment by region, regional analysis covers  
North America (United States, Canada, and Mexico)  
Europe (Germany, France, United Kingdom, Russia, Italy, and Rest of Europe)  
Asia-Pacific (China, Japan, Korea, India, Southeast Asia, and Australia)  
South America (Brazil, Argentina, Colombia, and Rest of South America)  
Middle East & Africa (Saudi Arabia, UAE, Egypt, South Africa, and Rest of Middle East & Africa)

The content of the study subjects, includes a total of 15 chapters:

Chapter 1, to describe Polishing N-type Silicon Wafer product scope, market overview, market estimation caveats and base year.

Chapter 2, to profile the top manufacturers of Polishing N-type Silicon Wafer, with price, sales quantity, revenue, and global market share of Polishing N-type Silicon Wafer from 2020 to 2025.

Chapter 3, the Polishing N-type Silicon Wafer competitive situation, sales quantity, revenue, and global market share of top manufacturers are analyzed emphatically by landscape contrast.

Chapter 4, the Polishing N-type Silicon Wafer breakdown data are shown at the regional level, to show the sales quantity, consumption value, and growth by regions, from 2020 to 2031.

Chapter 5 and 6, to segment the sales by Type and by Application, with sales market share and growth rate by Type, by Application, from 2020 to 2031.

Chapter 7, 8, 9, 10 and 11, to break the sales data at the country level, with sales quantity, consumption value, and market share for key countries in the world, from 2020 to 2025. and Polishing N-type Silicon Wafer market forecast, by regions, by Type, and by Application, with sales and revenue, from 2026 to 2031.

Chapter 12, market dynamics, drivers, restraints, trends, and Porters Five Forces analysis.

Chapter 13, the key raw materials and key suppliers, and industry chain of Polishing N-type Silicon Wafer.

Chapter 14 and 15, to describe Polishing N-type Silicon Wafer sales channel, distributors, customers, research findings and conclusion.

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