

Global Wired Interface IP Supply, Demand and Key Producers, 2026-2032

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Abstracts

The global Wired Interface IP market size is expected to reach \$ 2733 million by 2032, rising at a market growth of 6.6% CAGR during the forecast period (2026-2032).

Wired Interface IP is a key semiconductor IP category used in chip designs to implement peripheral connectivity, memory access, network communication, display transmission, chip-to-chip interconnects and on-chip or in-package data paths. Its core value lies in packaging complex protocol control logic, PHY analog circuits, high-speed SerDes, clocking and signal-integrity design, power management, protocol compliance and cross-node portability into reusable modules, helping chip designers reduce development complexity, shorten tape-out cycles and improve system interoperability. With rising demand for high-bandwidth, low-latency and low-power connectivity in AI computing, chiplets, automotive electronics, smart devices, industrial control and data-center equipment, Wired Interface IP has become one of the most important foundational IP categories in advanced SoCs and heterogeneous integrated chips.

The Wired Interface IP market is driven by AI computing, data-center bandwidth upgrades, chiplet and advanced packaging architectures, the evolution of intelligent automotive electronics, high-speed consumer connectivity and rising chip design complexity. AI accelerators, CPUs, GPUs, DPUs, switch chips and storage controllers require PCIe, CXL, UCIe, HBM, DDR and high-speed SerDes to enable higher-bandwidth data movement. Chiplet architectures are shifting die-to-die connectivity from proprietary interfaces toward standardized solutions, creating incremental demand for UCIe and die-to-die IP. Automotive electronics, industrial control and smart devices continue to increase demand for MIPI, USB, Ethernet, CAN/LIN and low-power interface IP. At advanced process nodes, PHY design, signal integrity, clock recovery, power optimization and protocol compliance verification are becoming more difficult,

encouraging chip companies to license mature silicon-proven IP to reduce tape-out risk and accelerate time to market.

This report studies the global Wired Interface IP demand, key companies, and key regions.

This report is a detailed and comprehensive analysis of the world market for Wired Interface IP, and provides market size (US\$ million) and Year-over-Year (YoY) growth, considering 2025 as the base year. This report explores demand trends and competition, as well as details the characteristics of Wired Interface IP that contribute to its increasing demand across many markets.

Highlights and key features of the study

Global Wired Interface IP total market, 2021-2032, (USD Million)

Global Wired Interface IP total market by region & country, CAGR, 2021-2032, (USD Million)

U.S. VS China: Wired Interface IP total market, key domestic companies, and share, (USD Million)

Global Wired Interface IP revenue by player, revenue and market share 2021-2026, (USD Million)

Global Wired Interface IP total market by Type, CAGR, 2021-2032, (USD Million)

Global Wired Interface IP total market by Application, CAGR, 2021-2032, (USD Million)

This report profiles major players in the global Wired Interface IP market based on the following parameters - company overview, revenue, gross margin, product portfolio, geographical presence, and key developments. Key companies covered as a part of this study include Synopsys, Cadence Design Systems, Siemens EDA, Qualcomm, Renesas Electronics, Arasan Chip Systems, Comcores ApS, CAST, Inc., T2M IP, Analog Bits, Inc., etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals.

Stakeholders would have ease in decision-making through various strategy matrices used in analyzing the world Wired Interface IP market

Detailed Segmentation:

Each section contains quantitative market data including market by value (US\$

Millions), by player, by regions, by Type, and by Application. Data is given for the years 2021-2032 by year with 2025 as the base year, 2026 as the estimate year, and 2027-2032 as the forecast year.

Global Wired Interface IP Market, By Region:

United States

China

Europe

Japan

South Korea

ASEAN

India

Rest of World

Global Wired Interface IP Market, Segmentation by Type:

PCIe/CXL/UCIe IP

USB/Ethernet/SATA IP

DDR/LPDDR/HBM IP

Others

Global Wired Interface IP Market, Segmentation by Data Rate:

Low and Medium-speed Interface IP

High-speed Interface IP

Global Wired Interface IP Market, Segmentation by Application:

Data Center and AI Computing

Automotive Electronics

Consumer Electronics

Industrial and IoT

Others

Companies Profiled:

Synopsys

Cadence Design Systems

Siemens EDA

Qualcomm

Renesas Electronics

Arasan Chip Systems

Comcores ApS

CAST, Inc.

T2M IP

Analog Bits, Inc.

Silicon Creations

Credo Technology Group Holding Ltd

Mobiveil, Inc.

Kandou

Dolphin Technology

VeriSilicon Microelectronics (Shanghai)

Innosilicon Technology

Brite Semiconductor (Shanghai)

Socionext

Faraday Technology Corporation

M31 Technology Corporation

Rambus Inc.

Key Questions Answered

1. How big is the global Wired Interface IP market?
2. What is the demand of the global Wired Interface IP market?
3. What is the year over year growth of the global Wired Interface IP market?
4. What is the total value of the global Wired Interface IP market?
5. Who are the Major Players in the global Wired Interface IP market?
6. What are the growth factors driving the market demand?

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