

Global UV-Curable Semiconductor Dicing Tapes Supply, Demand and Key Producers, 2026-2032

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Abstracts

The global UV-Curable Semiconductor Dicing Tapes market size is expected to reach \$ 362 million by 2032, rising at a market growth of 7.3% CAGR during the forecast period (2026-2032).

UV-Curable Semiconductor Dicing Tape is a functional pressure-sensitive tape used to secure semiconductor wafers, molded packages and packaging substrates during dicing and singulation. It generally consists of a polyolefin, polyvinyl chloride or polyethylene terephthalate backing film, a UV-reactive acrylic pressure-sensitive adhesive layer and a release liner. Before UV irradiation, the tape provides sufficient adhesion to hold the workpiece firmly on a dicing frame and to reduce die movement, flying chips, edge chipping and cutting-fluid penetration. Following dicing, ultraviolet irradiation crosslinks and cures the adhesive, substantially reducing adhesion and enabling low-stress die pickup and clean release. Major product types include standard, high-tack and antistatic grades, together with heat-resistant, solvent-resistant, transparent and double-sided specialty grades. The product is used in blade dicing, stealth laser dicing, laser full-cut and other precision singulation processes. The scope excludes non-UV dicing tape, wafer backgrinding tape, dicing die attach film, temporary bonding film and standalone die attach film.

In 2025, global production of UV-curable semiconductor dicing tape reached approximately 34 million to 39 million square meters, while mainstream weighted FOB prices ranged from about USD 5.3 to USD 6.3 per square meter. Standard products for conventional silicon-wafer dicing were generally positioned at the lower end of the price range, whereas high-tack, antistatic, low-residue, heat-resistant and solvent-resistant grades designed for ultrathin wafers, small dies, compound semiconductors, backside-metallized wafers and package singulation typically commanded higher unit prices.

Market Opportunities and Core Drivers

Capacity expansion for artificial intelligence servers, high-performance computing, high-bandwidth memory and advanced logic devices is increasing demand for process materials used in high-end wafer dicing, thin-wafer handling and multilayer chip packaging. Global silicon wafer shipments returned to growth in 2025, while semiconductor materials, packaging materials and advanced packaging equipment also recorded solid expansion. As wafers become thinner, die dimensions decrease and package integration increases, dicing tapes must simultaneously deliver high initial adhesion, low residual adhesion after UV exposure, minimal residue, reduced chipping and stable tape expansion. These requirements are shifting the product mix from general-purpose grades toward high-cleanliness, high-uniformity and process-specific solutions.

Industry Chain and Competitive Factors

Upstream materials principally include polyolefin, polyvinyl chloride and polyester backing films, together with acrylic resins, UV-reactive monomers, photoinitiators, antistatic additives and release liners. Midstream production involves adhesive formulation, cleanroom precision coating, curing, lamination, slitting and performance inspection. Downstream customers include wafer manufacturers, integrated device manufacturers, outsourced semiconductor assembly and test companies, and producers of power and optoelectronic devices. The primary barriers to entry are not basic film availability but adhesive formulation expertise, micron-level coating uniformity, control of particles and metallic contamination, lot-to-lot consistency and lengthy customer qualification procedures. Japanese suppliers continue to lead the high-end segment and maintain broad global customer coverage, while manufacturers in China, South Korea and Taiwan are accelerating qualification through proximity to regional wafer fabrication and packaging ecosystems.

Downstream Demand Trends

Advanced packaging and high-bandwidth memory are expected to remain the principal sources of growth for higher-value products. Thin wafers, backside metallization, smaller dies and high-density package structures require improved low-stress pickup, antistatic behavior and contamination control, while also increasing consumption of tapes used for package and substrate singulation. Compound semiconductors such as silicon carbide, gallium nitride and gallium arsenide present an additional opportunity

because their hardness and brittleness require more stable wafer holding, expansion control and chipping suppression. Blade dicing remains the predominant process, but the broader adoption of stealth laser dicing and laser full-cut for ultrathin wafers, compound semiconductors and precision devices will support demand for transparent, highly extensible and laser-compatible UV-curable dicing tapes.

This report studies the global UV-Curable Semiconductor Dicing Tapes production, demand, key manufacturers, and key regions.

This report is a detailed and comprehensive analysis of the world market for UV-Curable Semiconductor Dicing Tapes and provides market size (US\$ million) and Year-over-Year (YoY) Growth, considering 2025 as the base year. This report explores demand trends and competition, as well as details the characteristics of UV-Curable Semiconductor Dicing Tapes that contribute to its increasing demand across many markets.

Highlights and key features of the study

Global UV-Curable Semiconductor Dicing Tapes total production and demand, 2021-2032, (K Sqm)

Global UV-Curable Semiconductor Dicing Tapes total production value, 2021-2032, (USD Million)

Global UV-Curable Semiconductor Dicing Tapes production by region & country, production, value, CAGR, 2021-2032, (USD Million) & (K Sqm), (based on production site)

Global UV-Curable Semiconductor Dicing Tapes consumption by region & country, CAGR, 2021-2032 & (K Sqm)

U.S. VS China: UV-Curable Semiconductor Dicing Tapes domestic production, consumption, key domestic manufacturers and share

Global UV-Curable Semiconductor Dicing Tapes production by manufacturer, production, price, value and market share 2021-2026, (USD Million) & (K Sqm)

Global UV-Curable Semiconductor Dicing Tapes production by Type, production, value, CAGR, 2021-2032, (USD Million) & (K Sqm)

Global UV-Curable Semiconductor Dicing Tapes production by Application, production, value, CAGR, 2021-2032, (USD Million) & (K Sqm)

This report profiles key players in the global UV-Curable Semiconductor Dicing Tapes market based on the following parameters - company overview, production, value, price, gross margin, product portfolio, geographical presence, and key developments. Key

companies covered as a part of this study include LINTEC Corporation, Nitto Denko Corporation, Furukawa Electric Co., Ltd., Denka Company Limited, Sumitomo Bakelite Co., Ltd., Maxell, Ltd., Mitsui Chemicals ICT Materia, Inc., AI Technology, Inc., Ultron Systems, Inc., INNOX Advanced Materials Co., Ltd., etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals.

Stakeholders would have ease in decision-making through various strategy matrices used in analyzing the World UV-Curable Semiconductor Dicing Tapes market

Detailed Segmentation:

Each section contains quantitative market data including market by value (US\$ Millions), volume (production, consumption) & (K Sqm) and average price (US\$/Sq m) by manufacturer, by Type, and by Application. Data is given for the years 2021-2032 by year with 2025 as the base year, 2026 as the estimate year, and 2027-2032 as the forecast year.

Global UV-Curable Semiconductor Dicing Tapes Market, By Region:

United States

China

Europe

Japan

South Korea

ASEAN

India

Rest of World

Global UV-Curable Semiconductor Dicing Tapes Market, Segmentation by Type:

Standard Grade

Antistatic Grade

Low Residue and Low Whisker Grade

Global UV-Curable Semiconductor Dicing Tapes Market, Segmentation by Dicing Method:

Blade Dicing

Laser Dicing

Others

Global UV-Curable Semiconductor Dicing Tapes Market, Segmentation by Workpiece Material:

Silicon

Compound Semiconductor

Mold Resin and Organic Package Substrate

Glass, Ceramic and Sapphire

Global UV-Curable Semiconductor Dicing Tapes Market, Segmentation by Base Film Material:

Polyolefin

Polyvinyl Chloride

Polyethylene Terephthalate

Others

Global UV-Curable Semiconductor Dicing Tapes Market, Segmentation by Application:

Wafer Dicing

Package and Substrate Singulation

Others

Companies Profiled:

LINTEC Corporation

Nitto Denko Corporation

Furukawa Electric Co., Ltd.

Denka Company Limited

Sumitomo Bakelite Co., Ltd.

Maxell, Ltd.

Mitsui Chemicals ICT Materia, Inc.

AI Technology, Inc.

Ultron Systems, Inc.

INNOX Advanced Materials Co., Ltd.

ANYONE Co., Ltd.

SeoKwang Co., Ltd.

MTI Co., Ltd.

Koatech Technology Corporation

Symbio, Inc.

Solar Plus Company

Gillion Application Technology Co., Ltd.

Darbond Technology Co., Ltd.

Cybrid Technologies Inc.

Nantong WINTIME Semiconductor Technology Co., Ltd.

Dongguan Aozon New Material Technology Co., Ltd.

Jiangyin Tongli Optoelectronic Technology Co., Ltd.

Jiangsu Telilan New Material Technology Co., Ltd.

Key Questions Answered:

1. How big is the global UV-Curable Semiconductor Dicing Tapes market?
2. What is the demand of the global UV-Curable Semiconductor Dicing Tapes market?
3. What is the year over year growth of the global UV-Curable Semiconductor Dicing Tapes market?
4. What is the production and production value of the global UV-Curable Semiconductor Dicing Tapes market?
5. Who are the key producers in the global UV-Curable Semiconductor Dicing Tapes market?
6. What are the growth factors driving the market demand?

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