

Global Translational Chip Testing and Sorting Machine Market 2026 by Manufacturers, Regions, Type and Application, Forecast to 2032

<https://marketpublishers.com/r/G83F9D40EAC3EN.html>

Date: June 2026

Pages: 170

Price: US\$ 3,480.00 (Single User License)

ID: G83F9D40EAC3EN

Abstracts

According to our (Global Info Research) latest study, the global Translational Chip Testing and Sorting Machine market size was valued at US\$ 1276 million in 2025 and is forecast to a readjusted size of US\$ 2536 million by 2032 with a CAGR of 10.4% during review period.

Translational chip testing and sorting machine usually refers to an automatic machine with a linear translation conveying structure as the core, which clamps packaged integrated circuits one by one or in rows on a carrier, jig or pallet, and is driven by a servo or linear motor to move along one or more linear tracks, and then goes through loading, preheating or temperature adjustment, electrical performance and functional testing, visual/appearance inspection, and graded unloading. Compared with gravity-type and turret-type sorters, automated test sorting equipment has more stable package attitude control, better adaptability to large-size packages and high-pin-count devices, and is easy to arrange multiple test positions and multiple processes. It is suitable for analog/digital-analog hybrid chips, power management ICs, power devices, small batches of multi-variety chips, and other medium-to-high-speed scenarios that require high test conditions and action flexibility. In 2024, global Translational Chip Testing and Sorting Machine production reached approximately 9,392 units, with an average global market price of around K US\$ 117.64 per unit.

The annual production capacity of translational chip testing and sorting machines is 13,000 units. In terms of gross profit margin, mid-range general-purpose models usually range from 25% to 35%, and high-end platform models can reach 35% to 45%. The upstream mainly includes precision mechanical parts and sheet metal parts, linear motors and servo systems, motion control cards, industrial PCs and PLCs, power

supplies and I/O modules, ATE test machine interface boards and test sockets, vision and sensors, temperature control and constant temperature chambers, pneumatic and vacuum components, as well as supporting processing such as cables, cabinets and spraying. The downstream is connected to IDM and OSAT packaging and testing plants, and is divided into logic IC, analog IC, power devices, memory, Different product lines, such as automotive and industrial chips, are deployed in the mid-test, final test and reliability screening sections, and serve end industries such as consumer electronics, automotive electronics, industrial power supplies, new energy and data centers through these customers. From a cost structure perspective, the material BOM (mechanism, servo and control, electrical, temperature control, fixtures and fixtures, etc.) generally accounts for 55% to 70% of the total machine cost, and assembly and joint debugging, on-site installation acceptance and training account for about 10% to 15%.

On the one hand, translational chip testing and sorting machines are evolving in the direction of 'multi-temperature zones + high parallelism'. More and more models support three-temperature (low temperature/normal temperature/high temperature) or at least high-temperature test chambers, as well as multi-socket parallel testing to meet the requirements for temperature stress and long-term stress screening of automotive-grade and industrial-grade chips. ; On the other hand, as the number of SKUs in packaging and testing plants increases and order batches become smaller, the translational platform is being strengthened as a 'highly flexible Handler' because its tracks and fixtures are easy to change and program switching is convenient, covering a variety of packages and material types through quickly replaceable fixtures and universal carrier designs. At the same time, the equipment is more deeply integrated with ATE testing machines, MES systems, barcodes, and yield analysis platforms, requiring translation sorters to not only complete physical handling and test interfaces, but also undertake functions such as data collection, binning rule management, and traceability coding. Generally speaking, it has given way to turret sorters in the field of ultra-high-speed small packages. However, in the testing of power devices, automotive chips, high-end analog ICs, and multi-variety mixed lines, it continues to increase its volume and upgrade towards mid-to-high-end platforms and intelligence due to its flexible layout, large expansion space, and easy connection to form an entire automated test production line.

This report is a detailed and comprehensive analysis for global Translational Chip Testing and Sorting Machine market. Both quantitative and qualitative analyses are presented by manufacturers, by region & country, by Type and by Application. As the market is constantly changing, this report explores the competition, supply and demand

trends, as well as key factors that contribute to its changing demands across many markets. Company profiles and product examples of selected competitors, along with market share estimates of some of the selected leaders for the year 2025, are provided.

Key Features:

Global Translational Chip Testing and Sorting Machine market size and forecasts, in consumption value (\$ Million), sales quantity (Units), and average selling prices (K US\$/Unit), 2021-2032

Global Translational Chip Testing and Sorting Machine market size and forecasts by region and country, in consumption value (\$ Million), sales quantity (Units), and average selling prices (K US\$/Unit), 2021-2032

Global Translational Chip Testing and Sorting Machine market size and forecasts, by Type and by Application, in consumption value (\$ Million), sales quantity (Units), and average selling prices (K US\$/Unit), 2021-2032

Global Translational Chip Testing and Sorting Machine market shares of main players, shipments in revenue (\$ Million), sales quantity (Units), and ASP (K US\$/Unit), 2021-2026

The Primary Objectives in This Report Are:

To determine the size of the total market opportunity of global and key countries

To assess the growth potential for Translational Chip Testing and Sorting Machine

To forecast future growth in each product and end-use market

To assess competitive factors affecting the marketplace

This report profiles key players in the global Translational Chip Testing and Sorting Machine market based on the following parameters - company overview, sales quantity, revenue, price, gross margin, product portfolio, geographical presence, and key developments. Key companies covered as a part of this study include Cohu, Inc. (Xcerra & MCT), Changchuan Technology, Advantest, Hon Precision, Techwing, Tianjin

JHT Design, Kanematsu (Epson), Boston Semi Equipment, Chroma ATE, EXIS TECH, etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals.

Market Segmentation

Translational Chip Testing and Sorting Machine market is split by Type and by Application. For the period 2021-2032, the growth among segments provides accurate calculations and forecasts for consumption value by Type, and by Application in terms of volume and value. This analysis can help you expand your business by targeting qualified niche markets.

Market segment by Type

Ambient/High-temp Pick and Place Handler

Tri-temp Pick and Place Handler

Market segment by Automation

Fully Automatic Test Handler

Semi-Automatic Test Handler

Market segment by IC Type

Universal Chip Test Handler

Special Chip Test Handler

Market segment by Application

IDMs

OSATs

Major players covered

Cohu, Inc. (Xcerra & MCT)

Changchuan Technology

Advantest

Hon Precision

Techwing

Tianjin JHT Design

Kanematsu (Epson)

Boston Semi Equipment

Chroma ATE

EXIS TECH

SRM Integration

Shanghai Yingshuo

Ueno Seiki

YoungTek Electronics Corp (YTEC)

SYNAX

Pentamaster

ATECO

MIRAE

SEMES

JT Corp

Genesem

Fuzhou Palide

Shanghai Cascol

Shenzhen Biaopu Semiconductor

M?hlbauer

MIT Semiconductor

Y.A.C. MECHATRONICS

HappyJapan

Market segment by region, regional analysis covers

North America (United States, Canada, and Mexico)

Europe (Germany, France, United Kingdom, Russia, Italy, and Rest of Europe)

Asia-Pacific (China, Japan, Korea, India, Southeast Asia, and Australia)

South America (Brazil, Argentina, Colombia, and Rest of South America)

Middle East & Africa (Saudi Arabia, UAE, Egypt, South Africa, and Rest of Middle East & Africa)

The content of the study subjects, includes a total of 15 chapters:

Chapter 1, to describe Translational Chip Testing and Sorting Machine product scope, market overview, market estimation caveats and base year.

Chapter 2, to profile the top manufacturers of Translational Chip Testing and Sorting Machine, with price, sales quantity, revenue, and global market share of Translational Chip Testing and Sorting Machine from 2021 to 2026.

Chapter 3, the Translational Chip Testing and Sorting Machine competitive situation, sales quantity, revenue, and global market share of top manufacturers are analyzed emphatically by landscape contrast.

Chapter 4, the Translational Chip Testing and Sorting Machine breakdown data are shown at the regional level, to show the sales quantity, consumption value, and growth by regions, from 2021 to 2032.

Chapter 5 and 6, to segment the sales by Type and by Application, with sales market share and growth rate by Type, by Application, from 2021 to 2032.

Chapter 7, 8, 9, 10 and 11, to break the sales data at the country level, with sales quantity, consumption value, and market share for key countries in the world, from 2021 to 2026. and Translational Chip Testing and Sorting Machine market forecast, by regions, by Type, and by Application, with sales and revenue, from 2027 to 2032.

Chapter 12, market dynamics, drivers, restraints, trends, and Porters Five Forces analysis.

Chapter 13, the key raw materials and key suppliers, and industry chain of Translational Chip Testing and Sorting Machine.

Chapter 14 and 15, to describe Translational Chip Testing and Sorting Machine sales channel, distributors, customers, research findings and conclusion.

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