

Global System-in-Package (SiP) Die Market 2018 by Manufacturers, Countries, Type and Application, Forecast to 2023

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Abstracts

A system in package (SiP) or system-in-a-package is a number of integrated circuits enclosed in a single chip carrier package....SiP dies can be stacked vertically or tiled horizontally, unlike less dense multi-chip modules, which placedies horizontally on a carrier.

SCOPE OF THE REPORT:

Market demand for high performance, small size, low power and low cost cannot be met through conventional packaging and interconnect technology. Conventional technology is unable to address limitations such as density; bandwidth and signal integrity and thermal management posed by interconnect technology. System in Package (SiP) technology helps to address these limitations effectively to a certain extent.

The global System-in-Package (SiP) Die market is valued at xx million USD in 2017 and is expected to reach xx million USD by the end of 2023, growing at a CAGR of xx% between 2017 and 2023.

The Asia-Pacific will occupy for more market share in following years, especially in China, also fast growing India and Southeast Asia regions.

North America, especially The United States, will still play an important role which cannot be ignored. Any changes from United States might affect the development trend of System-in-Package (SiP) Die.

Europe also play important roles in global market, with market size of xx million USD in 2017 and will be xx million USD in 2023, with a CAGR of xx%.

This report studies the System-in-Package (SiP) Die market status and outlook of Global and major regions, from angles of players, countries, product types and end industries; this report analyzes the top players in global market, and splits the System-in-

Package (SiP) Die market by product type and applications/end industries.
Market Segment by Companies, this report covers

ASE Global(China)

ChipMOS Technologies(China)

Nanium S.A.(Portugal)

Siliconware Precision Industries Co(US)

InsightSiP(France)

Fujitsu(Japan)

Amkor Technology(US)

Freescale Semiconductor(US)

Market Segment by Regions, regional analysis covers

North America (United States, Canada and Mexico)

Europe (Germany, France, UK, Russia and Italy)

Asia-Pacific (China, Japan, Korea, India and Southeast Asia)

South America (Brazil, Argentina, Colombia)

Middle East and Africa (Saudi Arabia, UAE, Egypt, Nigeria and South Africa)

Market Segment by Type, covers

2D IC Packaging

3D IC Packaging

Market Segment by Applications, can be divided into

Consumer Electronics

Automotive

Networking

Medical Electronics

Mobile

Others

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