

Global Sram and Rom Design IP Market 2025 by Company, Regions, Type and Application, Forecast to 2031

<https://marketpublishers.com/r/GFFCF223713FEN.html>

Date: July 2025

Pages: 117

Price: US\$ 3,480.00 (Single User License)

ID: GFFCF223713FEN

Abstracts

According to our (Global Info Research) latest study, the global Sram and Rom Design IP market size was valued at US\$ million in 2024 and is forecast to a readjusted size of USD million by 2031 with a CAGR of %during review period.

SRAM and ROM Design IP refer to intellectual property cores or modules that are pre-designed and ready-to-use in the development of integrated circuits (ICs) or System-on-Chip (SoC) designs. They are essential building blocks used by semiconductor companies to incorporate specific functionalities into their custom-designed chips without having to create these components from scratch.

SRAM and ROM are integral components in System-on-Chips (SoCs) and Application-Specific Integrated Circuits (ASICs). As the demand for these complex integrated circuits grows, so does the demand for reliable and efficient memory IP.

This report is a detailed and comprehensive analysis for global Sram and Rom Design IP market. Both quantitative and qualitative analyses are presented by company, by region & country, by Type and by Application. As the market is constantly changing, this report explores the competition, supply and demand trends, as well as key factors that contribute to its changing demands across many markets. Company profiles and product examples of selected competitors, along with market share estimates of some of the selected leaders for the year 2025, are provided.

Key Features:

Global Sram and Rom Design IP market size and forecasts, in consumption value (\$

Million), 2020-2031

Global Sram and Rom Design IP market size and forecasts by region and country, in consumption value (\$ Million), 2020-2031

Global Sram and Rom Design IP market size and forecasts, by Type and by Application, in consumption value (\$ Million), 2020-2031

Global Sram and Rom Design IP market shares of main players, in revenue (\$ Million), 2020-2025

The Primary Objectives in This Report Are:

- To determine the size of the total market opportunity of global and key countries
- To assess the growth potential for Sram and Rom Design IP
- To forecast future growth in each product and end-use market
- To forecast future factors affecting the marketplace

This report profiles key players in the global Sram and Rom Design IP market based on the following parameters - company overview, revenue, gross margin, product portfolio, geographical presence, and key developments. Key companies covered as a part of this study include Arm, Avalanche Technology, Cadence Design Systems, Dolphin Technology, Infineon Technologies, Integrated Silicon Solution, Microchip Technology, Renesas Electronics, Surecore, Synopsys, etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals.

Market segmentation

Sram and Rom Design IP market is split by Type and by Application. For the period 2020-2031, the growth among segments provides accurate calculations and forecasts for Consumption Value by Type and by Application. This analysis can help you expand your business by targeting qualified niche markets.

Market segment by Type

Single Port

Dual Port

Market segment by Application

Consumer Electronics

Automotive

Medical

Others

Market segment by players, this report covers

Arm

Avalanche Technology

Cadence Design Systems

Dolphin Technology

Infineon Technologies

Integrated Silicon Solution

Microchip Technology

Renesas Electronics

Surecore

Synopsys

VeriSilicon Microelectronics (Shanghai)

Xilinx

Market segment by regions, regional analysis covers
North America (United States, Canada and Mexico)
Europe (Germany, France, UK, Russia, Italy and Rest of Europe)
Asia-Pacific (China, Japan, South Korea, India, Southeast Asia and Rest of Asia-Pacific)
South America (Brazil, Rest of South America)
Middle East & Africa (Turkey, Saudi Arabia, UAE, Rest of Middle East & Africa)

The content of the study subjects, includes a total of 13 chapters:

Chapter 1, to describe Sram and Rom Design IP product scope, market overview, market estimation caveats and base year.

Chapter 2, to profile the top players of Sram and Rom Design IP, with revenue, gross margin, and global market share of Sram and Rom Design IP from 2020 to 2025.

Chapter 3, the Sram and Rom Design IP competitive situation, revenue, and global market share of top players are analyzed emphatically by landscape contrast.

Chapter 4 and 5, to segment the market size by Type and by Application, with consumption value and growth rate by Type, by Application, from 2020 to 2031

Chapter 6, 7, 8, 9, and 10, to break the market size data at the country level, with revenue and market share for key countries in the world, from 2020 to 2025. and Sram and Rom Design IP market forecast, by regions, by Type and by Application, with consumption value, from 2026 to 2031.

Chapter 11, market dynamics, drivers, restraints, trends, Porters Five Forces analysis.

Chapter 12, the key raw materials and key suppliers, and industry chain of Sram and Rom Design IP.

Chapter 13, to describe Sram and Rom Design IP research findings and conclusion.

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