

Global Silicon-on-insulator CMOS Market 2024 by Manufacturers, Regions, Type and Application, Forecast to 2030

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Abstracts

According to our (Global Info Research) latest study, the global Silicon-on-insulator CMOS market size was valued at USD 1127.7 million in 2023 and is forecast to a readjusted size of USD 2884 million by 2030 with a CAGR of 14.4% during review period.

Silicon-On-Insulator (SOI) is a semiconductor fabrication technique developed by IBM that uses pure crystal silicon and silicon oxide for integrated circuits (ICs) and microchips. An SOI microchip processing speed is often 30% faster than today's complementary metal-oxide semiconductor (CMOS)-based chips and power consumption is reduced 80%, which makes them ideal for mobile devices. SOI chips also reduce the soft error rate, which is data corruption caused by cosmic rays and natural radioactive background signals.

Silicon-On-Insulator (SOI) is a new type of starting material for the chip making process. SOI wafers have three layers; a thin surface layer of silicon (from a few hundred Angstrom to several microns thick) where the transistors are formed, an underlying layer of insulating material on a support or 'handle' bulk silicon wafer. The insulating layer, usually made of silicon dioxide, is referred to as the 'buried oxide' or 'BOX', and is usually a few thousand Angstroms thick. Transistors built within the top silicon layer, switch signals faster, run at lower voltages, and are much less vulnerable to signal noise from background cosmic ray particles. Each transistor is isolated from its neighbor by a complete layer of silicon dioxide. These transistors are immune to 'latch-up' problems and can be spaced closer together than transistors built on bulk silicon wafers. Building circuits on SOI increases Fab productivity by allowing for more compact circuit designs, yielding more chips per wafer.

SOI enables increased chip functionality without the cost of major Fab process equipment changes. Faster circuit operation and lower operating voltages have produced a powerful surge in the performance of high-speed network servers and new designs for hand-held computing and communication devices with longer battery life. Advanced circuits, using multiple layers of SOI-type device silicon, can lead the way to a coupling of electrical and optical signal processing into a single chip resulting in a dramatic broadening of communication bandwidth and new applications such as global-ranging, direct-link entertainment and communication to hand-held devices.

Global key players of silicon-on-insulator (SOI) include Soitec, Shin-Etsu Chemical, GlobalWafers, SUMCO, NSIG, etc. Global top five manufacturers hold a share over 97%. Europe is the largest producer of silicon-on-insulator (SOI), holds a share over 68%, followed by Asia-Pacific. In terms of product, below 200 mm is the largest segment, with a share over 50%. And in terms of application, the largest application is consumer electronics, with a share over 59%, followed by automotive.

The Global Info Research report includes an overview of the development of the Silicon-on-insulator CMOS industry chain, the market status of Consumer Electronics (14nm, 7nm), Chemicals (14nm, 7nm), and key enterprises in developed and developing market, and analysed the cutting-edge technology, patent, hot applications and market trends of Silicon-on-insulator CMOS.

Regionally, the report analyzes the Silicon-on-insulator CMOS markets in key regions. North America and Europe are experiencing steady growth, driven by government initiatives and increasing consumer awareness. Asia-Pacific, particularly China, leads the global Silicon-on-insulator CMOS market, with robust domestic demand, supportive policies, and a strong manufacturing base.

Key Features:

The report presents comprehensive understanding of the Silicon-on-insulator CMOS market. It provides a holistic view of the industry, as well as detailed insights into individual components and stakeholders. The report analysis market dynamics, trends, challenges, and opportunities within the Silicon-on-insulator CMOS industry.

The report involves analyzing the market at a macro level:

Market Sizing and Segmentation: Report collect data on the overall market size,

including the sales quantity (K Units), revenue generated, and market share of different by Type (e.g., 14nm, 7nm).

Industry Analysis: Report analyse the broader industry trends, such as government policies and regulations, technological advancements, consumer preferences, and market dynamics. This analysis helps in understanding the key drivers and challenges influencing the Silicon-on-insulator CMOS market.

Regional Analysis: The report involves examining the Silicon-on-insulator CMOS market at a regional or national level. Report analyses regional factors such as government incentives, infrastructure development, economic conditions, and consumer behaviour to identify variations and opportunities within different markets.

Market Projections: Report covers the gathered data and analysis to make future projections and forecasts for the Silicon-on-insulator CMOS market. This may include estimating market growth rates, predicting market demand, and identifying emerging trends.

The report also involves a more granular approach to Silicon-on-insulator CMOS:

Company Analysis: Report covers individual Silicon-on-insulator CMOS manufacturers, suppliers, and other relevant industry players. This analysis includes studying their financial performance, market positioning, product portfolios, partnerships, and strategies.

Consumer Analysis: Report covers data on consumer behaviour, preferences, and attitudes towards Silicon-on-insulator CMOS This may involve surveys, interviews, and analysis of consumer reviews and feedback from different by Application (Consumer Electronics, Chemicals).

Technology Analysis: Report covers specific technologies relevant to Silicon-on-insulator CMOS. It assesses the current state, advancements, and potential future developments in Silicon-on-insulator CMOS areas.

Competitive Landscape: By analyzing individual companies, suppliers, and consumers, the report present insights into the competitive landscape of the Silicon-on-insulator CMOS market. This analysis helps understand market share, competitive advantages, and potential areas for differentiation among industry players.

Market Validation: The report involves validating findings and projections through primary research, such as surveys, interviews, and focus groups.

Market Segmentation

Silicon-on-insulator CMOS market is split by Type and by Application. For the period 2019-2030, the growth among segments provides accurate calculations and forecasts for consumption value by Type, and by Application in terms of volume and value.

Market segment by Type

14nm

7nm

5nm

Others

Market segment by Application

Consumer Electronics

Chemicals

Aerospace & Defense

Oil & Gas

Others

Major players covered

Honeywell

ST Microelectronics

NXP

SOITEC

American Semiconductor

Market segment by region, regional analysis covers

North America (United States, Canada and Mexico)

Europe (Germany, France, United Kingdom, Russia, Italy, and Rest of Europe)

Asia-Pacific (China, Japan, Korea, India, Southeast Asia, and Australia)

South America (Brazil, Argentina, Colombia, and Rest of South America)

Middle East & Africa (Saudi Arabia, UAE, Egypt, South Africa, and Rest of Middle East & Africa)

The content of the study subjects, includes a total of 15 chapters:

Chapter 1, to describe Silicon-on-insulator CMOS product scope, market overview, market estimation caveats and base year.

Chapter 2, to profile the top manufacturers of Silicon-on-insulator CMOS, with price, sales, revenue and global market share of Silicon-on-insulator CMOS from 2019 to 2024.

Chapter 3, the Silicon-on-insulator CMOS competitive situation, sales quantity, revenue and global market share of top manufacturers are analyzed emphatically by landscape contrast.

Chapter 4, the Silicon-on-insulator CMOS breakdown data are shown at the regional level, to show the sales quantity, consumption value and growth by regions, from 2019 to 2030.

Chapter 5 and 6, to segment the sales by Type and application, with sales market share

and growth rate by type, application, from 2019 to 2030.

Chapter 7, 8, 9, 10 and 11, to break the sales data at the country level, with sales quantity, consumption value and market share for key countries in the world, from 2017 to 2023. and Silicon-on-insulator CMOS market forecast, by regions, type and application, with sales and revenue, from 2025 to 2030.

Chapter 12, market dynamics, drivers, restraints, trends and Porters Five Forces analysis.

Chapter 13, the key raw materials and key suppliers, and industry chain of Silicon-on-insulator CMOS.

Chapter 14 and 15, to describe Silicon-on-insulator CMOS sales channel, distributors, customers, research findings and conclusion.

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