Global SiC?Semiconductor Processing Equipment Market 2026 by Manufacturers, Regions, Type and Application, Forecast to 2032

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Abstracts

According to our (Global Info Research) latest study, the global SiC Semiconductor Processing Equipment market size was valued at US$ 3373 million in 2025 and is forecast to a readjusted size of US$ 8075 million by 2032 with a CAGR of 14.0% during review period.

SiC Semiconductor Processing Equipment refers to the core toolsets required across the SiC value chain?SiC substrate manufacturing, SiC epitaxy, and SiC device front-end processing?with the emphasis on tools that directly determine yield, reliability, and cost per cm?. Compared with silicon, SiC processing imposes distinct equipment requirements driven by (i) a hard/brittle wafering and surface-finishing regime, and (ii) a device flow that typically depends on heated ion implantation, very high-temperature activation anneals, and robust plasma etching/ohmic-contact formation, making thermal budgets, chamber materials, particle control, and defect sensitivity materially more demanding.

The market can be framed around 10 core equipment categories: (1) SiC crystal growth (PVT) platforms engineered for long-cycle thermal-field stability (e.g., PVA TePla/CGS highlights PVT systems for SiC single crystals); (2) boule processing & wafering (orientation/end-face, slicing) to reduce kerf loss and increase wafers per boule (e.g., DISCO?s KABRA laser ingot slicing applicable to SiC; Okamoto?s SiC ingot grinding/orientation workflow); (3) wafer surface finishing (grinding/lapping/DSP/CMP) to reach tight TTV/warp and nanometer-scale surfaces (e.g., EBARA CMP systems emphasize nano-level planarity and contamination/particle mitigation concepts); (4) cleaning & surface preparation (single-wafer/batch, megasonic, dry-in/dry-out) to manage particle sensitivity (e.g., SCREEN single-wafer cleaner product line); (5)

metrology & defect inspection for substrates/epi/patterned wafers?closing feedback loops on BPDs/stacking faults and other SiC-relevant defects (e.g., KLA Candela for SiC-oriented inspection and classification); (6) SiC epitaxy (high-temperature CVD reactors) focused on low defectivity, thickness/doping uniformity, and output per m? (e.g., AIXTRON G10-SiC for 150/200 mm; ASM PE2O8 positioned for yield/throughput/cost); (7) lithography & track (often mainstream silicon toolsets, but critical for trench/termination patterning); (8) plasma etch & ash/strip where SiC deep/trench etches demand high-density plasma with low damage and tight profile control (e.g., Oxford Instruments? ICP-RIE tooling and SiC-focused etch content); (9) thin film deposition & metallization (CVD/ALD/PVD) for dielectrics/barriers/metals (Applied Materials summarizes PVD/CVD/ALD as core deposition technologies in semiconductor manufacturing); and (10) doping & thermal processing (implant/activation anneal/oxidation) that defines the SiC ?high-temperature window? (Axcelis highlights high-temperature implant capability and Si/SiC flexibility; Nissin highlights high-temperature SiC implantation; centrotherm positions c.ACTIVATOR for up to 2000?C anneals and c.OXIDATOR for SiC oxidation/anneal up to 1500?C).

The equipment challenge in SiC is inherently cross-step coupled: upstream wafering/surface finishing must minimize damage layers and particles on a hard/brittle material; epitaxy must control defect propagation and doping/thickness uniformity that directly map into device blocking/leakage behavior; and device fabrication typically requires heated implantation followed by extreme-temperature activation anneals, while plasma etches (especially trenches/deep features) must balance rate, selectivity, and low-damage surfaces to avoid reliability penalties (interface traps, leakage, oxide integrity). Meanwhile, SiC?s defect spectrum (e.g., BPDs, stacking faults) drives a stronger pull for inspection + classification + feedback earlier in the flow to reduce downstream scrap of high-value processed wafers.

The current industry posture is moving from ?materials bottleneck? to ?manufacturing system competition,? where tool value is measured by throughput ? yield ? repeatability ? cost-of-ownership rather than single-step capability. A second structural driver is the accelerating transition toward 200 mm SiC manufacturing, which triggers a new capex cycle across epitaxy, thermal, metrology, and cleaning. Public disclosures illustrate this clearly: Infineon explicitly references shifting SiC manufacturing to 200-mm wafers; ST announced a high-volume 200-mm SiC manufacturing facility in Catania; and Wolfspeed continues to position 200-mm materials/device scaling as central to its roadmap. On the equipment side, AIXTRON?s G10-SiC shipment milestone explicitly ties demand to ramping 200-mm SiC epitaxy capacity?highlighting high-throughput epitaxy as a key expansion lever.

Looking forward, equipment growth is driven by (i) electrification and energy-efficiency demand from EV (incl. 800V architectures), renewables/storage, industrial power and grid, sustaining SiC capacity build-outs; (ii) the industry pivot from capacity to cost curve reduction, pushing tool roadmaps toward higher wafer output per m?, longer stable runs, and deeper automation/SPC; and (iii) three practical ?battlefields? for tool innovation: wafering/surface finishing (damage/kerf reduction), epitaxy (defectivity + throughput), and implant/anneal/oxidation/clean (high-temperature capability with ultra-low contamination). Along the chain, upstream consumables and critical parts (hot-zone materials, process gases/chemicals, chamber materials/coatings) increasingly co-evolve with tool design; midstream equipment suppliers differentiate via integrated process windows and service uptime; and downstream SiC substrate/epi/device makers require faster qualification loops?making joint development and platform standardization more common in the 200-mm era.

This report is a detailed and comprehensive analysis for global SiC Semiconductor Processing Equipment market. Both quantitative and qualitative analyses are presented by manufacturers, by region & country, by Equipment Type and by Application. As the market is constantly changing, this report explores the competition, supply and demand trends, as well as key factors that contribute to its changing demands across many markets. Company profiles and product examples of selected competitors, along with market share estimates of some of the selected leaders for the year 2025, are provided.

Key Features:

Global SiC Semiconductor Processing Equipment market size and forecasts, in consumption value ($ Million), sales quantity (Units), and average selling prices (K US$/Unit), 2021-2032

Global SiC Semiconductor Processing Equipment market size and forecasts by region and country, in consumption value ($ Million), sales quantity (Units), and average selling prices (K US$/Unit), 2021-2032

Global SiC Semiconductor Processing Equipment market size and forecasts, by Equipment Type and by Application, in consumption value ($ Million), sales quantity (Units), and average selling prices (K US$/Unit), 2021-2032

Global SiC Semiconductor Processing Equipment market shares of main players, shipments in revenue ($ Million), sales quantity (Units), and ASP (K US$/Unit),

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2021-2026

The Primary Objectives in This Report Are:

To determine the size of the total market opportunity of global and key countries

To assess the growth potential for SiC Semiconductor Processing Equipment

To forecast future growth in each product and end-use market

To assess competitive factors affecting the marketplace

This report profiles key players in the global SiC Semiconductor Processing Equipment market based on the following parameters - company overview, sales quantity, revenue, price, gross margin, product portfolio, geographical presence, and key developments. Key companies covered as a part of this study include Applied Materials, KLA SPTS, Lam, Tel, Axcelis, Centrotherm, ULVAC, PVA TePla, NAURA, Kokusai, etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals.

Market Segmentation

SiC Semiconductor Processing Equipment market is split by Equipment Type and by Application. For the period 2021-2032, the growth among segments provides accurate calculations and forecasts for consumption value by Equipment Type, and by Application in terms of volume and value. This analysis can help you expand your business by targeting qualified niche markets.

Market segment by Equipment Type

SiC Crystal Growth Furnace

SiC Cutting Equipment

SiC Epitaxy/HTCVD Equipment

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SiC Grinding/CMP Equipment

SiC Deposition Equipment

SiC Thermal Processing Equipment

SiC Etch and Clean Equipment

SiC Ion Implant Equipment

SiC Patterning Equipment

SiC Metrology and Inspection Equipment

SiC Wafer Bonders

Others

Market segment by SiC Wafer Size

150mm SiC Equipment

200mm SiC Equipment

Others

Market segment by Device Type

SiC MOSFET

SiC Diode

Others

Market segment by Application

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Silicon Carbide Wafer

Silicon Carbide Epitaxial Wafer

Silicon Carbide Devices

Major players covered

Applied Materials

KLA SPTS

Lam

Tel

Axcelis

Centrotherm

ULVAC

PVA TePla

NAURA

Kokusai

SCREEN

Sumitomo

Nissin Ion

Oxford Instruments

ASML

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Nikon

Canon

AIXTRON

ASM/LPE

Accretech

DISCO

Okamoto

Lasertec

Advantest

Teradyne

Crystal Growth & Energy Equipment Inc.

Han's Laser Technology Industry

Beijing TSD Semiconductor Co., Ltd.

Shenzhen Naso Tech

Beijing U-Precision Tech

AMIES Technology

Gurui Semiconductor Equipment (Guangzhou)

Angkun Vision (Beijing) Technology

Market segment by region, regional analysis covers

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North America (United States, Canada, and Mexico)

Europe (Germany, France, United Kingdom, Russia, Italy, and Rest of Europe)

Asia-Pacific (China, Japan, Korea, India, Southeast Asia, and Australia)

South America (Brazil, Argentina, Colombia, and Rest of South America)

Middle East & Africa (Saudi Arabia, UAE, Egypt, South Africa, and Rest of Middle East & Africa)

The content of the study subjects, includes a total of 15 chapters:

Chapter 1, to describe SiC Semiconductor Processing Equipment product scope, market overview, market estimation caveats and base year.

Chapter 2, to profile the top manufacturers of SiC Semiconductor Processing Equipment, with price, sales quantity, revenue, and global market share of SiC Semiconductor Processing Equipment from 2021 to 2026.

Chapter 3, the SiC Semiconductor Processing Equipment competitive situation, sales quantity, revenue, and global market share of top manufacturers are analyzed emphatically by landscape contrast.

Chapter 4, the SiC Semiconductor Processing Equipment breakdown data are shown at the regional level, to show the sales quantity, consumption value, and growth by regions, from 2021 to 2032.

Chapter 5 and 6, to segment the sales by Equipment Type and by Application, with sales market share and growth rate by Equipment Type, by Application, from 2021 to 2032.

Chapter 7, 8, 9, 10 and 11, to break the sales data at the country level, with sales quantity, consumption value, and market share for key countries in the world, from 2021 to 2026.and SiC Semiconductor Processing Equipment market forecast, by regions, by Equipment Type, and by Application, with sales and revenue, from 2027 to 2032.

Chapter 12, market dynamics, drivers, restraints, trends, and Porters Five Forces

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analysis.

Chapter 13, the key raw materials and key suppliers, and industry chain of SiC Semiconductor Processing Equipment.

Chapter 14 and 15, to describe SiC Semiconductor Processing Equipment sales channel, distributors, customers, research findings and conclusion.

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