

Global SERDES IP Cores Supply, Demand and Key Producers, 2023-2029

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Abstracts

The global SERDES IP Cores market size is expected to reach \$ million by 2029, rising at a market growth of % CAGR during the forecast period (2023-2029).

The Global Mobile Economy Development Report 2023 released by GSMA Intelligence pointed out that by the end of 2022, the number of global mobile users would exceed 5.4 billion. The mobile ecosystem supports 16 million jobs directly and 12 million jobs indirectly.

According to our Communications Research Centre, in 2022, the global communication equipment was valued at US\$ 100 billion. The U.S. and China are powerhouses in the manufacture of communications equipment. According to data from the Ministry of Industry and Information Technology of China, the cumulative revenue of telecommunications services in 2022 was ?1.58 trillion, an increase of 8% over the previous year. The total amount of telecommunications business calculated at the price of the previous year reached ?1.75 trillion, a year-on-year increase of 21.3%. In the same year, the fixed Internet broadband access business revenue was ?240.2 billion, an increase of 7.1% over the previous year, and its proportion in the telecommunications business revenue decreased from 15.3% in the previous year to 15.2%, driving the telecommunications business revenue to increase by 1.1 percentage points.

SERDES IP stands for Serializer/Deserializer Intellectual Property. It refers to a specialized electronic design component or module that facilitates the transmission of serial data over high-speed communication interfaces. SERDES technology is commonly used in various applications, including data communication, networking, telecommunications, and storage systems.

SERDES IP is typically implemented as a digital integrated circuit (IC) and is often provided as intellectual property (IP) by semiconductor companies or IP vendors. It incorporates the necessary circuitry and algorithms to convert parallel data into a serialized format for transmission and then convert the received serialized data back into parallel form for processing.

The primary purpose of SERDES IP is to enable the efficient and reliable transfer of data across high-speed interfaces while minimizing signal degradation and noise interference. It handles tasks such as data serialization, clock recovery, signal conditioning, and error detection and correction.

This report studies the global SERDES IP Cores production, demand, key manufacturers, and key regions.

This report is a detailed and comprehensive analysis of the world market for SERDES IP Cores, and provides market size (US\$ million) and Year-over-Year (YoY) Growth, considering 2022 as the base year. This report explores demand trends and competition, as well as details the characteristics of SERDES IP Cores that contribute to its increasing demand across many markets.

Highlights and key features of the study

Global SERDES IP Cores total production and demand, 2018-2029, (K Units)

Global SERDES IP Cores total production value, 2018-2029, (USD Million)

Global SERDES IP Cores production by region & country, production, value, CAGR, 2018-2029, (USD Million) & (K Units)

Global SERDES IP Cores consumption by region & country, CAGR, 2018-2029 & (K Units)

U.S. VS China: SERDES IP Cores domestic production, consumption, key domestic manufacturers and share

Global SERDES IP Cores production by manufacturer, production, price, value and market share 2018-2023, (USD Million) & (K Units)

Global SERDES IP Cores production by Type, production, value, CAGR, 2018-2029, (USD Million) & (K Units)

Global SERDES IP Cores production by Application production, value, CAGR, 2018-2029, (USD Million) & (K Units).

This reports profiles key players in the global SERDES IP Cores market based on the following parameters – company overview, production, value, price, gross margin, product portfolio, geographical presence, and key developments. Key companies covered as a part of this study include Synopsys, Xilinx, Cadence Design Systems, Rambus, Marvell, Intel, Credo, Lattice Semiconductor and eSilicon, etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals, COVID-19 and Russia-Ukraine War Influence.

Stakeholders would have ease in decision-making through various strategy matrices used in analyzing the World SERDES IP Cores market.

Detailed Segmentation:

Each section contains quantitative market data including market by value (US\$ Millions), volume (production, consumption) & (K Units) and average price (US\$/Unit) by manufacturer, by Type, and by Application. Data is given for the years 2018-2029 by year with 2022 as the base year, 2023 as the estimate year, and 2024-2029 as the forecast year.

Global SERDES IP Cores Market, By Region:

United States

China

Europe

Japan

South Korea

ASEAN

India

Rest of World

Global SERDES IP Cores Market, Segmentation by Type

High-Speed SERDES

Low-Power SERDES

Long-Reach SERDES

Others

Global SERDES IP Cores Market, Segmentation by Application

Networking Equipment

Storage Systems

Video and Audio Interfaces

Others

Companies Profiled:

Synopsys

Xilinx

Cadence Design Systems

Rambus

Marvell

Intel

Credo

Lattice Semiconductor

eSilicon

Texas Instruments

S2C

Peraso

Semtech

Point2 Technology

Microchip Technology

Fermionic Design

Silicon Creations

M31 Technology

Microtronix

Global Unichip Crop

Key Questions Answered

1. How big is the global SERDES IP Cores market?
2. What is the demand of the global SERDES IP Cores market?
3. What is the year over year growth of the global SERDES IP Cores market?

4. What is the production and production value of the global SERDES IP Cores market?
5. Who are the key producers in the global SERDES IP Cores market?
6. What are the growth factors driving the market demand?

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