

Global Semiconductor Test Handler Market 2024 by Manufacturers, Regions, Type and Application, Forecast to 2030

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Abstracts

According to our (Global Info Research) latest study, the global Semiconductor Test Handler market size was valued at USD 1836.7 million in 2023 and is forecast to a readjusted size of USD 4026.6 million by 2030 with a CAGR of 11.9% during review period.

Test Handler in Semiconductors are primarily used to for sorting, testing and transferring chips with the simple electrical interface diagram: ATE ? ITA ? DUT (package) ? Handler. And these machines are capable of processing thousands of chips in an hour. Normally, packaged parts use a handler to place the device on a customized interface board, then IC chip handler transfers finished chips to the inspection equipment for final visual and electrical performance test and then sort the chips into good, bad or other bins based on test results. Note: ATE: Automatic Test Equipment; ITA: Interface Test Adapter; DUT: Device Under Test.

Major semiconductor test handler players in the world include Cohu, Inc. (Xcerra), Advantest, Changchuan Technology, Hon Precision and Techwing, etc. The top five players in the world by revenue account for almost 57 per cent. Asia-pacific is the world's largest market with a market share of about 74 percent, followed by North America and Europe with about 13 percent and 5 percent, respectively. In terms of product type, pick-and-place handlers are the largest segment with a 48% share, and in terms of applications, OSATs has the largest share with about 68%.

The Global Info Research report includes an overview of the development of the Semiconductor Test Handler industry chain, the market status of IDMs (Gravity Handlers, Turret Handlers), OSATs (Gravity Handlers, Turret Handlers), and key

enterprises in developed and developing market, and analysed the cutting-edge technology, patent, hot applications and market trends of Semiconductor Test Handler.

Regionally, the report analyzes the Semiconductor Test Handler markets in key regions. North America and Europe are experiencing steady growth, driven by government initiatives and increasing consumer awareness. Asia-Pacific, particularly China, leads the global Semiconductor Test Handler market, with robust domestic demand, supportive policies, and a strong manufacturing base.

Key Features:

The report presents comprehensive understanding of the Semiconductor Test Handler market. It provides a holistic view of the industry, as well as detailed insights into individual components and stakeholders. The report analysis market dynamics, trends, challenges, and opportunities within the Semiconductor Test Handler industry.

The report involves analyzing the market at a macro level:

Market Sizing and Segmentation: Report collect data on the overall market size, including the sales quantity (Units), revenue generated, and market share of different by Type (e.g., Gravity Handlers, Turret Handlers).

Industry Analysis: Report analyse the broader industry trends, such as government policies and regulations, technological advancements, consumer preferences, and market dynamics. This analysis helps in understanding the key drivers and challenges influencing the Semiconductor Test Handler market.

Regional Analysis: The report involves examining the Semiconductor Test Handler market at a regional or national level. Report analyses regional factors such as government incentives, infrastructure development, economic conditions, and consumer behaviour to identify variations and opportunities within different markets.

Market Projections: Report covers the gathered data and analysis to make future projections and forecasts for the Semiconductor Test Handler market. This may include estimating market growth rates, predicting market demand, and identifying emerging trends.

The report also involves a more granular approach to Semiconductor Test Handler:

Company Analysis: Report covers individual Semiconductor Test Handler manufacturers, suppliers, and other relevant industry players. This analysis includes studying their financial performance, market positioning, product portfolios, partnerships, and strategies.

Consumer Analysis: Report covers data on consumer behaviour, preferences, and attitudes towards Semiconductor Test Handler. This may involve surveys, interviews, and analysis of consumer reviews and feedback from different by Application (IDMs, OSATs).

Technology Analysis: Report covers specific technologies relevant to Semiconductor Test Handler. It assesses the current state, advancements, and potential future developments in Semiconductor Test Handler areas.

Competitive Landscape: By analyzing individual companies, suppliers, and consumers, the report presents insights into the competitive landscape of the Semiconductor Test Handler market. This analysis helps understand market share, competitive advantages, and potential areas for differentiation among industry players.

Market Validation: The report involves validating findings and projections through primary research, such as surveys, interviews, and focus groups.

Market Segmentation

Semiconductor Test Handler market is split by Type and by Application. For the period 2019-2030, the growth among segments provides accurate calculations and forecasts for consumption value by Type, and by Application in terms of volume and value.

Market segment by Type

Gravity Handlers

Turret Handlers

Pick-and-Place Handlers

Market segment by Application

IDMs

OSATs

Major players covered

Cohu, Inc. (Xcerra)

Advantest

Changchuan Technology

Hon Precision

Techwing

ASM Pacific Technology

Kanematsu (Epson)

EXIS TECH

MCT

Boston Semi Equipment

Shenkeda Semiconductor

Chroma ATE

SRM Integration

Tianjin JHT Design

TESEC Corporation

Shanghai Yingshuo

Ueno Seiki

SYNAX

YoungTek Electronics Corp (YTEC)

Shanghai Cascol

Innogrity Pte Ltd

ATECO

Fuzhou Palide

Shenzhen Biaopu Semiconductor

Market segment by region, regional analysis covers

North America (United States, Canada and Mexico)

Europe (Germany, France, United Kingdom, Russia, Italy, and Rest of Europe)

Asia-Pacific (China, Japan, Korea, India, Southeast Asia, and Australia)

South America (Brazil, Argentina, Colombia, and Rest of South America)

Middle East & Africa (Saudi Arabia, UAE, Egypt, South Africa, and Rest of Middle East & Africa)

The content of the study subjects, includes a total of 15 chapters:

Chapter 1, to describe Semiconductor Test Handler product scope, market overview, market estimation caveats and base year.

Chapter 2, to profile the top manufacturers of Semiconductor Test Handler, with price, sales, revenue and global market share of Semiconductor Test Handler from 2019 to 2024.

Chapter 3, the Semiconductor Test Handler competitive situation, sales quantity, revenue and global market share of top manufacturers are analyzed emphatically by landscape contrast.

Chapter 4, the Semiconductor Test Handler breakdown data are shown at the regional level, to show the sales quantity, consumption value and growth by regions, from 2019 to 2030.

Chapter 5 and 6, to segment the sales by Type and application, with sales market share and growth rate by type, application, from 2019 to 2030.

Chapter 7, 8, 9, 10 and 11, to break the sales data at the country level, with sales quantity, consumption value and market share for key countries in the world, from 2017 to 2023. and Semiconductor Test Handler market forecast, by regions, type and application, with sales and revenue, from 2025 to 2030.

Chapter 12, market dynamics, drivers, restraints, trends and Porters Five Forces analysis.

Chapter 13, the key raw materials and key suppliers, and industry chain of Semiconductor Test Handler.

Chapter 14 and 15, to describe Semiconductor Test Handler sales channel, distributors, customers, research findings and conclusion.

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