

Global Semiconductor Physics IP Market 2026 by Company, Regions, Type and Application, Forecast to 2032

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Abstracts

According to our (Global Info Research) latest study, the global Semiconductor Physics IP market size was valued at US\$ 1391 million in 2025 and is forecast to a readjusted size of US\$ 2658 million by 2032 with a CAGR of 9.6% during review period.

Semiconductor Physical IP, also referred to as Semiconductor Foundation IP, refers to licensable silicon-proven building blocks that are tightly coupled with specific semiconductor process nodes and foundry platforms. Typical products include standard cell libraries, embedded memory compilers, I/O and GPIO libraries, ESD protection cells, eFuse, OTP, MTP, embedded NVM, PVT sensors, and selected process-dependent hard macros. These IP blocks provide the physical foundation for SoCs, ASICs, MCUs, AI chips, automotive semiconductors, communication chips, and industrial control ICs, helping design teams optimize area, power, performance, reliability, yield, and tape-out risk. Compared with processor IP or interface protocol IP, physical IP is less application-specific but more fundamental, acting as the underlying design infrastructure that enables a chip to be reliably implemented on a target process technology.

The unique value of semiconductor physical IP lies in its ability to productize, standardize, and silicon-validate the most fundamental and process-dependent building blocks in chip design. For chip design companies, standard cell libraries, memory compilers, I/O cells, ESD protection, eNVM, and PVT sensors may not directly define end-product differentiation, but they determine whether a chip can be reliably implemented, taped out successfully, and optimized for power, performance, area, yield, and reliability. As advanced-node development costs rise, SoC integration becomes more complex, and automotive and industrial chips demand higher long-term reliability,

it is increasingly difficult for design teams to build all foundation libraries and hard macros internally. Proven physical IP has therefore become a critical tool for reducing design risk, shortening development cycles, and improving first-pass silicon success.

The global semiconductor physical IP market is characterized by a structure in which international platform vendors lead, foundry ecosystems play a central role, and regional specialists continue to fill important technology gaps. Companies such as Synopsys, Cadence, and Silvaco hold strong positions through long-standing foundry alignment, silicon-proven assets, and broad IP portfolios. Taiwan-based suppliers such as M31, Faraday, and eMemory benefit from close proximity to foundry and ASIC service ecosystems, with strengths in memory compilers, I/O, and embedded NVM. In Mainland China, companies such as VeriSilicon, Actt are gaining relevance as local chip design, specialty processes, and domestic semiconductor ecosystems expand. Competition is not defined only by the number of IP blocks offered, but also by process coverage, silicon validation history, customer trust, and long-term support capability. Smaller suppliers can still build defensible positions by focusing on low-power memory, eNVM, ESD/I/O, or specialty process libraries.

Looking forward, growth in semiconductor physical IP will be driven by the parallel expansion of advanced nodes and mature specialty processes. AI accelerators, data-center ASICs, automotive SoCs, and high-performance computing platforms will continue to require high-density standard cells, high-performance SRAM, and low-power hard macros at advanced nodes. At the same time, MCUs, mixed-signal ICs, industrial control chips, automotive electronics, and IoT devices will continue to rely heavily on mature and specialty processes, supporting demand for highly reliable, low-leakage, embedded NVM, and automotive-grade physical IP. Compared with processor IP or high-speed interface IP, physical IP is closer to an infrastructure asset within the semiconductor value chain. Its commercial relevance is less dependent on a single end-market cycle and more closely tied to the long-term rise of chip design complexity, process platform diversification, and regional supply-chain localization.

This report is a detailed and comprehensive analysis for global Semiconductor Physics IP market. Both quantitative and qualitative analyses are presented by company, by region & country, by Type and by Application. As the market is constantly changing, this report explores the competition, supply and demand trends, as well as key factors that contribute to its changing demands across many markets. Company profiles and product examples of selected competitors, along with market share estimates of some of the selected leaders for the year 2025, are provided.

Key Features:

Global Semiconductor Physics IP market size and forecasts, in consumption value (\$ Million), 2021-2032

Global Semiconductor Physics IP market size and forecasts by region and country, in consumption value (\$ Million), 2021-2032

Global Semiconductor Physics IP market size and forecasts, by Type and by Application, in consumption value (\$ Million), 2021-2032

Global Semiconductor Physics IP market shares of main players, in revenue (\$ Million), 2021-2026

The Primary Objectives in This Report Are:

- To determine the size of the total market opportunity of global and key countries

- To assess the growth potential for Semiconductor Physics IP

- To forecast future growth in each product and end-use market

- To assess competitive factors affecting the marketplace

This report profiles key players in the global Semiconductor Physics IP market based on the following parameters - company overview, revenue, gross margin, product portfolio, geographical presence, and key developments. Key companies covered as a part of this study include Synopsys, Cadence, Silvaco Group, Inc., Dolphin Technology, Inc., NSCore, Inc., sureCore Ltd., DXCorr Design Inc., Agile Analog Ltd., Sofics BV, M31 Technology, etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals.

Market segmentation

Semiconductor Physics IP market is split by Type and by Application. For the period

2021-2032, the growth among segments provides accurate calculations and forecasts for Consumption Value by Type and by Application. This analysis can help you expand your business by targeting qualified niche markets.

Market segment by Type

SRAM Compiler IP

ROM Compiler IP

Others

Market segment by Delivery Form

Soft IP

Hard IP

Others

Market segment by Licensing Model

Subscription

Buyout

Market segment by Application

IDM

Wafer Foundry

Fabless

OSAT

Market segment by players, this report covers

Synopsys

Cadence

Silvaco Group, Inc.

Dolphin Technology, Inc.

NSCore, Inc.

sureCore Ltd.

DXCorr Design Inc.

Agile Analog Ltd.

Sofics BV

M31 Technology

Faraday

eMemory Technology

VeriSilicon

Chengdu Analog Circuit Technology Inc.

Market segment by regions, regional analysis covers

North America (United States, Canada and Mexico)

Europe (Germany, France, UK, Russia, Italy and Rest of Europe)

Asia-Pacific (China, Japan, South Korea, India, Southeast Asia and Rest of Asia-Pacific)

South America (Brazil, Rest of South America)

Middle East & Africa (Turkey, Saudi Arabia, UAE, Rest of Middle East & Africa)

The content of the study subjects, includes a total of 13 chapters:

Chapter 1, to describe Semiconductor Physics IP product scope, market overview, market estimation caveats and base year.

Chapter 2, to profile the top players of Semiconductor Physics IP, with revenue, gross margin, and global market share of Semiconductor Physics IP from 2021 to 2026.

Chapter 3, the Semiconductor Physics IP competitive situation, revenue, and global market share of top players are analyzed emphatically by landscape contrast.

Chapter 4 and 5, to segment the market size by Type and by Application, with consumption value and growth rate by Type, by Application, from 2021 to 2032.

Chapter 6, 7, 8, 9, and 10, to break the market size data at the country level, with revenue and market share for key countries in the world, from 2021 to 2026. and Semiconductor Physics IP market forecast, by regions, by Type and by Application, with consumption value, from 2027 to 2032.

Chapter 11, market dynamics, drivers, restraints, trends, Porters Five Forces analysis.

Chapter 12, the key raw materials and key suppliers, and industry chain of Semiconductor Physics IP.

Chapter 13, to describe Semiconductor Physics IP research findings and conclusion.

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