

# Global Semiconductor Packaging and Test Equipment Market 2024 by Manufacturers, Regions, Type and Application, Forecast to 2030

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## Abstracts

According to our (Global Info Research) latest study, the global Semiconductor Packaging and Test Equipment market size was valued at USD 11930 million in 2023 and is forecast to a readjusted size of USD 17630 million by 2030 with a CAGR of 5.7% during review period.

The rapid expansion of semiconductor chip application is one of the primary factors contributing to the growth of the semiconductor packaging and test market in China. The use of semiconductor chips has expanded broadly with rising demands from various industries such as power, energy, medical, green cars, networking and telecommunications, LED lighting, automobile, consumer applications, military, aerospace and defense, motor control applications, and robotics.

In China and China Taiwan, Semiconductor Packaging and Test Equipment key players include TEL, DISCO, ASM, etc. China and China Taiwan top three manufacturers hold a share over 40%.

In terms of product, Wafer Probe Station is the largest segment of Cobalt, with a share over 35%. And in terms of application, the largest application is OSAT, followed by IDMs.

The Global Info Research report includes an overview of the development of the Semiconductor Packaging and Test Equipment industry chain, the market status of Integrated Device Manufacturer (IDMs) (Wafer Probe Station, Die Bonder), Outsourced Semiconductor Assembly and Test (OSAT) (Wafer Probe Station, Die Bonder), and key enterprises in developed and developing market, and analysed the cutting-edge

technology, patent, hot applications and market trends of Semiconductor Packaging and Test Equipment.

Regionally, the report analyzes the Semiconductor Packaging and Test Equipment markets in key regions. North America and Europe are experiencing steady growth, driven by government initiatives and increasing consumer awareness. Asia-Pacific, particularly China, leads the global Semiconductor Packaging and Test Equipment market, with robust domestic demand, supportive policies, and a strong manufacturing base.

#### Key Features:

The report presents comprehensive understanding of the Semiconductor Packaging and Test Equipment market. It provides a holistic view of the industry, as well as detailed insights into individual components and stakeholders. The report analysis market dynamics, trends, challenges, and opportunities within the Semiconductor Packaging and Test Equipment industry.

The report involves analyzing the market at a macro level:

**Market Sizing and Segmentation:** Report collect data on the overall market size, including the sales quantity (Units), revenue generated, and market share of different by Type (e.g., Wafer Probe Station, Die Bonder).

**Industry Analysis:** Report analyse the broader industry trends, such as government policies and regulations, technological advancements, consumer preferences, and market dynamics. This analysis helps in understanding the key drivers and challenges influencing the Semiconductor Packaging and Test Equipment market.

**Regional Analysis:** The report involves examining the Semiconductor Packaging and Test Equipment market at a regional or national level. Report analyses regional factors such as government incentives, infrastructure development, economic conditions, and consumer behaviour to identify variations and opportunities within different markets.

**Market Projections:** Report covers the gathered data and analysis to make future projections and forecasts for the Semiconductor Packaging and Test Equipment market. This may include estimating market growth rates, predicting market demand, and identifying emerging trends.

The report also involves a more granular approach to Semiconductor Packaging and Test Equipment:

**Company Analysis:** Report covers individual Semiconductor Packaging and Test Equipment manufacturers, suppliers, and other relevant industry players. This analysis includes studying their financial performance, market positioning, product portfolios, partnerships, and strategies.

**Consumer Analysis:** Report covers data on consumer behaviour, preferences, and attitudes towards Semiconductor Packaging and Test Equipment. This may involve surveys, interviews, and analysis of consumer reviews and feedback from different by Application (Integrated Device Manufacturer (IDMs), Outsourced Semiconductor Assembly and Test (OSAT)).

**Technology Analysis:** Report covers specific technologies relevant to Semiconductor Packaging and Test Equipment. It assesses the current state, advancements, and potential future developments in Semiconductor Packaging and Test Equipment areas.

**Competitive Landscape:** By analyzing individual companies, suppliers, and consumers, the report presents insights into the competitive landscape of the Semiconductor Packaging and Test Equipment market. This analysis helps understand market share, competitive advantages, and potential areas for differentiation among industry players.

**Market Validation:** The report involves validating findings and projections through primary research, such as surveys, interviews, and focus groups.

## Market Segmentation

Semiconductor Packaging and Test Equipment market is split by Type and by Application. For the period 2019-2030, the growth among segments provides accurate calculations and forecasts for consumption value by Type, and by Application in terms of volume and value.

### Market segment by Type

Wafer Probe Station

Die Bonder

Dicing Machine

Test handler

Sorter

#### Market segment by Application

Integrated Device Manufacturer (IDMs)

Outsourced Semiconductor Assembly and Test (OSAT)

#### Major players covered

TEL

DISCO

ASM

Tokyo Seimitsu

Besi

Semes

Cohu, Inc.

Techwing

Kulicke & Soffa Industries

Fasford

Advantest

Hanmi semiconductor

Shinkawa

Shen Zhen Sidea

DIAS Automation

Market segment by region, regional analysis covers

North America (United States, Canada and Mexico)

Europe (Germany, France, United Kingdom, Russia, Italy, and Rest of Europe)

Asia-Pacific (China, Japan, Korea, India, Southeast Asia, and Australia)

South America (Brazil, Argentina, Colombia, and Rest of South America)

Middle East & Africa (Saudi Arabia, UAE, Egypt, South Africa, and Rest of Middle East & Africa)

The content of the study subjects, includes a total of 15 chapters:

Chapter 1, to describe Semiconductor Packaging and Test Equipment product scope, market overview, market estimation caveats and base year.

Chapter 2, to profile the top manufacturers of Semiconductor Packaging and Test Equipment, with price, sales, revenue and global market share of Semiconductor Packaging and Test Equipment from 2019 to 2024.

Chapter 3, the Semiconductor Packaging and Test Equipment competitive situation, sales quantity, revenue and global market share of top manufacturers are analyzed emphatically by landscape contrast.

Chapter 4, the Semiconductor Packaging and Test Equipment breakdown data are shown at the regional level, to show the sales quantity, consumption value and growth by regions, from 2019 to 2030.

Chapter 5 and 6, to segment the sales by Type and application, with sales market share and growth rate by type, application, from 2019 to 2030.

Chapter 7, 8, 9, 10 and 11, to break the sales data at the country level, with sales quantity, consumption value and market share for key countries in the world, from 2017 to 2023. and Semiconductor Packaging and Test Equipment market forecast, by regions, type and application, with sales and revenue, from 2025 to 2030.

Chapter 12, market dynamics, drivers, restraints, trends and Porters Five Forces analysis.

Chapter 13, the key raw materials and key suppliers, and industry chain of Semiconductor Packaging and Test Equipment.

Chapter 14 and 15, to describe Semiconductor Packaging and Test Equipment sales channel, distributors, customers, research findings and conclusion.

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