

# Global Semiconductor Defect Review System Market 2026 by Manufacturers, Regions, Type and Application, Forecast to 2032

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## Abstracts

According to our (Global Info Research) latest study, the global Semiconductor Defect Review System market size was valued at US\$ 994 million in 2025 and is forecast to a readjusted size of US\$ 1541 million by 2032 with a CAGR of 6.5% during review period.

Defect review is a process that uses scanning electron microscopy (SEM) technology to carefully examine defects on semiconductor wafers. First, the defects on the wafer are initially identified by the defect detection system and their location coordinates are recorded in a file. These wafers and the inspection result files are then loaded into the defect review equipment. The review equipment detects and accurately locates defects by comparing with the circuit patterns of adjacent dies and using differential image processing technology. Then, the review equipment automatically moves each defect to the center of the field of view and takes a high-magnification image for further review and classification. This process mainly works in conjunction with the inspection systems of electronic equipment and other semiconductor production lines to ensure that defects are accurately identified and recorded.

Semiconductor defect review systems are high-precision devices used to review and confirm defects on wafers during the semiconductor manufacturing process. These devices are critical in the semiconductor production process because they help identify and classify defects, ensure product quality and improve yield. At present, the main mask inspection/review technologies are optics and SEM. The representative companies of optical defect review are Lasertec and TASMITECH, Inc. The representative companies of DR-SEM are KLA, Applied Materials, Hitachi High-Tech, Holon, and ADVANTEST.

### Market Trend:

1. The investment of various foundries in cutting-edge nodes such as advanced packaging is progressing steadily. Wafer inspection and review equipment needs to adapt to more complex packaging structures and higher precision requirements. This will drive the continuous upgrading of wafer inspection equipment in terms of resolution, inspection speed and data processing capabilities to meet the accurate detection of tiny defects and packaging structure integrity.
2. The investment focus in the memory field, especially DRAM (HBM) and NAND, will promote the technological innovation of wafer inspection and review equipment in memory chip inspection. In view of the special structure and performance requirements of memory chips, the inspection and review equipment need to have higher inspection accuracy and more comprehensive inspection capabilities to ensure the quality and reliability of the chips.

This report is a detailed and comprehensive analysis for global Semiconductor Defect Review System market. Both quantitative and qualitative analyses are presented by manufacturers, by region & country, by Type and by Application. As the market is constantly changing, this report explores the competition, supply and demand trends, as well as key factors that contribute to its changing demands across many markets. Company profiles and product examples of selected competitors, along with market share estimates of some of the selected leaders for the year 2025, are provided.

### Key Features:

Global Semiconductor Defect Review System market size and forecasts, in consumption value (\$ Million), sales quantity (Units), and average selling prices (K US\$/Unit), 2021-2032

Global Semiconductor Defect Review System market size and forecasts by region and country, in consumption value (\$ Million), sales quantity (Units), and average selling prices (K US\$/Unit), 2021-2032

Global Semiconductor Defect Review System market size and forecasts, by Type and by Application, in consumption value (\$ Million), sales quantity (Units), and average selling prices (K US\$/Unit), 2021-2032

Global Semiconductor Defect Review System market shares of main players, shipments

in revenue (\$ Million), sales quantity (Units), and ASP (K US\$/Unit), 2021-2026

**The Primary Objectives in This Report Are:**

To determine the size of the total market opportunity of global and key countries

To assess the growth potential for Semiconductor Defect Review System

To forecast future growth in each product and end-use market

To assess competitive factors affecting the marketplace

This report profiles key players in the global Semiconductor Defect Review System market based on the following parameters - company overview, sales quantity, revenue, price, gross margin, product portfolio, geographical presence, and key developments. Key companies covered as a part of this study include Applied Materials, Lasertec, KLA, Hitachi High-Tech, Advantest, Holon Co., Ltd, Wuhan Jingce Electronic Group, TASMIT, Inc, DJEL, etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals.

**Market Segmentation**

Semiconductor Defect Review System market is split by Type and by Application. For the period 2021-2032, the growth among segments provides accurate calculations and forecasts for consumption value by Type, and by Application in terms of volume and value. This analysis can help you expand your business by targeting qualified niche markets.

**Market segment by Type**

5-7nm Process

10-16nm Process

20-28nm Process

Others

**Market segment by Application**

8 Inch Wafer

12 Inch Wafer

Mask/Reticle

Others

#### Major players covered

Applied Materials

Lasertec

KLA

Hitachi High-Tech

Advantest

Holon Co., Ltd

Wuhan Jingce Electronic Group

TASMIT, Inc

DJEL

Market segment by region, regional analysis covers

North America (United States, Canada, and Mexico)

Europe (Germany, France, United Kingdom, Russia, Italy, and Rest of Europe)

Asia-Pacific (China, Japan, Korea, India, Southeast Asia, and Australia)

South America (Brazil, Argentina, Colombia, and Rest of South America)

Middle East & Africa (Saudi Arabia, UAE, Egypt, South Africa, and Rest of Middle East & Africa)

**The content of the study subjects, includes a total of 15 chapters:**

Chapter 1, to describe Semiconductor Defect Review System product scope, market overview, market estimation caveats and base year.

Chapter 2, to profile the top manufacturers of Semiconductor Defect Review System, with price, sales quantity, revenue, and global market share of Semiconductor Defect Review System from 2021 to 2026.

Chapter 3, the Semiconductor Defect Review System competitive situation, sales quantity, revenue, and global market share of top manufacturers are analyzed emphatically by landscape contrast.

Chapter 4, the Semiconductor Defect Review System breakdown data are shown at the regional level, to show the sales quantity, consumption value, and growth by regions, from 2021 to 2032.

Chapter 5 and 6, to segment the sales by Type and by Application, with sales market share and growth rate by Type, by Application, from 2021 to 2032.

Chapter 7, 8, 9, 10 and 11, to break the sales data at the country level, with sales quantity, consumption value, and market share for key countries in the world, from 2021 to 2026. and Semiconductor Defect Review System market forecast, by regions, by Type, and by Application, with sales and revenue, from 2027 to 2032.

Chapter 12, market dynamics, drivers, restraints, trends, and Porters Five Forces analysis.

Chapter 13, the key raw materials and key suppliers, and industry chain of Semiconductor Defect Review System.

Chapter 14 and 15, to describe Semiconductor Defect Review System sales channel, distributors, customers, research findings and conclusion.

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