

Global (North America, Europe, Asia-Pacific, South America, Middle East and Africa) Semiconductor Automated Test Equipment (ATE) Market 2018, Forecast to 2023

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Abstracts

Semiconductor automated test equipment (ATE) consists of a variety of instruments or cards for testing memory, digital, and mixed-signal at the wafer and Packaged stages, and single-chip system (SoC) components that are also in the wafer and Packaged stages.

Scope of the Report:

This report focuses on the Semiconductor Automated Test Equipment (ATE) in global market, especially in North America, Europe and Asia-Pacific, South America, Middle East and Africa. This report categorizes the market based on manufacturers, regions, type and application.

At present, in the foreign industrial developed countries the semiconductor automated test equipment (ATE) industry is generally at a more advanced level, the world's large enterprises are mainly concentrated in Americas and Japan. Meanwhile, foreign companies have more advanced equipment, strong R & D capability, the technical level is in a leading position.

The market size of semiconductor automatic test equipment is expected to grow due to the growing SoC system and the surge in demand for consumer electronics products. In a rapidly changing market environment, device performance continues to challenge the limits of ATE system functionality. Especially nowadays, the increase of analog and mixed-signal ICs has made ATE equipment originally used for testing pure digital ICs unable to meet new test requirements. The elimination of test equipment has

accelerated and the test cost has increased.

The global semiconductor industry has the characteristics of cyclical development of technology and cyclical fluctuations in the market. Although the global semiconductor industry has gradually entered a stage of mature development in recent years, industry fluctuations have narrowed and cyclicity has become weaker and weaker, but there are still cyclical fluctuations inherent in the industry, with the cyclical changes in the semiconductor industry, corresponding Semiconductor automatic test equipment is affected by its cyclical changes.

The expansion of wireless networks in developing countries and the development of semiconductor manufacturing processes are expected to provide multiple market growth opportunities in the future. These devices offer advantages such as reduced costs, improved operator ergonomics, test repeatability, and improved product quality. In addition, ATE is capable of testing with minimal human intervention and is a viable option for high volume testing. The demand for automatic test equipment is mainly due to increased unit production of semiconductor devices, complexity and performance levels of devices used in multiple electronic products, and the emergence of semiconductor device technologies.

Teradyne, Advantest and LTX-Credence (Xcerra) captured the top three production value share spots in the semiconductor automated test equipment (ATE) market in 2017. Teradyne dominated with 38.38% production value share, followed by Advantest with 33.96% production value share and LTX-Credence (Xcerra) with 8.15% production value share.

As downstream demand grows and technical barriers become less, there will be a huge market space for Semiconductor Automated Test Equipment (ATE). To grab more market, the little companies have to expand the technology, capital investment and brand influence. To meet the challenge of little companies and keep their leading stage, leading companies need to increase the technology innovation and speed up the product upgrading. In the future, Semiconductor Automated Test Equipment (ATE) market will still be a market of fierce competition.

The worldwide market for Semiconductor Automated Test Equipment (ATE) is expected to grow at a CAGR of roughly 3.2% over the next five years, will reach 4210 million US\$ in 2023, from 3480 million US\$ in 2017, according to a new GIR (Global Info Research) study.

Market Segment by Manufacturers, this report covers

Teradyne

Advantest

LTX-Credence

Cohu

Astronics

Chroma

SPEA

Averna

Shibasoku

ChangChuan

Macrotest

Huafeng

Market Segment by Regions, regional analysis covers

North America (United States, Canada and Mexico)

Europe (Germany, France, UK, Russia and Italy)

Asia-Pacific (China, Japan, Korea, India and Southeast Asia)

South America (Brazil, Argentina, Colombia etc.)

Middle East and Africa (Saudi Arabia, UAE, Egypt, Nigeria and South Africa)

Market Segment by Type, covers

Wafer ATE

Packaged Device ATE

Market Segment by Applications, can be divided into

Automotive Electronics

Consumer Electronics

Communications

Computer

Industrial/medical

Military/aviation

There are 15 Chapters to deeply display the global Semiconductor Automated Test Equipment (ATE) market.

Chapter 1, to describe Semiconductor Automated Test Equipment (ATE) Introduction, product scope, market overview, market opportunities, market risk, market driving force;

Chapter 2, to analyze the top manufacturers of Semiconductor Automated Test Equipment (ATE), with sales, revenue, and price of Semiconductor Automated Test Equipment (ATE), in 2016 and 2017;

Chapter 3, to display the competitive situation among the top manufacturers, with sales, revenue and market share in 2016 and 2017;

Chapter 4, to show the global market by regions, with sales, revenue and market share of Semiconductor Automated Test Equipment (ATE), for each region, from 2013 to 2018;

Chapter 5, 6, 7, 8 and 9, to analyze the market by countries, by type, by application and by manufacturers, with sales, revenue and market share by key countries in these regions;

Chapter 10 and 11, to show the market by type and application, with sales market share

and growth rate by type, application, from 2013 to 2018;
Chapter 12, Semiconductor Automated Test Equipment (ATE) market forecast, by regions, type and application, with sales and revenue, from 2018 to 2023;
Chapter 13, 14 and 15, to describe Semiconductor Automated Test Equipment (ATE) sales channel, distributors, traders, dealers, Research Findings and Conclusion, appendix and data source

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