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Global Sample Rate Conversion Supply, Demand and Key Producers, 2026-2032

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Abstracts

The global Sample Rate Conversion market size is expected to reach $ 66.33 million by 2032, rising at a market growth of 3.1% CAGR during the forecast period (2026-2032).

Sample Rate Conversion is a digital signal-processing product category used to convert discrete digital audio streams from one sampling frequency to another while preserving signal integrity across different clock domains. The study focuses on commercially identifiable Sample Rate Conversion products, including standalone SRC/ASRC integrated circuits, licensable FPGA/ASIC IP cores, dedicated DSP/MCU/processor software libraries, and integrated SRC functional blocks where the conversion technology represents a clearly identifiable product capability. Core processing typically combines interpolation, anti-imaging or anti-alias filtering, decimation, fractional-delay processing and sample-rate-ratio estimation. In asynchronous implementations, the converter continuously tracks independent input and output clocks and dynamically adjusts the conversion ratio to compensate for frequency mismatch and clock drift. Key technical parameters include supported input and output sampling rates, conversion-ratio range, PCM word length, channel count, dynamic range, THD+N, stop-band attenuation, group delay, synchronization time, jitter attenuation and digital audio interfaces such as I²S and TDM. The principal commercial applications are professional and broadcast audio, automotive audio, consumer electronics, computing and peripheral devices, industrial and embedded systems, and communications and networked audio.

Key Findings

2025 standalone hardware shipments are estimated at 4.3–5.8 million units under the

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narrow market scope

Standalone SRC/ASRC ICs contribute roughly four-fifths of narrow-scope market revenue in the 2025 model

North America remains the largest supply and commercial center, while Asia-Pacific integration activity continues to deepen

Professional audio and broadcast remain the most stable application base, with automotive and embedded systems offering incremental opportunities

Market Trends

The principal structural trend in Sample Rate Conversion is a migration in implementation rather than a decline in the underlying function. Dedicated merchant ASRC devices remain relevant where deterministic real-time performance, very low distortion, high dynamic range and long design lifecycles are required, particularly in professional and broadcast audio. At the same time, new designs increasingly implement Sample Rate Conversion through FPGA/ASIC IP, DSP software libraries or hardware blocks embedded in larger audio and application processors. Current standalone devices still support high-performance conversion, with Texas Instruments maintaining the SRC4192 as an active product and Analog Devices maintaining the AD1896 in production, while AKM’s AK4137EQ is already in a later product-lifecycle stage. In parallel, newer IP solutions emphasize multi-channel scalability, flexible conversion ratios, low latency and easier integration; CAST’s ASRC core supports FPGA and ASIC implementation, while XMOS continues to maintain software supporting both synchronous and asynchronous SRC. The long-term direction is therefore toward higher channel density, programmable implementation, reduced external component count and tighter integration with the wider digital-audio processing chain.

Market Dynamics

Drivers

Demand for Sample Rate Conversion is primarily driven by the increasing number of systems in which multiple independent digital audio clock domains must coexist. Professional mixers, broadcast systems, automotive infotainment platforms, USB audio

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products, networked audio equipment and embedded processors frequently receive audio streams operating at different nominal sampling frequencies or with independent clock drift. In these applications, ASRC provides a practical method for maintaining continuous audio transmission without forcing all subsystems to share a single physical clock. Higher audio resolution, increasing channel counts and more complex digital signal chains further increase the need for robust clock-domain bridging. The expansion of software-defined audio architectures also supports demand because Sample Rate Conversion can increasingly be deployed as configurable IP or software rather than requiring an additional dedicated component.

Restraints

The principal restraint is the progressive absorption of Sample Rate Conversion into larger SoCs, codecs, DSPs and audio processors. Functional deployment can therefore continue to increase while revenue attributable to standalone Sample Rate Conversion products grows much more slowly. The dedicated IC segment is also mature, with long product lifecycles and relatively limited new part-number introduction compared with broader semiconductor markets. For many cost-sensitive systems, an integrated SRC engine or software implementation can reduce bill-of-material cost, PCB area and clock-management complexity. In addition, customers with fixed and synchronous sampling relationships may use simpler interpolation or decimation solutions, limiting the addressable market for higher-value asynchronous devices. These factors constrain the expansion of standalone merchant hardware even where total SRC usage continues to rise.

Opportunities

The strongest opportunities are emerging in implementations where flexibility, channel scalability and integration efficiency are more important than the use of a dedicated converter IC. FPGA and ASIC IP can address professional audio, automotive, telecommunications and embedded designs requiring multiple asynchronous channels, while software libraries enable processors and DSPs to add SRC functionality without changing hardware architecture. Automotive digital cockpits, increasingly complex in-vehicle audio networks, USB and networked audio, edge devices and multi-source embedded systems create additional demand for real-time clock-domain conversion. There is also an opportunity for domestic semiconductor ecosystems in Asia to localize Sample Rate Conversion through FPGA IP and SoC integration rather than directly replicating the traditional standalone-IC model. Higher channel counts and

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programmable conversion ratios should expand the value of IP-based solutions in systems that require multiple simultaneous audio streams.

Challenges

The central technical challenge is balancing audio quality, latency, processing resources and rapid adaptation to clock variation. High-performance asynchronous conversion requires accurate sample-rate-ratio estimation, stable buffering and filtering, and very low distortion across a wide range of conversion ratios. FPGA and software implementations must additionally control memory usage, DSP utilization, processor load and power consumption. Commercially, the market is relatively narrow, making it difficult for new dedicated-IC entrants to justify development, qualification and long-term product-support costs. Product lifecycle management is also important because professional, industrial and automotive customers often require long availability periods. As integration increases, specialist suppliers must demonstrate sufficient performance, portability and ease of implementation to justify separate IP or software licensing rather than relying on functionality already embedded in a customer’s processor platform.

Industry Chain Analysis

The Sample Rate Conversion industry chain begins with digital signal-processing algorithms, semiconductor design tools, DSP architectures, FPGA platforms, silicon foundry capacity and packaging and testing resources. For standalone SRC/ASRC IC suppliers, product development combines digital filter architecture, clock-domain control, interface design and semiconductor implementation, followed by wafer fabrication, assembly, testing and distribution. For FPGA/ASIC IP and software suppliers, physical manufacturing is largely removed from the direct value chain; value creation is concentrated in resampling algorithms, RTL or software optimization, verification, documentation, portability and technical support. This difference explains why companies with very different business models can participate in the same Sample Rate Conversion market.

The midstream consists of standalone converter IC manufacturers, semiconductor companies providing embedded SRC software, and specialist IP suppliers offering synthesizable or processor-based implementations. Downstream demand comes from professional and broadcast audio, automotive electronics, consumer electronics, computing and peripheral devices, industrial and embedded systems, and

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communications or networked-audio platforms. Value creation is gradually moving upstream from discrete hardware toward reusable algorithmic IP and deeper platform integration. Dedicated ICs retain value where performance predictability and independent implementation are essential, while IP and software gain relevance where customers prioritize channel scalability, BOM reduction and integration flexibility.

Segment Insights

By product type, standalone SRC/ASRC ICs remain the largest revenue component under the narrow market definition and are estimated to represent approximately four-fifths of 2025 market revenue. Their position is supported by long design cycles and established applications in professional, broadcast and high-performance audio. However, the direction of new design activity is increasingly toward FPGA/ASIC IP cores, embedded software libraries and integrated SRC blocks. These categories currently represent a smaller direct commercial value but have greater flexibility because a single implementation can support different processor platforms, channel configurations and product generations.

By conversion mode, asynchronous SRC carries greater technical differentiation because it must bridge independent clock domains and continuously track deviations in the sampling-rate relationship, whereas synchronous SRC is more efficient where the ratio between input and output rates is predetermined. By specification, the market spans products supporting conventional audio rates through 192 kHz and high-performance devices extending above 192 kHz, while channel configurations range from one or two channels in traditional standalone ICs to scalable multi-channel implementations in FPGA and ASIC IP. Higher maximum sample rates do not necessarily correspond to the largest shipment volumes; instead, channel scalability, conversion quality, latency and integration cost increasingly determine product selection.

Downstream Market Opportunities

Professional Audio & Broadcast remains the most stable application market because digital mixers, studio equipment, broadcast infrastructure and professional interfaces must continuously manage heterogeneous sample-rate families and independent clocks. Automotive Audio provides a more integration-driven opportunity as digital cockpits, distributed amplifiers and multiple audio sources increase the number of internal clock domains. Consumer Electronics and Computing & Peripheral Devices continue to support USB audio, high-fidelity playback and digital interface conversion,

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while Industrial & Embedded Systems and Communications & Networking create opportunities for software and IP-based SRC in systems where processor integration and flexible channel configuration are important. The strongest incremental opportunity is therefore not concentrated in a single end market, but in applications combining multiple audio sources, independent clocks and increasing channel density.

Regional Insights

North America represents the largest commercial and supply center for the narrow-scope Sample Rate Conversion market, supported by the concentration of major standalone semiconductor suppliers and specialist DSP/IP companies. Japan retains an important position in high-performance audio semiconductors and premium audio applications, while Europe has a relatively strong presence in embedded software and FPGA/ASIC IP. Mainland China is currently less developed in merchant standalone SRC chips but is building supply capability through domestic FPGA IP, embedded software and SoC integration. Taiwan is more strongly represented through application processors and integrated audio subsystems than through dedicated SRC products.

Regional opportunity is increasingly linked to semiconductor and system integration rather than the establishment of new standalone converter suppliers. Asia-Pacific benefits from a large electronics manufacturing base and expanding domestic FPGA, automotive electronics and application-processor ecosystems, creating additional opportunities for embedded Sample Rate Conversion functionality. North America and Europe are expected to remain important centers for high-performance algorithm development, IP licensing and professional-audio design, while Japan should retain relevance in premium audio components. The regional competitive structure is therefore becoming more differentiated by implementation model rather than by simple production capacity.

Competitive Landscape Analysis

The competitive landscape is concentrated in standalone hardware but considerably more fragmented when IP and software implementations are included. Under the validated narrow-scope framework, the core formal supplier set contains 13 companies. Texas Instruments and Analog Devices form the dominant merchant standalone-hardware tier, while Asahi Kasei retains an established position through AKM’s dedicated audio SRC portfolio; together, the leading standalone suppliers account for

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the majority of attributable hardware value in the model. The broader competitive field includes large semiconductor and IP platforms such as Infineon Technologies, NXP Semiconductors, GlobalFoundries through MIPS, Cadence Design Systems and Microchip Technology, as well as specialized suppliers including GOWIN Semiconductor, XMOS, DSP Concepts, CAST and Intona. Competition is therefore moving away from a simple contest over standalone IC specifications toward a broader comparison of conversion quality, supported sample rates, channel scalability, latency, processor or FPGA resource requirements and integration flexibility. CAST's current ASRC IP, for example, supports both synchronous and asynchronous operation and scalable multi-channel implementations, while Intona offers licensable FPGA-verified Verilog-HDL IP and XMOS provides processor-optimized SSRC/ASRC software.

Report Scope

This report studies the global Sample Rate Conversion production, demand, key manufacturers, and key regions.

This report is a detailed and comprehensive analysis of the world market for Sample Rate Conversion and provides market size (US$ million) and Year-over-Year (YoY) Growth, considering 2025 as the base year. This report explores demand trends and competition, as well as details the characteristics of Sample Rate Conversion that contribute to its increasing demand across many markets.

Highlights and key features of the study

Global Sample Rate Conversion total production and demand, 2021-2032, (Million Units)

Global Sample Rate Conversion total production value, 2021-2032, (USD Million)

Global Sample Rate Conversion production by region & country, production, value, CAGR, 2021-2032, (USD Million) & (Million Units), (based on production site)

Global Sample Rate Conversion consumption by region & country, CAGR, 2021-2032 & (Million Units)

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U.S. VS China: Sample Rate Conversion domestic production, consumption, key domestic manufacturers and share

Global Sample Rate Conversion production by manufacturer, production, price, value and market share 2021-2026, (USD Million) & (Million Units)

Global Sample Rate Conversion production by Type, production, value, CAGR, 2021-2032, (USD Million) & (Million Units)

Global Sample Rate Conversion production by Application, production, value, CAGR, 2021-2032, (USD Million) & (Million Units)

This report profiles key players in the global Sample Rate Conversion market based on the following parameters - company overview, production, value, price, gross margin, product portfolio, geographical presence, and key developments. Key companies covered as a part of this study include Asahi Kasei Corporation, Texas Instruments Incorporated, Infineon Technologies AG, NXP Semiconductors N.V., Analog Devices, Inc., GlobalFoundries Inc., Cadence Design Systems, Inc., Microchip Technology Incorporated, GOWIN Semiconductor Corp., XMOS Ltd., etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals.

Stakeholders would have ease in decision-making through various strategy matrices used in analyzing the World Sample Rate Conversion market

Detailed Segmentation:

Each section contains quantitative market data including market by value (US$ Millions), volume (production, consumption) & (Million Units) and average price (US$/Unit) by manufacturer, by Type, and by Application. Data is given for the years 2021-2032 by year with 2025 as the base year, 2026 as the estimate year, and 2027-2032 as the forecast year.

Global Sample Rate Conversion Market, By Region:

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United States

China

Europe

Japan

South Korea

ASEAN

India

Rest of World

Market Segmentation

Global Sample Rate Conversion Market, Segmentation by Type:

Standalone SRC/ASRC IC

Integrated SRC Block

Other

Global Sample Rate Conversion Market, Segmentation by Conversion Mode:

Asynchronous SRC

Synchronous SRC

Global Sample Rate Conversion Market, Segmentation by Maximum

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Sample Rate:

?96 kHz

>96–192 kHz

>192 kHz

Global Sample Rate Conversion Market, Segmentation by Maximum Channel Count:

1–2 Channels

3–8 Channels

>8 Channels

Global Sample Rate Conversion Market, Segmentation by Application:

Professional Audio & Broadcast

Consumer Electronics

Industrial & Embedded Systems

Communications & Networking

Other Applications

Companies Profiled:

Asahi Kasei Corporation

Texas Instruments Incorporated

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Infineon Technologies AG

NXP Semiconductors N.V.

Analog Devices, Inc.

GlobalFoundries Inc.

Cadence Design Systems, Inc.

Microchip Technology Incorporated

GOWIN Semiconductor Corp.

XMOS Ltd.

DSP Concepts, Inc.

Computer Aided Software Technologies, Inc. (CAST)

Intona Technology GmbH

Key Questions Answered

1. How big is the global Sample Rate Conversion market?
2. What is the demand of the global Sample Rate Conversion market?
3. What is the year over year growth of the global Sample Rate Conversion market?
4. What is the total value of the global Sample Rate Conversion market?
5. Who are the Major Players in the global Sample Rate Conversion market?
6. What are the growth factors driving the market demand?

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