

Global RDMA NIC Supply, Demand and Key Producers, 2026-2032

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Abstracts

The global RDMA NIC market size is expected to reach \$ 8284 million by 2032, rising at a market growth of 6.2% CAGR during the forecast period (2026-2032).

An RDMA NIC is a class of server-grade network interface device designed for data centers, artificial intelligence computing, high-performance computing, cloud infrastructure, and high-throughput storage systems. Its core function is to establish a low-latency, high-bandwidth, and low-CPU-overhead data transfer channel between hosts, allowing application data to bypass the traditional kernel protocol stack and multiple memory copies and to be read from or written directly between local memory and remote memory. These products are typically delivered as PCIe adapters, OCP adapters, mezzanine cards, SmartNICs, DPU cards, or embedded RNIC IP, and commonly support RoCE, RoCEv2, iWARP, InfiniBand, or dual-mode Ethernet and InfiniBand protocols. They improve cluster communication efficiency through queue pairs, memory registration, DMA engines, congestion control, flow control, virtualization offload, storage protocol offload, and security acceleration. Key performance indicators include port speed, port count, PCIe generation, message latency, throughput, queue scale, virtual function count, protocol offload scope, and power consumption. The products are mainly used in large model training and inference clusters, GPU servers, cloud hosts, bare-metal services, hyperconverged infrastructure, NVMe-oF storage, distributed databases, low-latency financial trading, media production, and high-speed data acquisition. Typical customers include cloud service providers, server vendors, AI computing centers, enterprise data centers, research institutions, and storage system integrators. As AI cluster scale expands and east-west traffic grows rapidly, RDMA NICs are evolving from traditional network adapters into core components for connectivity, offload, isolation, programmable processing, and infrastructure acceleration.

The industrial positioning of RDMA NICs is shifting from a server peripheral to a critical performance component of data center infrastructure. Traditional Ethernet communication relies on the host CPU, kernel protocol stack, and multiple memory copies, which can create latency, jitter, and CPU resource bottlenecks in large-scale AI training, distributed storage, cloud virtualization, and high-performance computing workloads. RDMA NICs use hardware queues, DMA transfer, memory registration, congestion control, and protocol offload to enable direct memory access between servers, thereby increasing throughput, reducing latency, and freeing host CPU resources. The value of these products is reflected not only in higher port speeds, but also in the combined improvement of cluster communication efficiency, GPU utilization, storage I/O efficiency, and cloud platform resource density. As port speeds evolve from 25G and 100G toward 200G, 400G, and even 800G, RDMA NICs have become indispensable connectivity and acceleration devices in AI computing clusters, cloud data centers, and next-generation storage networks.

From a product form perspective, RDMA NICs are developing into a supply system that includes traditional RNICs, converged Ethernet adapters, SmartNICs, DPU cards, and embedded RNIC IP. Traditional RNICs mainly provide RoCE, iWARP, or InfiniBand communication capabilities and are suitable for standard servers and high-performance storage systems. SmartNICs add network virtualization, traffic processing, telemetry, and security offload capabilities, reducing the burden of software switching and virtual networking on the host. DPU cards further integrate processor cores, dedicated acceleration engines, and programmable data planes, allowing them to handle infrastructure tasks related to bare metal, containers, virtual machines, storage, security, and operations. Embedded RNIC IP targets FPGAs, self-developed chips, and specialized acquisition systems, providing RoCE or RDMA capabilities for differentiated hardware platforms. The common direction across these product routes is to migrate more communication, storage, and security tasks from the CPU to the NIC side, transforming the network interface from a passive forwarding device into a programmable, offload-capable, and isolated infrastructure processing node.

From a market trend perspective, RDMA NICs benefit from multiple growth drivers, including large AI models, GPU clusters, hyperconverged infrastructure, NVMe-oF storage, and enterprise cloud adoption. AI training places extremely high bandwidth and latency requirements on cross-node parameter synchronization, gradient exchange, and data reading, driving high-end RDMA NICs toward higher speeds and stronger congestion management capabilities. Storage systems use RDMA to reduce read and write path overhead and improve the responsiveness of databases, object storage, distributed file systems, and all-flash arrays. Cloud service providers and large

enterprise data centers focus on multi-tenant isolation, virtualization performance, and infrastructure offload, supporting the penetration of SmartNICs and DPU cards. Future competition will center on high-end SerDes capabilities, protocol stack maturity, server compatibility, GPU and storage ecosystems, power control, programmability, and localized supply capability, while the overall industry remains in a strong growth phase.

This report studies the global RDMA NIC production, demand, key manufacturers, and key regions.

This report is a detailed and comprehensive analysis of the world market for RDMA NIC and provides market size (US\$ million) and Year-over-Year (YoY) Growth, considering 2025 as the base year. This report explores demand trends and competition, as well as details the characteristics of RDMA NIC that contribute to its increasing demand across many markets.

Highlights and key features of the study

Global RDMA NIC total production and demand, 2021-2032, (Million Pcs)

Global RDMA NIC total production value, 2021-2032, (USD Million)

Global RDMA NIC production by region & country, production, value, CAGR, 2021-2032, (USD Million) & (Million Pcs), (based on production site)

Global RDMA NIC consumption by region & country, CAGR, 2021-2032 & (Million Pcs)

U.S. VS China: RDMA NIC domestic production, consumption, key domestic manufacturers and share

Global RDMA NIC production by manufacturer, production, price, value and market share 2021-2026, (USD Million) & (Million Pcs)

Global RDMA NIC production by Port Speed, production, value, CAGR, 2021-2032, (USD Million) & (Million Pcs)

Global RDMA NIC production by Application, production, value, CAGR, 2021-2032, (USD Million) & (Million Pcs)

This report profiles key players in the global RDMA NIC market based on the following parameters - company overview, production, value, price, gross margin, product portfolio, geographical presence, and key developments. Key companies covered as a part of this study include NVIDIA, Broadcom, Intel, Marvell, Chelsio, Cisco, ATTO, Huawei, H3C, YUSUR, etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals.

Stakeholders would have ease in decision-making through various strategy matrices used in analyzing the World RDMA NIC market

Detailed Segmentation:

Each section contains quantitative market data including market by value (US\$ Millions), volume (production, consumption) & (Million Pcs) and average price (US\$/Pcs) by manufacturer, by Port Speed, and by Application. Data is given for the years 2021-2032 by year with 2025 as the base year, 2026 as the estimate year, and 2027-2032 as the forecast year.

Global RDMA NIC Market, By Region:

United States

China

Europe

Japan

South Korea

ASEAN

India

Rest of World

Global RDMA NIC Market, Segmentation by Port Speed:

25G and Below RDMA NIC

40G to 100G RDMA NIC

200G RDMA NIC

400G RDMA NIC

800G RDMA NIC

Others

Global RDMA NIC Market, Segmentation by Card Form Factor:

Standard PCIe Add-in Card

OCP 3.0 NIC

Mezzanine NIC

Onboard NIC

Others

Global RDMA NIC Market, Segmentation by Port Form Factor:

SFP28 Interface RDMA NIC

QSFP28 Interface RDMA NIC

QSFP56 Interface RDMA NIC

OSFP Interface RDMA NIC

RJ45 Interface RDMA NIC

Others

Global RDMA NIC Market, Segmentation by Application:

AI Training Cluster

Cloud Data Center

Hyperconverged Storage

High-Performance Computing

Media Production and High-Speed Acquisition

Financial Low-Latency Network

Others

Companies Profiled:

NVIDIA

Broadcom

Intel

Marvell

Chelsio

Cisco

ATTO

Huawei

H3C

YUSUR

JaguarMicro

LR-LINK

NebulaMatrix

AMD

Key Questions Answered:

1. How big is the global RDMA NIC market?
2. What is the demand of the global RDMA NIC market?
3. What is the year over year growth of the global RDMA NIC market?
4. What is the production and production value of the global RDMA NIC market?
5. Who are the key producers in the global RDMA NIC market?
6. What are the growth factors driving the market demand?

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