

Global RDMA NIC Market 2026 by Manufacturers, Regions, Type and Application, Forecast to 2032

<https://marketpublishers.com/r/G4E9B2F76B04EN.html>

Date: July 2026

Pages: 115

Price: US\$ 3,480.00 (Single User License)

ID: G4E9B2F76B04EN

Abstracts

According to our (Global Info Research) latest study, the global RDMA NIC market size was valued at US\$ 4976 million in 2025 and is forecast to a readjusted size of US\$ 8284 million by 2032 with a CAGR of 6.2% during review period.

An RDMA NIC is a class of server-grade network interface device designed for data centers, artificial intelligence computing, high-performance computing, cloud infrastructure, and high-throughput storage systems. Its core function is to establish a low-latency, high-bandwidth, and low-CPU-overhead data transfer channel between hosts, allowing application data to bypass the traditional kernel protocol stack and multiple memory copies and to be read from or written directly between local memory and remote memory. These products are typically delivered as PCIe adapters, OCP adapters, mezzanine cards, SmartNICs, DPU cards, or embedded RNIC IP, and commonly support RoCE, RoCEv2, iWARP, InfiniBand, or dual-mode Ethernet and InfiniBand protocols. They improve cluster communication efficiency through queue pairs, memory registration, DMA engines, congestion control, flow control, virtualization offload, storage protocol offload, and security acceleration. Key performance indicators include port speed, port count, PCIe generation, message latency, throughput, queue scale, virtual function count, protocol offload scope, and power consumption. The products are mainly used in large model training and inference clusters, GPU servers, cloud hosts, bare-metal services, hyperconverged infrastructure, NVMe-oF storage, distributed databases, low-latency financial trading, media production, and high-speed data acquisition. Typical customers include cloud service providers, server vendors, AI computing centers, enterprise data centers, research institutions, and storage system integrators. As AI cluster scale expands and east-west traffic grows rapidly, RDMA NICs are evolving from traditional network adapters into core components for connectivity, offload, isolation, programmable processing, and infrastructure acceleration.

The industrial positioning of RDMA NICs is shifting from a server peripheral to a critical performance component of data center infrastructure. Traditional Ethernet communication relies on the host CPU, kernel protocol stack, and multiple memory copies, which can create latency, jitter, and CPU resource bottlenecks in large-scale AI training, distributed storage, cloud virtualization, and high-performance computing workloads. RDMA NICs use hardware queues, DMA transfer, memory registration, congestion control, and protocol offload to enable direct memory access between servers, thereby increasing throughput, reducing latency, and freeing host CPU resources. The value of these products is reflected not only in higher port speeds, but also in the combined improvement of cluster communication efficiency, GPU utilization, storage I/O efficiency, and cloud platform resource density. As port speeds evolve from 25G and 100G toward 200G, 400G, and even 800G, RDMA NICs have become indispensable connectivity and acceleration devices in AI computing clusters, cloud data centers, and next-generation storage networks.

From a product form perspective, RDMA NICs are developing into a supply system that includes traditional RNICs, converged Ethernet adapters, SmartNICs, DPU cards, and embedded RNIC IP. Traditional RNICs mainly provide RoCE, iWARP, or InfiniBand communication capabilities and are suitable for standard servers and high-performance storage systems. SmartNICs add network virtualization, traffic processing, telemetry, and security offload capabilities, reducing the burden of software switching and virtual networking on the host. DPU cards further integrate processor cores, dedicated acceleration engines, and programmable data planes, allowing them to handle infrastructure tasks related to bare metal, containers, virtual machines, storage, security, and operations. Embedded RNIC IP targets FPGAs, self-developed chips, and specialized acquisition systems, providing RoCE or RDMA capabilities for differentiated hardware platforms. The common direction across these product routes is to migrate more communication, storage, and security tasks from the CPU to the NIC side, transforming the network interface from a passive forwarding device into a programmable, offload-capable, and isolated infrastructure processing node.

From a market trend perspective, RDMA NICs benefit from multiple growth drivers, including large AI models, GPU clusters, hyperconverged infrastructure, NVMe-oF storage, and enterprise cloud adoption. AI training places extremely high bandwidth and latency requirements on cross-node parameter synchronization, gradient exchange, and data reading, driving high-end RDMA NICs toward higher speeds and stronger congestion management capabilities. Storage systems use RDMA to reduce read and write path overhead and improve the responsiveness of databases, object storage,

distributed file systems, and all-flash arrays. Cloud service providers and large enterprise data centers focus on multi-tenant isolation, virtualization performance, and infrastructure offload, supporting the penetration of SmartNICs and DPU cards. Future competition will center on high-end SerDes capabilities, protocol stack maturity, server compatibility, GPU and storage ecosystems, power control, programmability, and localized supply capability, while the overall industry remains in a strong growth phase.

This report is a detailed and comprehensive analysis for global RDMA NIC market. Both quantitative and qualitative analyses are presented by manufacturers, by region & country, by Port Speed and by Application. As the market is constantly changing, this report explores the competition, supply and demand trends, as well as key factors that contribute to its changing demands across many markets. Company profiles and product examples of selected competitors, along with market share estimates of some of the selected leaders for the year 2025, are provided.

Key Features:

Global RDMA NIC market size and forecasts, in consumption value (\$ Million), sales quantity (Million Pcs), and average selling prices (US\$/Pcs), 2021-2032

Global RDMA NIC market size and forecasts by region and country, in consumption value (\$ Million), sales quantity (Million Pcs), and average selling prices (US\$/Pcs), 2021-2032

Global RDMA NIC market size and forecasts, by Port Speed and by Application, in consumption value (\$ Million), sales quantity (Million Pcs), and average selling prices (US\$/Pcs), 2021-2032

Global RDMA NIC market shares of main players, shipments in revenue (\$ Million), sales quantity (Million Pcs), and ASP (US\$/Pcs), 2021-2026

The Primary Objectives in This Report Are:

To determine the size of the total market opportunity of global and key countries

To assess the growth potential for RDMA NIC

To forecast future growth in each product and end-use market

To assess competitive factors affecting the marketplace

This report profiles key players in the global RDMA NIC market based on the following parameters - company overview, sales quantity, revenue, price, gross margin, product portfolio, geographical presence, and key developments. Key companies covered as a part of this study include NVIDIA, Broadcom, Intel, Marvell, Chelsio, Cisco, ATTO, Huawei, H3C, YUSUR, etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals.

Market Segmentation

RDMA NIC market is split by Port Speed and by Application. For the period 2021-2032, the growth among segments provides accurate calculations and forecasts for consumption value by Port Speed, and by Application in terms of volume and value. This analysis can help you expand your business by targeting qualified niche markets.

Market segment by Port Speed

25G and Below RDMA NIC

40G to 100G RDMA NIC

200G RDMA NIC

400G RDMA NIC

800G RDMA NIC

Others

Market segment by Card Form Factor

Standard PCIe Add-in Card

OCP 3.0 NIC

Mezzanine NIC

Onboard NIC

Others

Market segment by Port Form Factor

SFP28 Interface RDMA NIC

QSFP28 Interface RDMA NIC

QSFP56 Interface RDMA NIC

OSFP Interface RDMA NIC

RJ45 Interface RDMA NIC

Others

Market segment by Application

AI Training Cluster

Cloud Data Center

Hyperconverged Storage

High-Performance Computing

Media Production and High-Speed Acquisition

Financial Low-Latency Network

Others

Major players covered

NVIDIA

Broadcom

Intel

Marvell

Chelsio

Cisco

ATTO

Huawei

H3C

YUSUR

JaguarMicro

LR-LINK

NebulaMatrix

AMD

Market segment by region, regional analysis covers

North America (United States, Canada, and Mexico)

Europe (Germany, France, United Kingdom, Russia, Italy, and Rest of Europe)

Asia-Pacific (China, Japan, Korea, India, Southeast Asia, and Australia)

South America (Brazil, Argentina, Colombia, and Rest of South America)

Middle East & Africa (Saudi Arabia, UAE, Egypt, South Africa, and Rest of Middle East & Africa)

The content of the study subjects, includes a total of 15 chapters:

Chapter 1, to describe RDMA NIC product scope, market overview, market estimation caveats and base year.

Chapter 2, to profile the top manufacturers of RDMA NIC, with price, sales quantity, revenue, and global market share of RDMA NIC from 2021 to 2026.

Chapter 3, the RDMA NIC competitive situation, sales quantity, revenue, and global market share of top manufacturers are analyzed emphatically by landscape contrast.

Chapter 4, the RDMA NIC breakdown data are shown at the regional level, to show the sales quantity, consumption value, and growth by regions, from 2021 to 2032.

Chapter 5 and 6, to segment the sales by Port Speed and by Application, with sales market share and growth rate by Port Speed, by Application, from 2021 to 2032.

Chapter 7, 8, 9, 10 and 11, to break the sales data at the country level, with sales quantity, consumption value, and market share for key countries in the world, from 2021 to 2026. and RDMA NIC market forecast, by regions, by Port Speed, and by Application, with sales and revenue, from 2027 to 2032.

Chapter 12, market dynamics, drivers, restraints, trends, and Porters Five Forces analysis.

Chapter 13, the key raw materials and key suppliers, and industry chain of RDMA NIC.

Chapter 14 and 15, to describe RDMA NIC sales channel, distributors, customers, research findings and conclusion.

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