

Global PCIe Retimers Market 2024 by Manufacturers, Regions, Type and Application, Forecast to 2030

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Abstracts

According to our (Global Info Research) latest study, the global PCIe Retimers market size was valued at USD 82 million in 2023 and is forecast to a readjusted size of USD 795.8 million by 2030 with a CAGR of 38.3% during review period.

A retimer is a digital and analog device. It receives the signal and extracts the digital part of it, then it regenerates it as a separately trained link, in both directions. Therefore, the noise (and other imperfections such as jitter), that was originally present, will be eliminated, it is like a fresh start from the re-generated signal.

A PCIe Retimer is usually implemented as an integrated circuit (IC) chip that can be used, when placed on a PCB, to extend the length of a PCIe bus. It is particularly used it has to pass through a connector to a cable or to another PCB and then to another PCB (i.e. mid-plane or back-plane layouts). The discontinuities caused by the interconnect, PCB/cable changes, etc. produce reflections and increase inter-symbol-interference. These signal challenges will cause the PCIe signal to be too poor at the end point to be received without errors (or a high risk of errors), without some active circuitry to work past those discontinuities. That active circuitry is the Retimer. It takes as inputs a PCIe signal and outputs a re-generated signal as if it were a fresh PCIe device, in both directions.

The Asia-Pacific region is the largest consumer region, with a market share of nearly 37%. North America is the second largest consumer region after the Asia-Pacific region.

The market is relatively competitive. Astera Labs, Renesas Electronics, Parade Technologies, Texas Instrument, Microchip Technology, Montage Technology, among others, are the leaders in the sector, with the top six companies owning key

technologies and patents with the highest market share at 92 percent.

The Global Info Research report includes an overview of the development of the PCIe Retimers industry chain, the market status of Servers (PCIe 3.0, PCIe 4.0), Storage Applications (PCIe 3.0, PCIe 4.0), and key enterprises in developed and developing market, and analysed the cutting-edge technology, patent, hot applications and market trends of PCIe Retimers.

Regionally, the report analyzes the PCIe Retimers markets in key regions. North America and Europe are experiencing steady growth, driven by government initiatives and increasing consumer awareness. Asia-Pacific, particularly China, leads the global PCIe Retimers market, with robust domestic demand, supportive policies, and a strong manufacturing base.

Key Features:

The report presents comprehensive understanding of the PCIe Retimers market. It provides a holistic view of the industry, as well as detailed insights into individual components and stakeholders. The report analysis market dynamics, trends, challenges, and opportunities within the PCIe Retimers industry.

The report involves analyzing the market at a macro level:

Market Sizing and Segmentation: Report collect data on the overall market size, including the sales quantity (K Units), revenue generated, and market share of different by Type (e.g., PCIe 3.0, PCIe 4.0).

Industry Analysis: Report analyse the broader industry trends, such as government policies and regulations, technological advancements, consumer preferences, and market dynamics. This analysis helps in understanding the key drivers and challenges influencing the PCIe Retimers market.

Regional Analysis: The report involves examining the PCIe Retimers market at a regional or national level. Report analyses regional factors such as government incentives, infrastructure development, economic conditions, and consumer behaviour to identify variations and opportunities within different markets.

Market Projections: Report covers the gathered data and analysis to make future projections and forecasts for the PCIe Retimers market. This may include estimating

market growth rates, predicting market demand, and identifying emerging trends.

The report also involves a more granular approach to PCIe Retimers:

Company Analysis: Report covers individual PCIe Retimers manufacturers, suppliers, and other relevant industry players. This analysis includes studying their financial performance, market positioning, product portfolios, partnerships, and strategies.

Consumer Analysis: Report covers data on consumer behaviour, preferences, and attitudes towards PCIe Retimers. This may involve surveys, interviews, and analysis of consumer reviews and feedback from different by Application (Servers, Storage Applications).

Technology Analysis: Report covers specific technologies relevant to PCIe Retimers. It assesses the current state, advancements, and potential future developments in PCIe Retimers areas.

Competitive Landscape: By analyzing individual companies, suppliers, and consumers, the report presents insights into the competitive landscape of the PCIe Retimers market. This analysis helps understand market share, competitive advantages, and potential areas for differentiation among industry players.

Market Validation: The report involves validating findings and projections through primary research, such as surveys, interviews, and focus groups.

Market Segmentation

PCIe Retimers market is split by Type and by Application. For the period 2019-2030, the growth among segments provides accurate calculations and forecasts for consumption value by Type, and by Application in terms of volume and value.

Market segment by Type

PCIe 3.0

PCIe 4.0

PCIe 5.0 and Others

Market segment by Application

Servers

Storage Applications

Others

Major players covered

Astera Labs

Renesas Electronics

Parade Technologies

Texas Instrument

Microchip Technology

Montage Technology

Market segment by region, regional analysis covers

North America (United States, Canada and Mexico)

Europe (Germany, France, United Kingdom, Russia, Italy, and Rest of Europe)

Asia-Pacific (China, Japan, Korea, India, Southeast Asia, and Australia)

South America (Brazil, Argentina, Colombia, and Rest of South America)

Middle East & Africa (Saudi Arabia, UAE, Egypt, South Africa, and Rest of Middle East & Africa)

The content of the study subjects, includes a total of 15 chapters:

Chapter 1, to describe PCIe Retimers product scope, market overview, market estimation caveats and base year.

Chapter 2, to profile the top manufacturers of PCIe Retimers, with price, sales, revenue and global market share of PCIe Retimers from 2019 to 2024.

Chapter 3, the PCIe Retimers competitive situation, sales quantity, revenue and global market share of top manufacturers are analyzed emphatically by landscape contrast.

Chapter 4, the PCIe Retimers breakdown data are shown at the regional level, to show the sales quantity, consumption value and growth by regions, from 2019 to 2030.

Chapter 5 and 6, to segment the sales by Type and application, with sales market share and growth rate by type, application, from 2019 to 2030.

Chapter 7, 8, 9, 10 and 11, to break the sales data at the country level, with sales quantity, consumption value and market share for key countries in the world, from 2017 to 2023. and PCIe Retimers market forecast, by regions, type and application, with sales and revenue, from 2025 to 2030.

Chapter 12, market dynamics, drivers, restraints, trends and Porters Five Forces analysis.

Chapter 13, the key raw materials and key suppliers, and industry chain of PCIe Retimers.

Chapter 14 and 15, to describe PCIe Retimers sales channel, distributors, customers, research findings and conclusion.

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