

Global (North America, Europe, Asia-Pacific, South America, Middle East and Africa) Semiconductor Chip Handler Market 2018, Forecast to 2023

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Abstracts

Semiconductor Chip Handlers are primarily used to for sorting, testing and transferring chips with the simple electrical interface diagram: ATE ? ITA ? DUT (package) ? Handler. And these machines are capable of processing thousands of chips in an hour. Normally, packaged parts use a handler to place the device on a customized interface board, then IC chip handler transfers finished chips to the inspection equipment for final visual and electrical performance test and then sort the chips into good, bad or other bins based on test results.

SCOPE OF THE REPORT:

This report focuses on the Semiconductor Chip Handler in global market, especially in North America, Europe and Asia-Pacific, South America, Middle East and Africa. This report categorizes the market based on manufacturers, regions, type and application. Global semiconductor chip handler market will grow at a modest CAGR of just over 4% by the end of the forecast period. One of the primary drivers of this market is the increased demand for compact electronic devices. This trend is evident in sectors such as mobile phones, automotive, healthcare equipment, and even industrial manufacturing. This has led to semiconductor IC manufacturers investing in R&D to work towards reducing the size of ICs along with improved performance. Such R&D activities have resulted in the emergence of micro-electrical-mechanical systems (MEMs) and 3D stacked ICs. Consequently, growth in the MEMs market will propel demand for testing equipment for the complex ICs until the end of the forecast period. An important trend which is spurring market growth is the emergence of strip testing. Integrated device manufacturers and subcontractors use test-in-strip technology to cut down on costs. This technology helps testers to handle multiple small ICs at the same

time, test over 100 in parallel while reducing bottlenecks. Unlike gravity, pick-and-place, and turret type handlers, strip test handlers can handle IC chips with 0.3mm x 0.6mm dimensions and can test more than 100 devices in parallel without jams. The OSAT segment is the largest of the chip handler market and is expected to garner close to 63% of market share by the end of the forecast period. The growth of the semiconductor industry and rising costs of packaging and testing equipment will create a high demand for automated test equipment along with chip handlers from these OSATs. They are also investing in precision and high-speed IC test handlers so that they can speed up production and deliver high-performance products to market faster.

The worldwide market for Semiconductor Chip Handler is expected to grow at a CAGR of roughly 4.8% over the next five years, will reach 680 million US\$ in 2023, from 510 million US\$ in 2017, according to a new GIR (Global Info Research) study.

Market Segment by Manufacturers, this report covers

Advantest

Cohu

Multitest

Boston Semi Equipment

Seiko Epson Corporation

ASM Pacific Technology

Hon Technologies

Chroma

SRM Integration

MCT

CST

TESEC Corporation

SYNAX

ChangChuan Technology

Market Segment by Regions, regional analysis covers

North America (United States, Canada and Mexico)

Europe (Germany, France, UK, Russia and Italy)

Asia-Pacific (China, Japan, Korea, India and Southeast Asia)

South America (Brazil, Argentina, Colombia etc.)

Middle East and Africa (Saudi Arabia, UAE, Egypt, Nigeria and South Africa)

Market Segment by Type, covers

Logic

Memory

Market Segment by Applications, can be divided into

Outsourced Semiconductor Assembly and Test Providers (OSATs)

Integrated Device Manufacturers (IDMs)

There are 15 Chapters to deeply display the global Semiconductor Chip Handler market.

Chapter 1, to describe Semiconductor Chip Handler Introduction, product scope, market overview, market opportunities, market risk, market driving force;

Chapter 2, to analyze the top manufacturers of Semiconductor Chip Handler, with sales, revenue, and price of Semiconductor Chip Handler, in 2016 and 2017;

Chapter 3, to display the competitive situation among the top manufacturers, with sales, revenue and market share in 2016 and 2017;

Chapter 4, to show the global market by regions, with sales, revenue and market share of Semiconductor Chip Handler, for each region, from 2013 to 2018;

Chapter 5, 6, 7, 8 and 9, to analyze the market by countries, by type, by application and by manufacturers, with sales, revenue and market share by key countries in these regions;

Chapter 10 and 11, to show the market by type and application, with sales market share and growth rate by type, application, from 2013 to 2018;

Chapter 12, Semiconductor Chip Handler market forecast, by regions, type and application, with sales and revenue, from 2018 to 2023;

Chapter 13, 14 and 15, to describe Semiconductor Chip Handler sales channel, distributors, traders, dealers, Research Findings and Conclusion, appendix and data source

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