

Global Near-Memory AI Accelerator Chip Supply, Demand and Key Producers, 2026-2032

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Abstracts

The global Near-Memory AI Accelerator Chip market size is expected to reach \$ 1052 million by 2032, rising at a market growth of 15.0% CAGR during the forecast period (2026-2032).

A near-memory AI accelerator chip is a dedicated processor or acceleration hardware category designed around the data-movement bottleneck in artificial intelligence workloads. Its core positioning is not merely to increase the number of arithmetic units, but to place compute capability inside memory arrays, near memory chips, within memory buffer chips, or alongside high-bandwidth memory channels, so that neural network weights, activations, and retrieval vectors can complete reading, multiply-accumulate, reduction, search, and inference operations over shorter data paths. These products typically adopt digital in-memory computing, analog in-memory computing, processing-in-memory, near-data dataflow, large on-chip SRAM, RRAM arrays, HBM-PIM, GDDR-AiM, or associative processing array architectures. Their main purpose is to improve energy efficiency, latency, and throughput for large-model inference, recommendation systems, edge vision, low-power sensing, vector search, and memory-intensive AI workloads. Common delivery forms include standalone chips, AI accelerator cards, in-memory computing SoCs, PIM memory modules, server inference platforms, and customer-specific ASICs. Major customers include cloud computing providers, server vendors, edge device manufacturers, industrial vision system providers, automotive electronics companies, wearable device makers, and high-performance search system integrators.

The core industrial logic of near-memory AI accelerator chips is that artificial intelligence computing is shifting from a pure pursuit of peak compute performance to a combined pursuit of data access efficiency, system-level energy efficiency, and inference cost per

unit. Traditional processor architectures frequently move model weights, activations, and intermediate results between external memory and compute cores. As large-model inference, recommendation systems, vector search, and multi-channel video analytics become mainstream workloads, the power consumption, latency, and bandwidth occupation caused by data movement are becoming system-level bottlenecks. Near-memory computing, in-memory computing, and processing-in-memory approaches shorten data paths by placing part of matrix multiplication, reduction, search, or preprocessing tasks inside memory arrays or near memory channels, thereby improving overall AI system efficiency.

From a technology-route perspective, near-memory AI accelerator chips are not a single product form, but an industrial cluster formed by multiple architectural routes. Digital in-memory computing typically relies on SRAM and standard CMOS processes, emphasizing manufacturability, stable accuracy, and software toolchain compatibility, making it suitable for edge vision, industrial inspection, and multi-channel video analytics. Analog in-memory computing usually uses flash memory, RRAM, or other memory arrays to perform matrix operations, offering strong energy-efficiency potential and fitting low-power devices and inference tasks that are highly sensitive to data movement. PIM and AiM routes are more often driven by memory manufacturers, embedding compute capability into HBM, GDDR, DIMM, or mobile DRAM to improve data efficiency in existing AI accelerator systems.

From a market outlook perspective, the growth potential of near-memory AI accelerator chips comes from the simultaneous expansion of two types of demand. The first is data center large-model inference. As generative AI shifts from training competition toward online inference, agents, multimodal generation, and enterprise private deployment, inference cost, latency, and energy consumption will become important hardware procurement criteria. The second is edge and endpoint AI demand. Wearables, smart cameras, industrial sensing, robotics, automotive perception, and AR/VR devices require local AI model execution while being constrained by power, size, thermal design, and privacy. Near-memory computing approaches are expected to create differentiated value in these scenarios.

This report studies the global Near-Memory AI Accelerator Chip production, demand, key manufacturers, and key regions.

This report is a detailed and comprehensive analysis of the world market for Near-Memory AI Accelerator Chip and provides market size (US\$ million) and Year-over-Year (YoY) Growth, considering 2025 as the base year. This report explores demand trends

and competition, as well as details the characteristics of Near-Memory AI Accelerator Chip that contribute to its increasing demand across many markets.

Highlights and key features of the study

Global Near-Memory AI Accelerator Chip total production and demand, 2021-2032, (Million Units)

Global Near-Memory AI Accelerator Chip total production value, 2021-2032, (USD Million)

Global Near-Memory AI Accelerator Chip production by region & country, production, value, CAGR, 2021-2032, (USD Million) & (Million Units), (based on production site)

Global Near-Memory AI Accelerator Chip consumption by region & country, CAGR, 2021-2032 & (Million Units)

U.S. VS China: Near-Memory AI Accelerator Chip domestic production, consumption, key domestic manufacturers and share

Global Near-Memory AI Accelerator Chip production by manufacturer, production, price, value and market share 2021-2026, (USD Million) & (Million Units)

Global Near-Memory AI Accelerator Chip production by Technology Paradigm, production, value, CAGR, 2021-2032, (USD Million) & (Million Units)

Global Near-Memory AI Accelerator Chip production by Application, production, value, CAGR, 2021-2032, (USD Million) & (Million Units)

This report profiles key players in the global Near-Memory AI Accelerator Chip market based on the following parameters - company overview, production, value, price, gross margin, product portfolio, geographical presence, and key developments. Key companies covered as a part of this study include d-Matrix, EnCharge AI, Mythic, TetraMem, Axelera AI, GSI Technology, Samsung Electronics, SK hynix, etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals.

Stakeholders would have ease in decision-making through various strategy matrices used in analyzing the World Near-Memory AI Accelerator Chip market

Detailed Segmentation:

Each section contains quantitative market data including market by value (US\$ Millions), volume (production, consumption) & (Million Units) and average price (US\$/Unit) by manufacturer, by Technology Paradigm, and by Application. Data is given for the years 2021-2032 by year with 2025 as the base year, 2026 as the estimate year,

and 2027-2032 as the forecast year.

Global Near-Memory AI Accelerator Chip Market, By Region:

United States

China

Europe

Japan

South Korea

ASEAN

India

Rest of World

Global Near-Memory AI Accelerator Chip Market, Segmentation by Technology Paradigm:

Digital In-Memory Computing

Analog In-Memory Computing

Processing-in-Memory

Other

Global Near-Memory AI Accelerator Chip Market, Segmentation by Data Precision:

INT4

INT8

FP8

BF16

Mixed Precision

Global Near-Memory AI Accelerator Chip Market, Segmentation by Integration Location:

Inside Memory Array

Near On-Chip Cache

Memory Buffer Chip

Inside HBM Stack

Inside DRAM Module

Beside Accelerator Card Local Memory

Global Near-Memory AI Accelerator Chip Market, Segmentation by Application:

Data Center LLM Inference

Edge Vision Inference

Low-Power Sensing Inference

Vector Search Acceleration

Recommendation System Acceleration

Mobile AI Inference

In-Memory Database Acceleration

Companies Profiled:

d-Matrix

EnCharge AI

Mythic

TetraMem

Axelera AI

GSI Technology

Samsung Electronics

SK hynix

Key Questions Answered:

1. How big is the global Near-Memory AI Accelerator Chip market?
2. What is the demand of the global Near-Memory AI Accelerator Chip market?
3. What is the year over year growth of the global Near-Memory AI Accelerator Chip market?
4. What is the production and production value of the global Near-Memory AI Accelerator Chip market?
5. Who are the key producers in the global Near-Memory AI Accelerator Chip market?
6. What are the growth factors driving the market demand?

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