

Global Near-Memory AI Accelerator Chip Market 2026 by Manufacturers, Regions, Type and Application, Forecast to 2032

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Abstracts

According to our (Global Info Research) latest study, the global Near-Memory AI Accelerator Chip market size was valued at US\$ 256 million in 2025 and is forecast to a readjusted size of US\$ 1052 million by 2032 with a CAGR of 15.0% during review period.

A near-memory AI accelerator chip is a dedicated processor or acceleration hardware category designed around the data-movement bottleneck in artificial intelligence workloads. Its core positioning is not merely to increase the number of arithmetic units, but to place compute capability inside memory arrays, near memory chips, within memory buffer chips, or alongside high-bandwidth memory channels, so that neural network weights, activations, and retrieval vectors can complete reading, multiply-accumulate, reduction, search, and inference operations over shorter data paths. These products typically adopt digital in-memory computing, analog in-memory computing, processing-in-memory, near-data dataflow, large on-chip SRAM, RRAM arrays, HBM-PIM, GDDR-AiM, or associative processing array architectures. Their main purpose is to improve energy efficiency, latency, and throughput for large-model inference, recommendation systems, edge vision, low-power sensing, vector search, and memory-intensive AI workloads. Common delivery forms include standalone chips, AI accelerator cards, in-memory computing SoCs, PIM memory modules, server inference platforms, and customer-specific ASICs. Major customers include cloud computing providers, server vendors, edge device manufacturers, industrial vision system providers, automotive electronics companies, wearable device makers, and high-performance search system integrators.

The core industrial logic of near-memory AI accelerator chips is that artificial intelligence

computing is shifting from a pure pursuit of peak compute performance to a combined pursuit of data access efficiency, system-level energy efficiency, and inference cost per unit. Traditional processor architectures frequently move model weights, activations, and intermediate results between external memory and compute cores. As large-model inference, recommendation systems, vector search, and multi-channel video analytics become mainstream workloads, the power consumption, latency, and bandwidth occupation caused by data movement are becoming system-level bottlenecks. Near-memory computing, in-memory computing, and processing-in-memory approaches shorten data paths by placing part of matrix multiplication, reduction, search, or preprocessing tasks inside memory arrays or near memory channels, thereby improving overall AI system efficiency.

From a technology-route perspective, near-memory AI accelerator chips are not a single product form, but an industrial cluster formed by multiple architectural routes. Digital in-memory computing typically relies on SRAM and standard CMOS processes, emphasizing manufacturability, stable accuracy, and software toolchain compatibility, making it suitable for edge vision, industrial inspection, and multi-channel video analytics. Analog in-memory computing usually uses flash memory, RRAM, or other memory arrays to perform matrix operations, offering strong energy-efficiency potential and fitting low-power devices and inference tasks that are highly sensitive to data movement. PIM and AiM routes are more often driven by memory manufacturers, embedding compute capability into HBM, GDDR, DIMM, or mobile DRAM to improve data efficiency in existing AI accelerator systems.

From a market outlook perspective, the growth potential of near-memory AI accelerator chips comes from the simultaneous expansion of two types of demand. The first is data center large-model inference. As generative AI shifts from training competition toward online inference, agents, multimodal generation, and enterprise private deployment, inference cost, latency, and energy consumption will become important hardware procurement criteria. The second is edge and endpoint AI demand. Wearables, smart cameras, industrial sensing, robotics, automotive perception, and AR/VR devices require local AI model execution while being constrained by power, size, thermal design, and privacy. Near-memory computing approaches are expected to create differentiated value in these scenarios.

This report is a detailed and comprehensive analysis for global Near-Memory AI Accelerator Chip market. Both quantitative and qualitative analyses are presented by manufacturers, by region & country, by Technology Paradigm and by Application. As the market is constantly changing, this report explores the competition, supply and

demand trends, as well as key factors that contribute to its changing demands across many markets. Company profiles and product examples of selected competitors, along with market share estimates of some of the selected leaders for the year 2025, are provided.

Key Features:

Global Near-Memory AI Accelerator Chip market size and forecasts, in consumption value (\$ Million), sales quantity (Million Units), and average selling prices (US\$/Unit), 2021-2032

Global Near-Memory AI Accelerator Chip market size and forecasts by region and country, in consumption value (\$ Million), sales quantity (Million Units), and average selling prices (US\$/Unit), 2021-2032

Global Near-Memory AI Accelerator Chip market size and forecasts, by Technology Paradigm and by Application, in consumption value (\$ Million), sales quantity (Million Units), and average selling prices (US\$/Unit), 2021-2032

Global Near-Memory AI Accelerator Chip market shares of main players, shipments in revenue (\$ Million), sales quantity (Million Units), and ASP (US\$/Unit), 2021-2026

The Primary Objectives in This Report Are:

- To determine the size of the total market opportunity of global and key countries

- To assess the growth potential for Near-Memory AI Accelerator Chip

- To forecast future growth in each product and end-use market

- To assess competitive factors affecting the marketplace

This report profiles key players in the global Near-Memory AI Accelerator Chip market based on the following parameters - company overview, sales quantity, revenue, price, gross margin, product portfolio, geographical presence, and key developments. Key companies covered as a part of this study include d-Matrix, EnCharge AI, Mythic, TetraMem, Axelera AI, GSI Technology, Samsung Electronics, SK hynix, etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals.

Market Segmentation

Near-Memory AI Accelerator Chip market is split by Technology Paradigm and by Application. For the period 2021-2032, the growth among segments provides accurate calculations and forecasts for consumption value by Technology Paradigm, and by Application in terms of volume and value. This analysis can help you expand your business by targeting qualified niche markets.

Market segment by Technology Paradigm

Digital In-Memory Computing

Analog In-Memory Computing

Processing-in-Memory

Other

Market segment by Data Precision

INT4

INT8

FP8

BF16

Mixed Precision

Market segment by Integration Location

Inside Memory Array

Near On-Chip Cache

Memory Buffer Chip

Inside HBM Stack

Inside DRAM Module

Beside Accelerator Card Local Memory

Market segment by Application

Data Center LLM Inference

Edge Vision Inference

Low-Power Sensing Inference

Vector Search Acceleration

Recommendation System Acceleration

Mobile AI Inference

In-Memory Database Acceleration

Major players covered

d-Matrix

EnCharge AI

Mythic

TetraMem

Axelera AI

GSI Technology

Samsung Electronics

SK hynix

Market segment by region, regional analysis covers

North America (United States, Canada, and Mexico)

Europe (Germany, France, United Kingdom, Russia, Italy, and Rest of Europe)

Asia-Pacific (China, Japan, Korea, India, Southeast Asia, and Australia)

South America (Brazil, Argentina, Colombia, and Rest of South America)

Middle East & Africa (Saudi Arabia, UAE, Egypt, South Africa, and Rest of Middle East & Africa)

The content of the study subjects, includes a total of 15 chapters:

Chapter 1, to describe Near-Memory AI Accelerator Chip product scope, market overview, market estimation caveats and base year.

Chapter 2, to profile the top manufacturers of Near-Memory AI Accelerator Chip, with price, sales quantity, revenue, and global market share of Near-Memory AI Accelerator Chip from 2021 to 2026.

Chapter 3, the Near-Memory AI Accelerator Chip competitive situation, sales quantity, revenue, and global market share of top manufacturers are analyzed emphatically by landscape contrast.

Chapter 4, the Near-Memory AI Accelerator Chip breakdown data are shown at the regional level, to show the sales quantity, consumption value, and growth by regions, from 2021 to 2032.

Chapter 5 and 6, to segment the sales by Technology Paradigm and by Application, with sales market share and growth rate by Technology Paradigm, by Application, from 2021 to 2032.

Chapter 7, 8, 9, 10 and 11, to break the sales data at the country level, with sales quantity, consumption value, and market share for key countries in the world, from 2021 to 2026. and Near-Memory AI Accelerator Chip market forecast, by regions, by Technology Paradigm, and by Application, with sales and revenue, from 2027 to 2032.

Chapter 12, market dynamics, drivers, restraints, trends, and Porters Five Forces analysis.

Chapter 13, the key raw materials and key suppliers, and industry chain of Near-Memory AI Accelerator Chip.

Chapter 14 and 15, to describe Near-Memory AI Accelerator Chip sales channel, distributors, customers, research findings and conclusion.

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