

Global Low-Power IC Design Services Supply, Demand and Key Producers, 2026-2032

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Abstracts

The global Low-Power IC Design Services market size is expected to reach \$ 5387 million by 2032, rising at a market growth of 8.5% CAGR during the forecast period (2026-2032).

Low-power IC design services are professional engineering services for ASIC, SoC, and custom IC development, intended to systematically reduce dynamic power, static leakage, peak current, thermal density, and system-level energy consumption under defined constraints for performance, area, cost, reliability, and time to market. These services usually begin at the specification and microarchitecture stages, where the target process, IP portfolio, software workloads, and end-product power budget are used to plan power domains, clock domains, voltage domains, power-state machines, and power-management interfaces. They then continue through RTL design, logic synthesis, power-aware simulation, formal verification, physical implementation, timing closure, IR-drop analysis, thermal analysis, DFT insertion, and signoff. Common techniques include clock gating, power gating, multi-voltage supply, dynamic voltage and frequency scaling, adaptive voltage scaling, low-leakage standard-cell library selection, memory power optimization, power-aware verification, and software-coordinated power management. Typical customers include fabless semiconductor companies, system vendors, cloud service providers, automotive electronics companies, industrial equipment companies, consumer electronics brands, and semiconductor startups. Applications cover mobile devices, IoT, wearables, edge AI, automotive electronics, data-center acceleration, wireless communications, medical electronics, and industrial control. Delivery models include low-power architecture consulting, module-level optimization, front-end to back-end implementation services, RTL-to-GDSII delivery, turnkey ASIC development, IP integration, packaging and testing coordination, and production ramp support.

The strategic value of low-power IC design services is evolving from localized power optimization into a system-level capability for chip competitiveness. As ASICs and SoCs are increasingly adopted in mobile devices, IoT, edge AI, automotive electronics, and data centers, customers no longer focus only on reducing the power consumption of individual modules. Instead, they require a continuous energy-efficiency loop across architecture, software, IP, process technology, packaging, and production ramp-up. Low-power capability must be embedded at the early specification stage through the planning of power domains, clock domains, voltage domains, power states, and software control interfaces, and must then be continuously converged through RTL, synthesis, verification, place and route, IR-drop analysis, thermal analysis, and signoff. Its commercial value lies in extending battery life, reducing thermal costs, improving chip performance per watt, lowering system failure risks, and enhancing product differentiation. For customers, the value of external low-power design services is not simply engineering capacity, but the integration of advanced-node experience, power-aware methodology, IP integration capability, and production risk control into deliverable outcomes.

The competitive landscape is becoming increasingly ecosystem-driven and regionally clustered. Taiwan, Japan, South Korea, India, mainland China, the United States, and Europe all have different types of low-power IC design service providers. Taiwanese companies have strong foundations in turnkey ASIC services, IP integration, and advanced packaging coordination. Japanese companies emphasize high-reliability SoC design and power-intent flows. Korean companies are developing design services through the Samsung Foundry ecosystem. Indian companies continue to expand in engineering outsourcing, verification, back-end design, and embedded-system collaboration. Mainland Chinese companies are building service capabilities around domestic process technologies, IoT, automotive, and custom SoCs. Competition among service providers is no longer determined only by low-cost engineering labor, but by the ability to cover the full chain from specification to GDSII, from IP to packaging and testing, and from low-power verification to production ramp-up. As chip complexity increases, suppliers that can combine advanced process technology, low-power methodology, high-speed interfaces, automotive functional safety, and AI workload optimization will gain stronger pricing power.

Future growth will be driven jointly by demand for energy-efficient computing and customized chips. AI inference is moving from cloud environments toward the edge, automotive electronics are evolving from single-function control to high-computing domain control, industrial and medical devices are upgrading from connected terminals

to low-power systems with local intelligence, and cloud service providers are continuing to explore custom ASICs to improve computing efficiency per unit of energy. These trends will expand low-power design services beyond traditional smartphone and consumer electronics use cases into edge AI, smart vehicles, industrial IoT, data-center acceleration, and communications infrastructure. In terms of market size, low-power IC design services are usually embedded within ASIC design services, SoC development services, and chip design outsourcing contracts, so public statistical definitions vary. However, their growth rate is expected to exceed that of traditional general-purpose design services. As AI-assisted EDA, power-aware verification, advanced packaging, and software-coordinated power management mature, low-power design services will gradually evolve from project-based engineering services into platformized, process-driven, and ecosystem-based delivery capabilities.

This report studies the global Low-Power IC Design Services demand, key companies, and key regions.

This report is a detailed and comprehensive analysis of the world market for Low-Power IC Design Services, and provides market size (US\$ million) and Year-over-Year (YoY) growth, considering 2025 as the base year. This report explores demand trends and competition, as well as details the characteristics of Low-Power IC Design Services that contribute to its increasing demand across many markets.

Highlights and key features of the study

Global Low-Power IC Design Services total market, 2021-2032, (USD Million)

Global Low-Power IC Design Services total market by region & country, CAGR, 2021-2032, (USD Million)

U.S. VS China: Low-Power IC Design Services total market, key domestic companies, and share, (USD Million)

Global Low-Power IC Design Services revenue by player, revenue and market share 2021-2026, (USD Million)

Global Low-Power IC Design Services total market by Design Task, CAGR, 2021-2032, (USD Million)

Global Low-Power IC Design Services total market by Application, CAGR, 2021-2032, (USD Million)

This report profiles major players in the global Low-Power IC Design Services market based on the following parameters - company overview, revenue, gross margin, product portfolio, geographical presence, and key developments. Key companies covered as a

part of this study include GUC, Socionext Inc., MegaChips Corporation, Alchip Technologies, Limited, Faraday Technology Corporation, VeriSilicon Microelectronics (Shanghai) Co., Ltd., Brite Semiconductor (Shanghai) Co., Ltd., MediaTek Inc., ADTechnology Co., Ltd., GAONCHIPS Co., Ltd., etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals.

Stakeholders would have ease in decision-making through various strategy matrices used in analyzing the world Low-Power IC Design Services market

Detailed Segmentation:

Each section contains quantitative market data including market by value (US\$ Millions), by player, by regions, by Design Task, and by Application. Data is given for the years 2021-2032 by year with 2025 as the base year, 2026 as the estimate year, and 2027-2032 as the forecast year.

Global Low-Power IC Design Services Market, By Region:

United States

China

Europe

Japan

South Korea

ASEAN

India

Rest of World

Global Low-Power IC Design Services Market, Segmentation by Design Task:

Architectural Low-Power Design

RTL Low-Power Design

Power Intent Design

Power-Aware Verification

Physical Low-Power Implementation

Low-Power Signoff Optimization

Low-Power Production Ramp Support

Other

Global Low-Power IC Design Services Market, Segmentation by Technical Method:

Clock Gating

Power Gating

Multi-Voltage Domain

Dynamic Voltage and Frequency Scaling

Adaptive Voltage Scaling

Low-Leakage Library Optimization

Memory Power Optimization

Software-Coordinated Power Management

Other

Global Low-Power IC Design Services Market, Segmentation by Delivery Model:

Consulting and Assessment Service

Module Design Service

Front-End and Back-End Co-Design Service

RTL-to-GDSII Service

Turnkey ASIC Service

Production Support Service

Other

Global Low-Power IC Design Services Market, Segmentation by Application:

Mobile Devices

IoT Devices

Wearable Devices

Edge AI

Other

Companies Profiled:

GUC

Socionext Inc.

MegaChips Corporation

Alchip Technologies, Limited

Faraday Technology Corporation

VeriSilicon Microelectronics (Shanghai) Co., Ltd.

Brite Semiconductor (Shanghai) Co., Ltd.

MediaTek Inc.

ADTechnology Co., Ltd.

GAONCHIPS Co., Ltd.

CoAsia SEMI Co., Ltd.

Cyient Semiconductors

Tessolve Semiconductor Pvt. Ltd.

eInfochips

MosChip Technologies Limited

Sondrel (Holdings) plc

Avnet, Inc.

CoreHW Oy

eTech ASIC Inc.

Progate Group Corporation

ALi Corporation

CMSC, Inc.

Key Questions Answered

1. How big is the global Low-Power IC Design Services market?
2. What is the demand of the global Low-Power IC Design Services market?

3. What is the year over year growth of the global Low-Power IC Design Services market?
4. What is the total value of the global Low-Power IC Design Services market?
5. Who are the Major Players in the global Low-Power IC Design Services market?
6. What are the growth factors driving the market demand?

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