

Global LLM-Optimized AI Accelerator Chip Market 2026 by Manufacturers, Regions, Type and Application, Forecast to 2032

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Abstracts

According to our (Global Info Research) latest study, the global LLM-Optimized AI Accelerator Chip market size was valued at US\$ 30231 million in 2025 and is forecast to a readjusted size of US\$ 69266 million by 2032 with a CAGR of 10.0% during review period.

LLM-optimized AI accelerator chips are specialized computing chips, accelerator cards, modules, servers, and cloud-based compute platforms designed for large language models, multimodal models, generative AI, and agentic systems. Their core task is to efficiently execute matrix multiplication, tensor computation, attention mechanisms, low-precision floating-point operations, weight access, KV-cache access, and multi-chip communication during training, fine-tuning, and inference. These products typically adopt GPU, TPU, NPU, XPU, RDU, LPU, or wafer-scale computing architectures, and are combined with HBM, high-speed interconnects, on-chip cache, mixed-precision computing, quantized inference, compilers, and model deployment frameworks to address issues such as insufficient compute power, limited memory capacity, communication bottlenecks, high inference latency, and high cost per token as large model parameter scales continue to expand. Typical applications include foundation model pretraining, industry model fine-tuning, real-time dialogue, code generation, text generation, image generation, retrieval-augmented generation, enterprise knowledge-base Q&A, agentic task orchestration, and private large model deployment. Major customers include cloud service providers, AI model companies, internet platforms, intelligent computing centers, server manufacturers, research institutions, and large enterprises.

LLM-optimized AI accelerator chips have become one of the most critical hardware

layers in artificial intelligence infrastructure. Compared with conventional AI chips, these products must support workloads with larger parameter counts, longer context windows, more frequent inference calls, and higher sensitivity to deployment cost. As a result, improving peak compute performance on a single chip is no longer sufficient to create a durable competitive advantage. The industry is shifting toward system-level competition centered on chip architecture, high-bandwidth memory, high-speed interconnects, advanced packaging, server systems, liquid cooling, compilers, inference frameworks, and cloud platform integration. Training workloads emphasize efficient communication across multiple cards, servers, and racks, while inference workloads emphasize low latency, high throughput, low power consumption, and lower cost per token. As generative AI moves from model development into large-scale deployment, chip suppliers are no longer only hardware vendors but providers of compute platforms capable of supporting the full LLM lifecycle.

From a competitive perspective, the LLM-optimized AI accelerator market is developing through multiple architectural routes. General-purpose GPUs remain the mainstream choice for training and high-end inference, supported by mature ecosystems, broad software compatibility, and large-scale server supply chains. Cloud providers' custom chips are creating a closed-loop model from silicon to cloud instances and model services, binding internal models, external developers, and enterprise customers to their platforms. At the same time, TPUs, RDUs, LPUs, wafer-scale chips, and low-power inference accelerators are building differentiated positions around specific workloads, especially real-time inference, enterprise RAG, agentic AI, multi-model residency, and local edge deployment. Chinese, Korean, and Japanese vendors still face challenges in ecosystem maturity and advanced manufacturing resources, but they have continuing opportunities in local intelligent computing centers, sovereign AI, industry-specific models, and localized supply chains. Future competition will depend more on software-hardware adaptation, cluster reliability, and real business cost than on nominal chip specifications alone.

Future market growth will be driven by the normalization of large model training, enterprise private deployment, commercialization of generative AI applications, the spread of agentic AI systems, and sovereign AI infrastructure investment. The data center accelerator market reached tens of billions of dollars in 2025 and is expected to maintain high growth through 2032, indicating that investment in LLM compute remains in an expansion cycle. At the same time, pricing trends will diverge structurally. High-end training chips and HBM resources may remain constrained, while mid-range inference chips and dedicated inference cards are likely to push down unit inference costs as competition intensifies. Production growth will be jointly constrained by

advanced process capacity, HBM supply, CoWoS-class advanced packaging, liquid-cooled server availability, and power infrastructure. In the long term, companies with chip design capability, system integration strength, software ecosystem control, supply chain coordination, and strong customer workload binding will be better positioned to capture sustainable share in the LLM compute market.

This report is a detailed and comprehensive analysis for global LLM-Optimized AI Accelerator Chip market. Both quantitative and qualitative analyses are presented by manufacturers, by region & country, by Delivery Form and by Application. As the market is constantly changing, this report explores the competition, supply and demand trends, as well as key factors that contribute to its changing demands across many markets. Company profiles and product examples of selected competitors, along with market share estimates of some of the selected leaders for the year 2025, are provided.

Key Features:

Global LLM-Optimized AI Accelerator Chip market size and forecasts, in consumption value (\$ Million), sales quantity (Million Units), and average selling prices (US\$/Unit), 2021-2032

Global LLM-Optimized AI Accelerator Chip market size and forecasts by region and country, in consumption value (\$ Million), sales quantity (Million Units), and average selling prices (US\$/Unit), 2021-2032

Global LLM-Optimized AI Accelerator Chip market size and forecasts, by Delivery Form and by Application, in consumption value (\$ Million), sales quantity (Million Units), and average selling prices (US\$/Unit), 2021-2032

Global LLM-Optimized AI Accelerator Chip market shares of main players, shipments in revenue (\$ Million), sales quantity (Million Units), and ASP (US\$/Unit), 2021-2026

The Primary Objectives in This Report Are:

To determine the size of the total market opportunity of global and key countries

To assess the growth potential for LLM-Optimized AI Accelerator Chip

To forecast future growth in each product and end-use market

To assess competitive factors affecting the marketplace

This report profiles key players in the global LLM-Optimized AI Accelerator Chip market based on the following parameters - company overview, sales quantity, revenue, price, gross margin, product portfolio, geographical presence, and key developments. Key companies covered as a part of this study include NVIDIA, AMD, Intel, Amazon Web Services, Google, Microsoft, Qualcomm, Cerebras, Groq, SambaNova, etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals.

Market Segmentation

LLM-Optimized AI Accelerator Chip market is split by Delivery Form and by Application. For the period 2021-2032, the growth among segments provides accurate calculations and forecasts for consumption value by Delivery Form, and by Application in terms of volume and value. This analysis can help you expand your business by targeting qualified niche markets.

Market segment by Delivery Form

Bare Chip

Accelerator Card

Server Module

Full System

Market segment by Compute Task

Training LLM-Optimized AI Accelerator Chip

Fine-Tuning LLM-Optimized AI Accelerator Chip

Inference LLM-Optimized AI Accelerator Chip

Training and Inference LLM-Optimized AI Accelerator Chip

Market segment by Memory System

HBM

GDDR

Market segment by Application

Foundation Model Pretraining

Industry Model Fine-Tuning

Online Generative Inference

Retrieval-Augmented Inference

Agentic Inference

Local Edge Inference

Major players covered

NVIDIA

AMD

Intel

Amazon Web Services

Google

Microsoft

Qualcomm

Cerebras

Groq

SambaNova

Tenstorrent

Huawei

Kunlunxin

Cambricon

Moore Threads

MetaX

Biren Technology

Rebellions

FuriosaAI

DEEPX

Preferred Networks

EdgeCortex

Market segment by region, regional analysis covers

North America (United States, Canada, and Mexico)

Europe (Germany, France, United Kingdom, Russia, Italy, and Rest of Europe)

Asia-Pacific (China, Japan, Korea, India, Southeast Asia, and Australia)

South America (Brazil, Argentina, Colombia, and Rest of South America)

Middle East & Africa (Saudi Arabia, UAE, Egypt, South Africa, and Rest of Middle East & Africa)

The content of the study subjects, includes a total of 15 chapters:

Chapter 1, to describe LLM-Optimized AI Accelerator Chip product scope, market overview, market estimation caveats and base year.

Chapter 2, to profile the top manufacturers of LLM-Optimized AI Accelerator Chip, with price, sales quantity, revenue, and global market share of LLM-Optimized AI Accelerator Chip from 2021 to 2026.

Chapter 3, the LLM-Optimized AI Accelerator Chip competitive situation, sales quantity, revenue, and global market share of top manufacturers are analyzed emphatically by landscape contrast.

Chapter 4, the LLM-Optimized AI Accelerator Chip breakdown data are shown at the regional level, to show the sales quantity, consumption value, and growth by regions, from 2021 to 2032.

Chapter 5 and 6, to segment the sales by Delivery Form and by Application, with sales market share and growth rate by Delivery Form, by Application, from 2021 to 2032.

Chapter 7, 8, 9, 10 and 11, to break the sales data at the country level, with sales quantity, consumption value, and market share for key countries in the world, from 2021 to 2026. and LLM-Optimized AI Accelerator Chip market forecast, by regions, by Delivery Form, and by Application, with sales and revenue, from 2027 to 2032.

Chapter 12, market dynamics, drivers, restraints, trends, and Porters Five Forces analysis.

Chapter 13, the key raw materials and key suppliers, and industry chain of LLM-

Optimized AI Accelerator Chip.

Chapter 14 and 15, to describe LLM-Optimized AI Accelerator Chip sales channel, distributors, customers, research findings and conclusion.

Contents

1 MARKET OVERVIEW

1.1 Product Overview and Scope

1.2 Market Estimation Caveats and Base Year

1.3 Market Analysis by Delivery Form

1.3.1 Overview: Global LLM-Optimized AI Accelerator Chip Consumption Value by Delivery Form: 2021 Versus 2025 Versus 2032

1.3.2 Bare Chip

1.3.3 Accelerator Card

1.3.4 Server Module

1.3.5 Full System

1.4 Market Analysis by Compute Task

1.4.1 Overview: Global LLM-Optimized AI Accelerator Chip Consumption Value by Compute Task: 2021 Versus 2025 Versus 2032

1.4.2 Training LLM-Optimized AI Accelerator Chip

1.4.3 Fine-Tuning LLM-Optimized AI Accelerator Chip

1.4.4 Inference LLM-Optimized AI Accelerator Chip

1.4.5 Training and Inference LLM-Optimized AI Accelerator Chip

1.5 Market Analysis by Memory System

1.5.1 Overview: Global LLM-Optimized AI Accelerator Chip Consumption Value by Memory System: 2021 Versus 2025 Versus 2032

1.5.2 HBM

1.5.3 GDDR

1.6 Market Analysis by Application

1.6.1 Overview: Global LLM-Optimized AI Accelerator Chip Consumption Value by Application: 2021 Versus 2025 Versus 2032

1.6.2 Foundation Model Pretraining

1.6.3 Industry Model Fine-Tuning

1.6.4 Online Generative Inference

1.6.5 Retrieval-Augmented Inference

1.6.6 Agentic Inference

1.6.7 Local Edge Inference

1.7 Global LLM-Optimized AI Accelerator Chip Market Size & Forecast

1.7.1 Global LLM-Optimized AI Accelerator Chip Consumption Value (2021 & 2025 & 2032)

1.7.2 Global LLM-Optimized AI Accelerator Chip Sales Quantity (2021-2032)

1.7.3 Global LLM-Optimized AI Accelerator Chip Average Price (2021-2032)

2 MANUFACTURERS PROFILES

2.1 NVIDIA

2.1.1 NVIDIA Details

2.1.2 NVIDIA Major Business

2.1.3 NVIDIA LLM-Optimized AI Accelerator Chip Product and Services

2.1.4 NVIDIA LLM-Optimized AI Accelerator Chip Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)

2.1.5 NVIDIA Recent Developments/Updates

2.2 AMD

2.2.1 AMD Details

2.2.2 AMD Major Business

2.2.3 AMD LLM-Optimized AI Accelerator Chip Product and Services

2.2.4 AMD LLM-Optimized AI Accelerator Chip Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)

2.2.5 AMD Recent Developments/Updates

2.3 Intel

2.3.1 Intel Details

2.3.2 Intel Major Business

2.3.3 Intel LLM-Optimized AI Accelerator Chip Product and Services

2.3.4 Intel LLM-Optimized AI Accelerator Chip Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)

2.3.5 Intel Recent Developments/Updates

2.4 Amazon Web Services

2.4.1 Amazon Web Services Details

2.4.2 Amazon Web Services Major Business

2.4.3 Amazon Web Services LLM-Optimized AI Accelerator Chip Product and Services

2.4.4 Amazon Web Services LLM-Optimized AI Accelerator Chip Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)

2.4.5 Amazon Web Services Recent Developments/Updates

2.5 Google

2.5.1 Google Details

2.5.2 Google Major Business

2.5.3 Google LLM-Optimized AI Accelerator Chip Product and Services

2.5.4 Google LLM-Optimized AI Accelerator Chip Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)

2.5.5 Google Recent Developments/Updates

2.6 Microsoft

- 2.6.1 Microsoft Details
- 2.6.2 Microsoft Major Business
- 2.6.3 Microsoft LLM-Optimized AI Accelerator Chip Product and Services
- 2.6.4 Microsoft LLM-Optimized AI Accelerator Chip Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
- 2.6.5 Microsoft Recent Developments/Updates
- 2.7 Qualcomm
 - 2.7.1 Qualcomm Details
 - 2.7.2 Qualcomm Major Business
 - 2.7.3 Qualcomm LLM-Optimized AI Accelerator Chip Product and Services
 - 2.7.4 Qualcomm LLM-Optimized AI Accelerator Chip Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
 - 2.7.5 Qualcomm Recent Developments/Updates
- 2.8 Cerebras
 - 2.8.1 Cerebras Details
 - 2.8.2 Cerebras Major Business
 - 2.8.3 Cerebras LLM-Optimized AI Accelerator Chip Product and Services
 - 2.8.4 Cerebras LLM-Optimized AI Accelerator Chip Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
 - 2.8.5 Cerebras Recent Developments/Updates
- 2.9 Groq
 - 2.9.1 Groq Details
 - 2.9.2 Groq Major Business
 - 2.9.3 Groq LLM-Optimized AI Accelerator Chip Product and Services
 - 2.9.4 Groq LLM-Optimized AI Accelerator Chip Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
 - 2.9.5 Groq Recent Developments/Updates
- 2.10 SambaNova
 - 2.10.1 SambaNova Details
 - 2.10.2 SambaNova Major Business
 - 2.10.3 SambaNova LLM-Optimized AI Accelerator Chip Product and Services
 - 2.10.4 SambaNova LLM-Optimized AI Accelerator Chip Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
 - 2.10.5 SambaNova Recent Developments/Updates
- 2.11 Tenstorrent
 - 2.11.1 Tenstorrent Details
 - 2.11.2 Tenstorrent Major Business
 - 2.11.3 Tenstorrent LLM-Optimized AI Accelerator Chip Product and Services
 - 2.11.4 Tenstorrent LLM-Optimized AI Accelerator Chip Sales Quantity, Average Price,

Revenue, Gross Margin and Market Share (2021-2026)

2.11.5 Tenstorrent Recent Developments/Updates

2.12 Huawei

2.12.1 Huawei Details

2.12.2 Huawei Major Business

2.12.3 Huawei LLM-Optimized AI Accelerator Chip Product and Services

2.12.4 Huawei LLM-Optimized AI Accelerator Chip Sales Quantity, Average Price,

Revenue, Gross Margin and Market Share (2021-2026)

2.12.5 Huawei Recent Developments/Updates

2.13 Kunlunxin

2.13.1 Kunlunxin Details

2.13.2 Kunlunxin Major Business

2.13.3 Kunlunxin LLM-Optimized AI Accelerator Chip Product and Services

2.13.4 Kunlunxin LLM-Optimized AI Accelerator Chip Sales Quantity, Average Price,

Revenue, Gross Margin and Market Share (2021-2026)

2.13.5 Kunlunxin Recent Developments/Updates

2.14 Cambricon

2.14.1 Cambricon Details

2.14.2 Cambricon Major Business

2.14.3 Cambricon LLM-Optimized AI Accelerator Chip Product and Services

2.14.4 Cambricon LLM-Optimized AI Accelerator Chip Sales Quantity, Average Price,

Revenue, Gross Margin and Market Share (2021-2026)

2.14.5 Cambricon Recent Developments/Updates

2.15 Moore Threads

2.15.1 Moore Threads Details

2.15.2 Moore Threads Major Business

2.15.3 Moore Threads LLM-Optimized AI Accelerator Chip Product and Services

2.15.4 Moore Threads LLM-Optimized AI Accelerator Chip Sales Quantity, Average

Price, Revenue, Gross Margin and Market Share (2021-2026)

2.15.5 Moore Threads Recent Developments/Updates

2.16 MetaX

2.16.1 MetaX Details

2.16.2 MetaX Major Business

2.16.3 MetaX LLM-Optimized AI Accelerator Chip Product and Services

2.16.4 MetaX LLM-Optimized AI Accelerator Chip Sales Quantity, Average Price,

Revenue, Gross Margin and Market Share (2021-2026)

2.16.5 MetaX Recent Developments/Updates

2.17 Biren Technology

2.17.1 Biren Technology Details

- 2.17.2 Biren Technology Major Business
- 2.17.3 Biren Technology LLM-Optimized AI Accelerator Chip Product and Services
- 2.17.4 Biren Technology LLM-Optimized AI Accelerator Chip Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
- 2.17.5 Biren Technology Recent Developments/Updates
- 2.18 Rebellions
 - 2.18.1 Rebellions Details
 - 2.18.2 Rebellions Major Business
 - 2.18.3 Rebellions LLM-Optimized AI Accelerator Chip Product and Services
 - 2.18.4 Rebellions LLM-Optimized AI Accelerator Chip Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
 - 2.18.5 Rebellions Recent Developments/Updates
- 2.19 FuriosaAI
 - 2.19.1 FuriosaAI Details
 - 2.19.2 FuriosaAI Major Business
 - 2.19.3 FuriosaAI LLM-Optimized AI Accelerator Chip Product and Services
 - 2.19.4 FuriosaAI LLM-Optimized AI Accelerator Chip Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
 - 2.19.5 FuriosaAI Recent Developments/Updates
- 2.20 DEEPX
 - 2.20.1 DEEPX Details
 - 2.20.2 DEEPX Major Business
 - 2.20.3 DEEPX LLM-Optimized AI Accelerator Chip Product and Services
 - 2.20.4 DEEPX LLM-Optimized AI Accelerator Chip Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
 - 2.20.5 DEEPX Recent Developments/Updates
- 2.21 Preferred Networks
 - 2.21.1 Preferred Networks Details
 - 2.21.2 Preferred Networks Major Business
 - 2.21.3 Preferred Networks LLM-Optimized AI Accelerator Chip Product and Services
 - 2.21.4 Preferred Networks LLM-Optimized AI Accelerator Chip Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
 - 2.21.5 Preferred Networks Recent Developments/Updates
- 2.22 EdgeCortex
 - 2.22.1 EdgeCortex Details
 - 2.22.2 EdgeCortex Major Business
 - 2.22.3 EdgeCortex LLM-Optimized AI Accelerator Chip Product and Services
 - 2.22.4 EdgeCortex LLM-Optimized AI Accelerator Chip Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)

2.22.5 EdgeCortex Recent Developments/Updates

3 COMPETITIVE ENVIRONMENT: LLM-OPTIMIZED AI ACCELERATOR CHIP BY MANUFACTURER

3.1 Global LLM-Optimized AI Accelerator Chip Sales Quantity by Manufacturer (2021-2026)

3.2 Global LLM-Optimized AI Accelerator Chip Revenue by Manufacturer (2021-2026)

3.3 Global LLM-Optimized AI Accelerator Chip Average Price by Manufacturer (2021-2026)

3.4 Market Share Analysis (2025)

3.4.1 Producer Shipments of LLM-Optimized AI Accelerator Chip by Manufacturer Revenue (\$MM) and Market Share (%): 2025

3.4.2 Top 3 LLM-Optimized AI Accelerator Chip Manufacturer Market Share in 2025

3.4.3 Top 6 LLM-Optimized AI Accelerator Chip Manufacturer Market Share in 2025

3.5 LLM-Optimized AI Accelerator Chip Market: Overall Company Footprint Analysis

3.5.1 LLM-Optimized AI Accelerator Chip Market: Region Footprint

3.5.2 LLM-Optimized AI Accelerator Chip Market: Company Product Type Footprint

3.5.3 LLM-Optimized AI Accelerator Chip Market: Company Product Application Footprint

3.6 New Market Entrants and Barriers to Market Entry

3.7 Mergers, Acquisition, Agreements, and Collaborations

4 CONSUMPTION ANALYSIS BY REGION

4.1 Global LLM-Optimized AI Accelerator Chip Market Size by Region

4.1.1 Global LLM-Optimized AI Accelerator Chip Sales Quantity by Region (2021-2032)

4.1.2 Global LLM-Optimized AI Accelerator Chip Consumption Value by Region (2021-2032)

4.1.3 Global LLM-Optimized AI Accelerator Chip Average Price by Region (2021-2032)

4.2 North America LLM-Optimized AI Accelerator Chip Consumption Value (2021-2032)

4.3 Europe LLM-Optimized AI Accelerator Chip Consumption Value (2021-2032)

4.4 Asia-Pacific LLM-Optimized AI Accelerator Chip Consumption Value (2021-2032)

4.5 South America LLM-Optimized AI Accelerator Chip Consumption Value (2021-2032)

4.6 Middle East & Africa LLM-Optimized AI Accelerator Chip Consumption Value (2021-2032)

5 MARKET SEGMENT BY DELIVERY FORM

5.1 Global LLM-Optimized AI Accelerator Chip Sales Quantity by Delivery Form
(2021-2032)

5.2 Global LLM-Optimized AI Accelerator Chip Consumption Value by Delivery Form
(2021-2032)

5.3 Global LLM-Optimized AI Accelerator Chip Average Price by Delivery Form
(2021-2032)

6 MARKET SEGMENT BY APPLICATION

6.1 Global LLM-Optimized AI Accelerator Chip Sales Quantity by Application
(2021-2032)

6.2 Global LLM-Optimized AI Accelerator Chip Consumption Value by Application
(2021-2032)

6.3 Global LLM-Optimized AI Accelerator Chip Average Price by Application
(2021-2032)

7 NORTH AMERICA

7.1 North America LLM-Optimized AI Accelerator Chip Sales Quantity by Delivery Form
(2021-2032)

7.2 North America LLM-Optimized AI Accelerator Chip Sales Quantity by Application
(2021-2032)

7.3 North America LLM-Optimized AI Accelerator Chip Market Size by Country

7.3.1 North America LLM-Optimized AI Accelerator Chip Sales Quantity by Country
(2021-2032)

7.3.2 North America LLM-Optimized AI Accelerator Chip Consumption Value by
Country (2021-2032)

7.3.3 United States Market Size and Forecast (2021-2032)

7.3.4 Canada Market Size and Forecast (2021-2032)

7.3.5 Mexico Market Size and Forecast (2021-2032)

8 EUROPE

8.1 Europe LLM-Optimized AI Accelerator Chip Sales Quantity by Delivery Form
(2021-2032)

8.2 Europe LLM-Optimized AI Accelerator Chip Sales Quantity by Application
(2021-2032)

8.3 Europe LLM-Optimized AI Accelerator Chip Market Size by Country

8.3.1 Europe LLM-Optimized AI Accelerator Chip Sales Quantity by Country (2021-2032)

8.3.2 Europe LLM-Optimized AI Accelerator Chip Consumption Value by Country (2021-2032)

8.3.3 Germany Market Size and Forecast (2021-2032)

8.3.4 France Market Size and Forecast (2021-2032)

8.3.5 United Kingdom Market Size and Forecast (2021-2032)

8.3.6 Russia Market Size and Forecast (2021-2032)

8.3.7 Italy Market Size and Forecast (2021-2032)

9 ASIA-PACIFIC

9.1 Asia-Pacific LLM-Optimized AI Accelerator Chip Sales Quantity by Delivery Form (2021-2032)

9.2 Asia-Pacific LLM-Optimized AI Accelerator Chip Sales Quantity by Application (2021-2032)

9.3 Asia-Pacific LLM-Optimized AI Accelerator Chip Market Size by Region

9.3.1 Asia-Pacific LLM-Optimized AI Accelerator Chip Sales Quantity by Region (2021-2032)

9.3.2 Asia-Pacific LLM-Optimized AI Accelerator Chip Consumption Value by Region (2021-2032)

9.3.3 China Market Size and Forecast (2021-2032)

9.3.4 Japan Market Size and Forecast (2021-2032)

9.3.5 South Korea Market Size and Forecast (2021-2032)

9.3.6 India Market Size and Forecast (2021-2032)

9.3.7 Southeast Asia Market Size and Forecast (2021-2032)

9.3.8 Australia Market Size and Forecast (2021-2032)

10 SOUTH AMERICA

10.1 South America LLM-Optimized AI Accelerator Chip Sales Quantity by Delivery Form (2021-2032)

10.2 South America LLM-Optimized AI Accelerator Chip Sales Quantity by Application (2021-2032)

10.3 South America LLM-Optimized AI Accelerator Chip Market Size by Country

10.3.1 South America LLM-Optimized AI Accelerator Chip Sales Quantity by Country (2021-2032)

10.3.2 South America LLM-Optimized AI Accelerator Chip Consumption Value by Country (2021-2032)

10.3.3 Brazil Market Size and Forecast (2021-2032)

10.3.4 Argentina Market Size and Forecast (2021-2032)

11 MIDDLE EAST & AFRICA

11.1 Middle East & Africa LLM-Optimized AI Accelerator Chip Sales Quantity by Delivery Form (2021-2032)

11.2 Middle East & Africa LLM-Optimized AI Accelerator Chip Sales Quantity by Application (2021-2032)

11.3 Middle East & Africa LLM-Optimized AI Accelerator Chip Market Size by Country

11.3.1 Middle East & Africa LLM-Optimized AI Accelerator Chip Sales Quantity by Country (2021-2032)

11.3.2 Middle East & Africa LLM-Optimized AI Accelerator Chip Consumption Value by Country (2021-2032)

11.3.3 Turkey Market Size and Forecast (2021-2032)

11.3.4 Egypt Market Size and Forecast (2021-2032)

11.3.5 Saudi Arabia Market Size and Forecast (2021-2032)

11.3.6 South Africa Market Size and Forecast (2021-2032)

12 MARKET DYNAMICS

12.1 LLM-Optimized AI Accelerator Chip Market Drivers

12.2 LLM-Optimized AI Accelerator Chip Market Restraints

12.3 LLM-Optimized AI Accelerator Chip Trends Analysis

12.4 Porters Five Forces Analysis

12.4.1 Threat of New Entrants

12.4.2 Bargaining Power of Suppliers

12.4.3 Bargaining Power of Buyers

12.4.4 Threat of Substitutes

12.4.5 Competitive Rivalry

13 RAW MATERIAL AND INDUSTRY CHAIN

13.1 Raw Material of LLM-Optimized AI Accelerator Chip and Key Manufacturers

13.2 Manufacturing Costs Percentage of LLM-Optimized AI Accelerator Chip

13.3 LLM-Optimized AI Accelerator Chip Production Process

13.4 Industry Value Chain Analysis

14 SHIPMENTS BY DISTRIBUTION CHANNEL

14.1 Sales Channel

14.1.1 Direct to End-User

14.1.2 Distributors

14.2 LLM-Optimized AI Accelerator Chip Typical Distributors

14.3 LLM-Optimized AI Accelerator Chip Typical Customers

15 RESEARCH FINDINGS AND CONCLUSION

16 APPENDIX

16.1 Methodology

16.2 Research Process and Data Source

16.3 Disclaimer

List Of Tables

LIST OF TABLES

Table 1. Global LLM-Optimized AI Accelerator Chip Consumption Value by Delivery Form, (USD Million), 2021 & 2025 & 2032

Table 2. Global LLM-Optimized AI Accelerator Chip Consumption Value by Compute Task, (USD Million), 2021 & 2025 & 2032

Table 3. Global LLM-Optimized AI Accelerator Chip Consumption Value by Memory System, (USD Million), 2021 & 2025 & 2032

Table 4. Global LLM-Optimized AI Accelerator Chip Consumption Value by Application, (USD Million), 2021 & 2025 & 2032

Table 5. NVIDIA Basic Information, Manufacturing Base and Competitors

Table 6. NVIDIA Major Business

Table 7. NVIDIA LLM-Optimized AI Accelerator Chip Product and Services

Table 8. NVIDIA LLM-Optimized AI Accelerator Chip Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 9. NVIDIA Recent Developments/Updates

Table 10. AMD Basic Information, Manufacturing Base and Competitors

Table 11. AMD Major Business

Table 12. AMD LLM-Optimized AI Accelerator Chip Product and Services

Table 13. AMD LLM-Optimized AI Accelerator Chip Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 14. AMD Recent Developments/Updates

Table 15. Intel Basic Information, Manufacturing Base and Competitors

Table 16. Intel Major Business

Table 17. Intel LLM-Optimized AI Accelerator Chip Product and Services

Table 18. Intel LLM-Optimized AI Accelerator Chip Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 19. Intel Recent Developments/Updates

Table 20. Amazon Web Services Basic Information, Manufacturing Base and Competitors

Table 21. Amazon Web Services Major Business

Table 22. Amazon Web Services LLM-Optimized AI Accelerator Chip Product and Services

Table 23. Amazon Web Services LLM-Optimized AI Accelerator Chip Sales Quantity

(Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 24. Amazon Web Services Recent Developments/Updates

Table 25. Google Basic Information, Manufacturing Base and Competitors

Table 26. Google Major Business

Table 27. Google LLM-Optimized AI Accelerator Chip Product and Services

Table 28. Google LLM-Optimized AI Accelerator Chip Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 29. Google Recent Developments/Updates

Table 30. Microsoft Basic Information, Manufacturing Base and Competitors

Table 31. Microsoft Major Business

Table 32. Microsoft LLM-Optimized AI Accelerator Chip Product and Services

Table 33. Microsoft LLM-Optimized AI Accelerator Chip Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 34. Microsoft Recent Developments/Updates

Table 35. Qualcomm Basic Information, Manufacturing Base and Competitors

Table 36. Qualcomm Major Business

Table 37. Qualcomm LLM-Optimized AI Accelerator Chip Product and Services

Table 38. Qualcomm LLM-Optimized AI Accelerator Chip Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 39. Qualcomm Recent Developments/Updates

Table 40. Cerebras Basic Information, Manufacturing Base and Competitors

Table 41. Cerebras Major Business

Table 42. Cerebras LLM-Optimized AI Accelerator Chip Product and Services

Table 43. Cerebras LLM-Optimized AI Accelerator Chip Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 44. Cerebras Recent Developments/Updates

Table 45. Groq Basic Information, Manufacturing Base and Competitors

Table 46. Groq Major Business

Table 47. Groq LLM-Optimized AI Accelerator Chip Product and Services

Table 48. Groq LLM-Optimized AI Accelerator Chip Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 49. Groq Recent Developments/Updates

Table 50. SambaNova Basic Information, Manufacturing Base and Competitors

Table 51. SambaNova Major Business

Table 52. SambaNova LLM-Optimized AI Accelerator Chip Product and Services

Table 53. SambaNova LLM-Optimized AI Accelerator Chip Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 54. SambaNova Recent Developments/Updates

Table 55. Tenstorrent Basic Information, Manufacturing Base and Competitors

Table 56. Tenstorrent Major Business

Table 57. Tenstorrent LLM-Optimized AI Accelerator Chip Product and Services

Table 58. Tenstorrent LLM-Optimized AI Accelerator Chip Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 59. Tenstorrent Recent Developments/Updates

Table 60. Huawei Basic Information, Manufacturing Base and Competitors

Table 61. Huawei Major Business

Table 62. Huawei LLM-Optimized AI Accelerator Chip Product and Services

Table 63. Huawei LLM-Optimized AI Accelerator Chip Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 64. Huawei Recent Developments/Updates

Table 65. Kunlunxin Basic Information, Manufacturing Base and Competitors

Table 66. Kunlunxin Major Business

Table 67. Kunlunxin LLM-Optimized AI Accelerator Chip Product and Services

Table 68. Kunlunxin LLM-Optimized AI Accelerator Chip Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 69. Kunlunxin Recent Developments/Updates

Table 70. Cambricon Basic Information, Manufacturing Base and Competitors

Table 71. Cambricon Major Business

Table 72. Cambricon LLM-Optimized AI Accelerator Chip Product and Services

Table 73. Cambricon LLM-Optimized AI Accelerator Chip Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 74. Cambricon Recent Developments/Updates

Table 75. Moore Threads Basic Information, Manufacturing Base and Competitors

Table 76. Moore Threads Major Business

Table 77. Moore Threads LLM-Optimized AI Accelerator Chip Product and Services

Table 78. Moore Threads LLM-Optimized AI Accelerator Chip Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market

Share (2021-2026)

Table 79. Moore Threads Recent Developments/Updates

Table 80. MetaX Basic Information, Manufacturing Base and Competitors

Table 81. MetaX Major Business

Table 82. MetaX LLM-Optimized AI Accelerator Chip Product and Services

Table 83. MetaX LLM-Optimized AI Accelerator Chip Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 84. MetaX Recent Developments/Updates

Table 85. Biren Technology Basic Information, Manufacturing Base and Competitors

Table 86. Biren Technology Major Business

Table 87. Biren Technology LLM-Optimized AI Accelerator Chip Product and Services

Table 88. Biren Technology LLM-Optimized AI Accelerator Chip Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 89. Biren Technology Recent Developments/Updates

Table 90. Rebellions Basic Information, Manufacturing Base and Competitors

Table 91. Rebellions Major Business

Table 92. Rebellions LLM-Optimized AI Accelerator Chip Product and Services

Table 93. Rebellions LLM-Optimized AI Accelerator Chip Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 94. Rebellions Recent Developments/Updates

Table 95. FuriosaAI Basic Information, Manufacturing Base and Competitors

Table 96. FuriosaAI Major Business

Table 97. FuriosaAI LLM-Optimized AI Accelerator Chip Product and Services

Table 98. FuriosaAI LLM-Optimized AI Accelerator Chip Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 99. FuriosaAI Recent Developments/Updates

Table 100. DEEPX Basic Information, Manufacturing Base and Competitors

Table 101. DEEPX Major Business

Table 102. DEEPX LLM-Optimized AI Accelerator Chip Product and Services

Table 103. DEEPX LLM-Optimized AI Accelerator Chip Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 104. DEEPX Recent Developments/Updates

Table 105. Preferred Networks Basic Information, Manufacturing Base and Competitors

Table 106. Preferred Networks Major Business

Table 107. Preferred Networks LLM-Optimized AI Accelerator Chip Product and Services

Table 108. Preferred Networks LLM-Optimized AI Accelerator Chip Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 109. Preferred Networks Recent Developments/Updates

Table 110. EdgeCortex Basic Information, Manufacturing Base and Competitors

Table 111. EdgeCortex Major Business

Table 112. EdgeCortex LLM-Optimized AI Accelerator Chip Product and Services

Table 113. EdgeCortex LLM-Optimized AI Accelerator Chip Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 114. EdgeCortex Recent Developments/Updates

Table 115. Global LLM-Optimized AI Accelerator Chip Sales Quantity by Manufacturer (2021-2026) & (Million Units)

Table 116. Global LLM-Optimized AI Accelerator Chip Revenue by Manufacturer (2021-2026) & (USD Million)

Table 117. Global LLM-Optimized AI Accelerator Chip Average Price by Manufacturer (2021-2026) & (US\$/Unit)

Table 118. Market Position of Manufacturers in LLM-Optimized AI Accelerator Chip, (Tier 1, Tier 2, and Tier 3), Based on Revenue in 2025

Table 119. Head Office and LLM-Optimized AI Accelerator Chip Production Site of Key Manufacturer

Table 120. LLM-Optimized AI Accelerator Chip Market: Company Product Type Footprint

Table 121. LLM-Optimized AI Accelerator Chip Market: Company Product Application Footprint

Table 122. LLM-Optimized AI Accelerator Chip New Market Entrants and Barriers to Market Entry

Table 123. LLM-Optimized AI Accelerator Chip Mergers, Acquisition, Agreements, and Collaborations

Table 124. Global LLM-Optimized AI Accelerator Chip Consumption Value by Region (2021-2025-2032) & (USD Million) & CAGR

Table 125. Global LLM-Optimized AI Accelerator Chip Sales Quantity by Region (2021-2026) & (Million Units)

Table 126. Global LLM-Optimized AI Accelerator Chip Sales Quantity by Region (2027-2032) & (Million Units)

Table 127. Global LLM-Optimized AI Accelerator Chip Consumption Value by Region (2021-2026) & (USD Million)

Table 128. Global LLM-Optimized AI Accelerator Chip Consumption Value by Region (2027-2032) & (USD Million)

Table 129. Global LLM-Optimized AI Accelerator Chip Average Price by Region (2021-2026) & (US\$/Unit)

Table 130. Global LLM-Optimized AI Accelerator Chip Average Price by Region (2027-2032) & (US\$/Unit)

Table 131. Global LLM-Optimized AI Accelerator Chip Sales Quantity by Delivery Form (2021-2026) & (Million Units)

Table 132. Global LLM-Optimized AI Accelerator Chip Sales Quantity by Delivery Form (2027-2032) & (Million Units)

Table 133. Global LLM-Optimized AI Accelerator Chip Consumption Value by Delivery Form (2021-2026) & (USD Million)

Table 134. Global LLM-Optimized AI Accelerator Chip Consumption Value by Delivery Form (2027-2032) & (USD Million)

Table 135. Global LLM-Optimized AI Accelerator Chip Average Price by Delivery Form (2021-2026) & (US\$/Unit)

Table 136. Global LLM-Optimized AI Accelerator Chip Average Price by Delivery Form (2027-2032) & (US\$/Unit)

Table 137. Global LLM-Optimized AI Accelerator Chip Sales Quantity by Application (2021-2026) & (Million Units)

Table 138. Global LLM-Optimized AI Accelerator Chip Sales Quantity by Application (2027-2032) & (Million Units)

Table 139. Global LLM-Optimized AI Accelerator Chip Consumption Value by Application (2021-2026) & (USD Million)

Table 140. Global LLM-Optimized AI Accelerator Chip Consumption Value by Application (2027-2032) & (USD Million)

Table 141. Global LLM-Optimized AI Accelerator Chip Average Price by Application (2021-2026) & (US\$/Unit)

Table 142. Global LLM-Optimized AI Accelerator Chip Average Price by Application (2027-2032) & (US\$/Unit)

Table 143. North America LLM-Optimized AI Accelerator Chip Sales Quantity by Delivery Form (2021-2026) & (Million Units)

Table 144. North America LLM-Optimized AI Accelerator Chip Sales Quantity by Delivery Form (2027-2032) & (Million Units)

Table 145. North America LLM-Optimized AI Accelerator Chip Sales Quantity by Application (2021-2026) & (Million Units)

Table 146. North America LLM-Optimized AI Accelerator Chip Sales Quantity by Application (2027-2032) & (Million Units)

Table 147. North America LLM-Optimized AI Accelerator Chip Sales Quantity by

Country (2021-2026) & (Million Units)

Table 148. North America LLM-Optimized AI Accelerator Chip Sales Quantity by Country (2027-2032) & (Million Units)

Table 149. North America LLM-Optimized AI Accelerator Chip Consumption Value by Country (2021-2026) & (USD Million)

Table 150. North America LLM-Optimized AI Accelerator Chip Consumption Value by Country (2027-2032) & (USD Million)

Table 151. Europe LLM-Optimized AI Accelerator Chip Sales Quantity by Delivery Form (2021-2026) & (Million Units)

Table 152. Europe LLM-Optimized AI Accelerator Chip Sales Quantity by Delivery Form (2027-2032) & (Million Units)

Table 153. Europe LLM-Optimized AI Accelerator Chip Sales Quantity by Application (2021-2026) & (Million Units)

Table 154. Europe LLM-Optimized AI Accelerator Chip Sales Quantity by Application (2027-2032) & (Million Units)

Table 155. Europe LLM-Optimized AI Accelerator Chip Sales Quantity by Country (2021-2026) & (Million Units)

Table 156. Europe LLM-Optimized AI Accelerator Chip Sales Quantity by Country (2027-2032) & (Million Units)

Table 157. Europe LLM-Optimized AI Accelerator Chip Consumption Value by Country (2021-2026) & (USD Million)

Table 158. Europe LLM-Optimized AI Accelerator Chip Consumption Value by Country (2027-2032) & (USD Million)

Table 159. Asia-Pacific LLM-Optimized AI Accelerator Chip Sales Quantity by Delivery Form (2021-2026) & (Million Units)

Table 160. Asia-Pacific LLM-Optimized AI Accelerator Chip Sales Quantity by Delivery Form (2027-2032) & (Million Units)

Table 161. Asia-Pacific LLM-Optimized AI Accelerator Chip Sales Quantity by Application (2021-2026) & (Million Units)

Table 162. Asia-Pacific LLM-Optimized AI Accelerator Chip Sales Quantity by Application (2027-2032) & (Million Units)

Table 163. Asia-Pacific LLM-Optimized AI Accelerator Chip Sales Quantity by Region (2021-2026) & (Million Units)

Table 164. Asia-Pacific LLM-Optimized AI Accelerator Chip Sales Quantity by Region (2027-2032) & (Million Units)

Table 165. Asia-Pacific LLM-Optimized AI Accelerator Chip Consumption Value by Region (2021-2026) & (USD Million)

Table 166. Asia-Pacific LLM-Optimized AI Accelerator Chip Consumption Value by Region (2027-2032) & (USD Million)

Table 167. South America LLM-Optimized AI Accelerator Chip Sales Quantity by Delivery Form (2021-2026) & (Million Units)

Table 168. South America LLM-Optimized AI Accelerator Chip Sales Quantity by Delivery Form (2027-2032) & (Million Units)

Table 169. South America LLM-Optimized AI Accelerator Chip Sales Quantity by Application (2021-2026) & (Million Units)

Table 170. South America LLM-Optimized AI Accelerator Chip Sales Quantity by Application (2027-2032) & (Million Units)

Table 171. South America LLM-Optimized AI Accelerator Chip Sales Quantity by Country (2021-2026) & (Million Units)

Table 172. South America LLM-Optimized AI Accelerator Chip Sales Quantity by Country (2027-2032) & (Million Units)

Table 173. South America LLM-Optimized AI Accelerator Chip Consumption Value by Country (2021-2026) & (USD Million)

Table 174. South America LLM-Optimized AI Accelerator Chip Consumption Value by Country (2027-2032) & (USD Million)

Table 175. Middle East & Africa LLM-Optimized AI Accelerator Chip Sales Quantity by Delivery Form (2021-2026) & (Million Units)

Table 176. Middle East & Africa LLM-Optimized AI Accelerator Chip Sales Quantity by Delivery Form (2027-2032) & (Million Units)

Table 177. Middle East & Africa LLM-Optimized AI Accelerator Chip Sales Quantity by Application (2021-2026) & (Million Units)

Table 178. Middle East & Africa LLM-Optimized AI Accelerator Chip Sales Quantity by Application (2027-2032) & (Million Units)

Table 179. Middle East & Africa LLM-Optimized AI Accelerator Chip Sales Quantity by Country (2021-2026) & (Million Units)

Table 180. Middle East & Africa LLM-Optimized AI Accelerator Chip Sales Quantity by Country (2027-2032) & (Million Units)

Table 181. Middle East & Africa LLM-Optimized AI Accelerator Chip Consumption Value by Country (2021-2026) & (USD Million)

Table 182. Middle East & Africa LLM-Optimized AI Accelerator Chip Consumption Value by Country (2027-2032) & (USD Million)

Table 183. LLM-Optimized AI Accelerator Chip Raw Material

Table 184. Key Manufacturers of LLM-Optimized AI Accelerator Chip Raw Materials

Table 185. LLM-Optimized AI Accelerator Chip Typical Distributors

Table 186. LLM-Optimized AI Accelerator Chip Typical Customers

List Of Figures

LIST OF FIGURES

- Figure 1. LLM-Optimized AI Accelerator Chip Picture
- Figure 2. Global LLM-Optimized AI Accelerator Chip Revenue by Delivery Form, (USD Million), 2021 & 2025 & 2032
- Figure 3. Global LLM-Optimized AI Accelerator Chip Revenue Market Share by Delivery Form in 2025
- Figure 4. Bare Chip Examples
- Figure 5. Accelerator Card Examples
- Figure 6. Server Module Examples
- Figure 7. Full System Examples
- Figure 8. Global LLM-Optimized AI Accelerator Chip Revenue by Compute Task, (USD Million), 2021 & 2025 & 2032
- Figure 9. Global LLM-Optimized AI Accelerator Chip Revenue Market Share by Compute Task in 2025
- Figure 10. Training LLM-Optimized AI Accelerator Chip Examples
- Figure 11. Fine-Tuning LLM-Optimized AI Accelerator Chip Examples
- Figure 12. Inference LLM-Optimized AI Accelerator Chip Examples
- Figure 13. Training and Inference LLM-Optimized AI Accelerator Chip Examples
- Figure 14. Global LLM-Optimized AI Accelerator Chip Revenue by Memory System, (USD Million), 2021 & 2025 & 2032
- Figure 15. Global LLM-Optimized AI Accelerator Chip Revenue Market Share by Memory System in 2025
- Figure 16. HBM Examples
- Figure 17. GDDR Examples
- Figure 18. Global LLM-Optimized AI Accelerator Chip Consumption Value by Application, (USD Million), 2021 & 2025 & 2032
- Figure 19. Global LLM-Optimized AI Accelerator Chip Revenue Market Share by Application in 2025
- Figure 20. Foundation Model Pretraining Examples
- Figure 21. Industry Model Fine-Tuning Examples
- Figure 22. Online Generative Inference Examples
- Figure 23. Retrieval-Augmented Inference Examples
- Figure 24. Agentic Inference Examples
- Figure 25. Local Edge Inference Examples
- Figure 26. Global LLM-Optimized AI Accelerator Chip Consumption Value, (USD Million): 2021 & 2025 & 2032

Figure 27. Global LLM-Optimized AI Accelerator Chip Consumption Value and Forecast (2021-2032) & (USD Million)

Figure 28. Global LLM-Optimized AI Accelerator Chip Sales Quantity (2021-2032) & (Million Units)

Figure 29. Global LLM-Optimized AI Accelerator Chip Price (2021-2032) & (US\$/Unit)

Figure 30. Global LLM-Optimized AI Accelerator Chip Sales Quantity Market Share by Manufacturer in 2025

Figure 31. Global LLM-Optimized AI Accelerator Chip Revenue Market Share by Manufacturer in 2025

Figure 32. Producer Shipments of LLM-Optimized AI Accelerator Chip by Manufacturer Sales (\$MM) and Market Share (%): 2025

Figure 33. Top 3 LLM-Optimized AI Accelerator Chip Manufacturer (Revenue) Market Share in 2025

Figure 34. Top 6 LLM-Optimized AI Accelerator Chip Manufacturer (Revenue) Market Share in 2025

Figure 35. Global LLM-Optimized AI Accelerator Chip Sales Quantity Market Share by Region (2021-2032)

Figure 36. Global LLM-Optimized AI Accelerator Chip Consumption Value Market Share by Region (2021-2032)

Figure 37. North America LLM-Optimized AI Accelerator Chip Consumption Value (2021-2032) & (USD Million)

Figure 38. Europe LLM-Optimized AI Accelerator Chip Consumption Value (2021-2032) & (USD Million)

Figure 39. Asia-Pacific LLM-Optimized AI Accelerator Chip Consumption Value (2021-2032) & (USD Million)

Figure 40. South America LLM-Optimized AI Accelerator Chip Consumption Value (2021-2032) & (USD Million)

Figure 41. Middle East & Africa LLM-Optimized AI Accelerator Chip Consumption Value (2021-2032) & (USD Million)

Figure 42. Global LLM-Optimized AI Accelerator Chip Sales Quantity Market Share by Delivery Form (2021-2032)

Figure 43. Global LLM-Optimized AI Accelerator Chip Consumption Value Market Share by Delivery Form (2021-2032)

Figure 44. Global LLM-Optimized AI Accelerator Chip Average Price by Delivery Form (2021-2032) & (US\$/Unit)

Figure 45. Global LLM-Optimized AI Accelerator Chip Sales Quantity Market Share by Application (2021-2032)

Figure 46. Global LLM-Optimized AI Accelerator Chip Revenue Market Share by Application (2021-2032)

Figure 47. Global LLM-Optimized AI Accelerator Chip Average Price by Application (2021-2032) & (US\$/Unit)

Figure 48. North America LLM-Optimized AI Accelerator Chip Sales Quantity Market Share by Delivery Form (2021-2032)

Figure 49. North America LLM-Optimized AI Accelerator Chip Sales Quantity Market Share by Application (2021-2032)

Figure 50. North America LLM-Optimized AI Accelerator Chip Sales Quantity Market Share by Country (2021-2032)

Figure 51. North America LLM-Optimized AI Accelerator Chip Consumption Value Market Share by Country (2021-2032)

Figure 52. United States LLM-Optimized AI Accelerator Chip Consumption Value (2021-2032) & (USD Million)

Figure 53. Canada LLM-Optimized AI Accelerator Chip Consumption Value (2021-2032) & (USD Million)

Figure 54. Mexico LLM-Optimized AI Accelerator Chip Consumption Value (2021-2032) & (USD Million)

Figure 55. Europe LLM-Optimized AI Accelerator Chip Sales Quantity Market Share by Delivery Form (2021-2032)

Figure 56. Europe LLM-Optimized AI Accelerator Chip Sales Quantity Market Share by Application (2021-2032)

Figure 57. Europe LLM-Optimized AI Accelerator Chip Sales Quantity Market Share by Country (2021-2032)

Figure 58. Europe LLM-Optimized AI Accelerator Chip Consumption Value Market Share by Country (2021-2032)

Figure 59. Germany LLM-Optimized AI Accelerator Chip Consumption Value (2021-2032) & (USD Million)

Figure 60. France LLM-Optimized AI Accelerator Chip Consumption Value (2021-2032) & (USD Million)

Figure 61. United Kingdom LLM-Optimized AI Accelerator Chip Consumption Value (2021-2032) & (USD Million)

Figure 62. Russia LLM-Optimized AI Accelerator Chip Consumption Value (2021-2032) & (USD Million)

Figure 63. Italy LLM-Optimized AI Accelerator Chip Consumption Value (2021-2032) & (USD Million)

Figure 64. Asia-Pacific LLM-Optimized AI Accelerator Chip Sales Quantity Market Share by Delivery Form (2021-2032)

Figure 65. Asia-Pacific LLM-Optimized AI Accelerator Chip Sales Quantity Market Share by Application (2021-2032)

Figure 66. Asia-Pacific LLM-Optimized AI Accelerator Chip Sales Quantity Market

Share by Region (2021-2032)

Figure 67. Asia-Pacific LLM-Optimized AI Accelerator Chip Consumption Value Market Share by Region (2021-2032)

Figure 68. China LLM-Optimized AI Accelerator Chip Consumption Value (2021-2032) & (USD Million)

Figure 69. Japan LLM-Optimized AI Accelerator Chip Consumption Value (2021-2032) & (USD Million)

Figure 70. South Korea LLM-Optimized AI Accelerator Chip Consumption Value (2021-2032) & (USD Million)

Figure 71. India LLM-Optimized AI Accelerator Chip Consumption Value (2021-2032) & (USD Million)

Figure 72. Southeast Asia LLM-Optimized AI Accelerator Chip Consumption Value (2021-2032) & (USD Million)

Figure 73. Australia LLM-Optimized AI Accelerator Chip Consumption Value (2021-2032) & (USD Million)

Figure 74. South America LLM-Optimized AI Accelerator Chip Sales Quantity Market Share by Delivery Form (2021-2032)

Figure 75. South America LLM-Optimized AI Accelerator Chip Sales Quantity Market Share by Application (2021-2032)

Figure 76. South America LLM-Optimized AI Accelerator Chip Sales Quantity Market Share by Country (2021-2032)

Figure 77. South America LLM-Optimized AI Accelerator Chip Consumption Value Market Share by Country (2021-2032)

Figure 78. Brazil LLM-Optimized AI Accelerator Chip Consumption Value (2021-2032) & (USD Million)

Figure 79. Argentina LLM-Optimized AI Accelerator Chip Consumption Value (2021-2032) & (USD Million)

Figure 80. Middle East & Africa LLM-Optimized AI Accelerator Chip Sales Quantity Market Share by Delivery Form (2021-2032)

Figure 81. Middle East & Africa LLM-Optimized AI Accelerator Chip Sales Quantity Market Share by Application (2021-2032)

Figure 82. Middle East & Africa LLM-Optimized AI Accelerator Chip Sales Quantity Market Share by Country (2021-2032)

Figure 83. Middle East & Africa LLM-Optimized AI Accelerator Chip Consumption Value Market Share by Country (2021-2032)

Figure 84. Turkey LLM-Optimized AI Accelerator Chip Consumption Value (2021-2032) & (USD Million)

Figure 85. Egypt LLM-Optimized AI Accelerator Chip Consumption Value (2021-2032) & (USD Million)

Figure 86. Saudi Arabia LLM-Optimized AI Accelerator Chip Consumption Value (2021-2032) & (USD Million)

Figure 87. South Africa LLM-Optimized AI Accelerator Chip Consumption Value (2021-2032) & (USD Million)

Figure 88. LLM-Optimized AI Accelerator Chip Market Drivers

Figure 89. LLM-Optimized AI Accelerator Chip Market Restraints

Figure 90. LLM-Optimized AI Accelerator Chip Market Trends

Figure 91. Porters Five Forces Analysis

Figure 92. Manufacturing Cost Structure Analysis of LLM-Optimized AI Accelerator Chip in 2025

Figure 93. Manufacturing Process Analysis of LLM-Optimized AI Accelerator Chip

Figure 94. LLM-Optimized AI Accelerator Chip Industrial Chain

Figure 95. Sales Channel: Direct to End-User vs Distributors

Figure 96. Direct Channel Pros & Cons

Figure 97. Indirect Channel Pros & Cons

Figure 98. Methodology

Figure 99. Research Process and Data Source

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