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Global Integrated Storage and Computing Chip Market 2026 by Company, Regions, Type and Application, Forecast to 2032

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Abstracts

According to our (Global Info Research) latest study, the global Integrated Storage and Computing Chip market size was valued at US$ 238 million in 2025 and is forecast to a readjusted size of US$ 42322 million by 2032 with a CAGR of 109.7% during review period.

Integrated Storage and Computing Chip, also known as In-Memory Computing, is an advanced chip architecture that integrates computation directly within memory cells or memory arrays. By performing operations such as multiply-accumulate, logic, or vector calculations at the data storage location, CIM significantly reduces frequent data movement between memory and processors, thereby overcoming the ?memory wall? and ?power wall? limitations of the traditional von Neumann architecture. These chips are typically implemented using SRAM, DRAM, Flash, or emerging non-volatile memories such as ReRAM, PCM, and MRAM, and are particularly well suited for AI inference, edge computing, and energy-efficient parallel processing, offering substantial improvements in energy efficiency, latency reduction, and overall system performance.

Integrated Storage and Computing Chip market is emerging as a critical segment within the next generation of AI and data-centric semiconductor technologies, driven by the growing limitations of traditional von Neumann architectures. IMC chips integrate computation directly within or near memory arrays, significantly reducing data movement between memory and processors, which in turn lowers latency and power consumption while improving overall system efficiency. This architecture is particularly well-suited for data-intensive workloads such as artificial intelligence, machine learning inference and training, edge computing, high-performance computing (HPC), and real-time analytics.

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Market growth is fueled by the rapid expansion of AI applications, especially in neural networks, recommendation systems, computer vision, and natural language processing, where memory bandwidth and energy efficiency are key bottlenecks. Both digital IMC (based on SRAM, DRAM, or emerging memory) and analog IMC (often using RRAM, PCM, or MRAM) approaches are being actively developed, with analog IMC attracting attention for its high parallelism and energy efficiency in matrix-vector multiplication tasks. However, challenges remain in areas such as precision control, reliability, process variation, and large-scale manufacturability.

From a market structure perspective, the IMC chip ecosystem includes emerging startups, leading semiconductor vendors, foundries, and research institutes, with strong participation from the United States, China, Europe, Japan, and South Korea. In the near to mid term, adoption is expected to accelerate in edge AI and inference-oriented applications, while broader deployment in data centers and large-scale AI training will depend on continued progress in accuracy, software ecosystems, and standardization. Overall, the In-Memory Computing chip market is positioned for long-term growth as AI workloads continue to scale and energy efficiency becomes a defining constraint in semiconductor system design.

This report is a detailed and comprehensive analysis for global Integrated Storage and Computing Chip market. Both quantitative and qualitative analyses are presented by company, by region & country, by Type and by Application. As the market is constantly changing, this report explores the competition, supply and demand trends, as well as key factors that contribute to its changing demands across many markets. Company profiles and product examples of selected competitors, along with market share estimates of some of the selected leaders for the year 2025, are provided.

Key Features:

Global Integrated Storage and Computing Chip market size and forecasts, in consumption value ($ Million), 2021-2032

Global Integrated Storage and Computing Chip market size and forecasts by region and country, in consumption value ($ Million), 2021-2032

Global Integrated Storage and Computing Chip market size and forecasts, by Type and by Application, in consumption value ($ Million), 2021-2032

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Global Integrated Storage and Computing Chip market shares of main players, in revenue ($ Million), 2021-2026

The Primary Objectives in This Report Are:

To determine the size of the total market opportunity of global and key countries

To assess the growth potential for Integrated Storage and Computing Chip

To forecast future growth in each product and end-use market

To assess competitive factors affecting the marketplace

This report profiles key players in the global Integrated Storage and Computing Chip market based on the following parameters - company overview, revenue, gross margin, product portfolio, geographical presence, and key developments. Key companies covered as a part of this study include Samsung, SK Hynix, Syntiant, Myhtic, D-Matrix, Hangzhou Zhicun (Witmem) Technology, Beijing Pingxin Technology, Shenzhen Reexen Technology, Beijing Houmo Technology, Graphcore, etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals.

Market segmentation

Integrated Storage and Computing Chip market is split by Type and by Application. For the period 2021-2032, the growth among segments provides accurate calculations and forecasts for Consumption Value by Type and by Application. This analysis can help you expand your business by targeting qualified niche markets.

Market segment by Type

DRAM-PIM

SRAM-PIM

Others

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Market segment by System Architecture

True In-Memory Computing

Near-Memory Computing

Market segment by Computing Method

Digital In-Memory Computing

Analog In-Memory Computing

Market segment by Application

Small Computing Power

Large Computing Power

Market segment by players, this report covers

Samsung

SK Hynix

Syntiant

Myhtic

D-Matrix

Hangzhou Zhicun (Witmem) Technology

Beijing Pingxin Technology

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Shenzhen Reexen Technology

Beijing Houmo Technology

Graphcore

AistarTek

Suzhou Yizhu Intelligent Technology

EnCharge AI

Axelera AI

Market segment by regions, regional analysis covers

North America (United States, Canada and Mexico)

Europe (Germany, France, UK, Russia, Italy and Rest of Europe)

Asia-Pacific (China, Japan, South Korea, India, Southeast Asia and Rest of Asia-Pacific)

South America (Brazil, Rest of South America)

Middle East & Africa (Turkey, Saudi Arabia, UAE, Rest of Middle East & Africa)

The content of the study subjects, includes a total of 13 chapters:

Chapter 1, to describe Integrated Storage and Computing Chip product scope, market overview, market estimation caveats and base year.

Chapter 2, to profile the top players of Integrated Storage and Computing Chip, with revenue, gross margin, and global market share of Integrated Storage and Computing Chip from 2021 to 2026.

Chapter 3, the Integrated Storage and Computing Chip competitive situation, revenue,

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and global market share of top players are analyzed emphatically by landscape contrast.

Chapter 4 and 5, to segment the market size by Type and by Application, with consumption value and growth rate by Type, by Application, from 2021 to 2032.

Chapter 6, 7, 8, 9, and 10, to break the market size data at the country level, with revenue and market share for key countries in the world, from 2021 to 2026.and Integrated Storage and Computing Chip market forecast, by regions, by Type and by Application, with consumption value, from 2027 to 2032.

Chapter 11, market dynamics, drivers, restraints, trends, Porters Five Forces analysis.

Chapter 12, the key raw materials and key suppliers, and industry chain of Integrated Storage and Computing Chip.

Chapter 13, to describe Integrated Storage and Computing Chip research findings and conclusion.

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