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Global Integrated Storage and Computing Chip Supply, Demand and Key Producers, 2026-2032

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Abstracts

The global Integrated Storage and Computing Chip market size is expected to reach $ 42322 million by 2032, rising at a market growth of 109.7% CAGR during the forecast period (2026-2032).
Integrated Storage and Computing Chip, also known as In-Memory Computing, is an advanced chip architecture that integrates computation directly within memory cells or memory arrays. By performing operations such as multiply-accumulate, logic, or vector calculations at the data storage location, CIM significantly reduces frequent data movement between memory and processors, thereby overcoming the ?memory wall? and ?power wall? limitations of the traditional von Neumann architecture. These chips are typically implemented using SRAM, DRAM, Flash, or emerging non-volatile memories such as ReRAM, PCM, and MRAM, and are particularly well suited for AI inference, edge computing, and energy-efficient parallel processing, offering substantial improvements in energy efficiency, latency reduction, and overall system performance.
Integrated Storage and Computing Chip market is emerging as a critical segment within the next generation of AI and data-centric semiconductor technologies, driven by the growing limitations of traditional von Neumann architectures. IMC chips integrate computation directly within or near memory arrays, significantly reducing data movement between memory and processors, which in turn lowers latency and power consumption while improving overall system efficiency. This architecture is particularly well-suited for data-intensive workloads such as artificial intelligence, machine learning inference and training, edge computing, high-performance computing (HPC), and real-time analytics.
Market growth is fueled by the rapid expansion of AI applications, especially in neural networks, recommendation systems, computer vision, and natural language processing, where memory bandwidth and energy efficiency are key bottlenecks. Both digital IMC (based on SRAM, DRAM, or emerging memory) and analog IMC (often using RRAM,

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PCM, or MRAM) approaches are being actively developed, with analog IMC attracting attention for its high parallelism and energy efficiency in matrix-vector multiplication tasks. However, challenges remain in areas such as precision control, reliability, process variation, and large-scale manufacturability.

From a market structure perspective, the IMC chip ecosystem includes emerging startups, leading semiconductor vendors, foundries, and research institutes, with strong participation from the United States, China, Europe, Japan, and South Korea. In the near to mid term, adoption is expected to accelerate in edge AI and inference-oriented applications, while broader deployment in data centers and large-scale AI training will depend on continued progress in accuracy, software ecosystems, and standardization. Overall, the In-Memory Computing chip market is positioned for long-term growth as AI workloads continue to scale and energy efficiency becomes a defining constraint in semiconductor system design.

This report studies the global Integrated Storage and Computing Chip demand, key companies, and key regions.

This report is a detailed and comprehensive analysis of the world market for Integrated Storage and Computing Chip, and provides market size (US$ million) and Year-over-Year (YoY) growth, considering 2025 as the base year. This report explores demand trends and competition, as well as details the characteristics of Integrated Storage and Computing Chip that contribute to its increasing demand across many markets.

Highlights and key features of the study

Global Integrated Storage and Computing Chip total market, 2021-2032, (USD Million)

Global Integrated Storage and Computing Chip total market by region & country, CAGR, 2021-2032, (USD Million)

U.S. VS China: Integrated Storage and Computing Chip total market, key domestic companies, and share, (USD Million)

Global Integrated Storage and Computing Chip revenue by player, revenue and market share 2021-2026, (USD Million)

Global Integrated Storage and Computing Chip total market by Type, CAGR, 2021-2032, (USD Million)

Global Integrated Storage and Computing Chip total market by Application, CAGR, 2021-2032, (USD Million)

This report profiles major players in the global Integrated Storage and Computing Chip market based on the following parameters - company overview, revenue, gross margin, product portfolio, geographical presence, and key developments. Key companies covered as a part of this study include Samsung, SK Hynix, Syntiant, Myhtic, D-Matrix, Hangzhou Zhicun (Witmem) Technology, Beijing Pingxin Technology, Shenzhen Reexen Technology, Beijing Houmo Technology, Graphcore, etc.

This report also provides key insights about market drivers, restraints, opportunities,

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new product launches or approvals.
Stakeholders would have ease in decision-making through various strategy matrices used in analyzing the world Integrated Storage and Computing Chip market
Detailed Segmentation:
Each section contains quantitative market data including market by value (US$ Millions), by player, by regions, by Type, and by Application. Data is given for the years 2021-2032 by year with 2025 as the base year, 2026 as the estimate year, and 2027-2032 as the forecast year.
Global Integrated Storage and Computing Chip Market, By Region:

United States

China

Europe

Japan

South Korea

ASEAN

India

Rest of World

Global Integrated Storage and Computing Chip Market, Segmentation by Type:

DRAM-PIM

SRAM-PIM

Others

Global Integrated Storage and Computing Chip Market, Segmentation by System Architecture:

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True In-Memory Computing

Near-Memory Computing

Global Integrated Storage and Computing Chip Market, Segmentation by Computing Method:

Digital In-Memory Computing

Analog In-Memory Computing

Global Integrated Storage and Computing Chip Market, Segmentation by Application:

Small Computing Power

Large Computing Power

Companies Profiled:

Samsung

SK Hynix

Syntiant

Myhtic

D-Matrix

Hangzhou Zhicun (Witmem) Technology

Beijing Pingxin Technology

Shenzhen Reexen Technology

Beijing Houmo Technology

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Graphcore

AistarTek

Suzhou Yizhu Intelligent Technology

EnCharge AI

Axelera AI

Key Questions Answered

1. How big is the global Integrated Storage and Computing Chip market?
2. What is the demand of the global Integrated Storage and Computing Chip market?
3. What is the year over year growth of the global Integrated Storage and Computing Chip market?
4. What is the total value of the global Integrated Storage and Computing Chip market?
5. Who are the Major Players in the global Integrated Storage and Computing Chip market?
6. What are the growth factors driving the market demand?

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