

Global GaAs Polished Wafer Market 2026 by Manufacturers, Regions, Type and Application, Forecast to 2032

<https://marketpublishers.com/r/GD7BA0F03AF9EN.html>

Date: May 2026

Pages: 117

Price: US\$ 3,480.00 (Single User License)

ID: GD7BA0F03AF9EN

Abstracts

According to our (Global Info Research) latest study, the global GaAs Polished Wafer market size was valued at US\$ 1358 million in 2025 and is forecast to a readjusted size of US\$ 2843 million by 2032 with a CAGR of 11.1% during review period.

Gallium arsenide polished wafers are key second generation compound semiconductor substrates used for epitaxial growth and device manufacturing. Their core role is to provide a low defect, low contamination, high flatness, and highly consistent material foundation for downstream epitaxy, etching, deposition, and chip fabrication when silicon substrates cannot meet requirements for high frequency, high speed, high photoelectric conversion efficiency, low noise, and high temperature performance. Official product pages show that these wafers typically include both semi insulating and semiconducting types, cover sizes from 2 inches to 8 inches, and are produced through crystal growth routes such as VGF, LEC, and VB. Delivery forms extend from single side polished and double side polished wafers to EPI Ready grades, with customization available for orientation, off cut, thickness, dopant type, and packaging. Downstream applications are concentrated in RF front end devices, microwave and millimeter wave devices, HBT, pHEMT, VCSEL, lasers, LEDs, optical communications, and high efficiency solar cells, while major customers include epitaxy houses, IDMs, compound semiconductor device makers, and research institutions. The common business model starts with standard substrate sales and then adds customized parameters, reclaim and repolishing, epitaxy collaboration, and technical support services. Some suppliers also operate across crystals, substrates, epitaxy, and recycling related services to improve delivery stability and customer stickiness. As a result, gallium arsenide polished wafers are not only material products, but also platform type process foundations that directly influence downstream device performance, yield, and scalable manufacturing capability.

The core characteristic of the gallium arsenide polished wafer industry is that it is not a conventional materials market won by a single specification. Instead, it is a high barrier foundational materials segment built jointly on crystal growth, defect control, surface treatment, and customer qualification. Official product pages show that mainstream suppliers typically emphasize growth routes such as VGF, LEC, and VB, offer both semi insulating and semiconducting types, and support multiple delivery states including single side polished, double side polished, and EPI Ready grades. This indicates that the true basis of competition lies in manufacturability and verifiability rather than in simple product naming. For downstream customers, the value of a GaAs wafer does not lie only in the material itself, but in whether it can consistently enter the epitaxy and device processing window, whether it can repeatedly control flatness, thickness, off cut, and defect density, and whether it can support volume manufacturing and long term supply. As a result, the companies that remain at the top of this industry are usually those that combine long standing materials expertise with fine grained specification management and deep customer collaboration. That is what gives this industry its natural attributes of high entry barriers, qualification intensity, and strong customer stickiness.

From the demand side, gallium arsenide polished wafers do not serve only one downstream market. They form a shared platform across RF, optoelectronics, and selected high value energy applications. Official product pages show uses spanning HBT, pHEMT, VCSEL, lasers, LEDs, optical communications, and high efficiency solar cells. This means the market can benefit from mobile communications, data centers, and high speed interconnects, while also gaining from the expansion of new display technologies, automotive sensing, and industrial laser applications. In environments that require high frequency, high speed, and strong photoelectric conversion performance, GaAs still retains clear material advantages over silicon. It is therefore unlikely to be displaced quickly by improvements in silicon based technology, and is more likely to maintain its strategic role in narrower but higher performance use cases. This diversified downstream structure is important because it reduces the impact of any single demand cycle and allows suppliers to maintain revenue resilience through application diversification and portfolio optimization. Over the medium term, as long as 5G evolution, AI driven data center buildout, and the expansion of VCSEL and sensing applications continue, the demand foundation for GaAs polished wafers should remain solid.

From the perspective of regional competition and long term outlook, the market today resembles a structural expansion led jointly by a small number of mature global

suppliers and a small set of rising regional manufacturers. The United States, Germany, and Japan still hold strong positions in high end substrates and long term mass production capability, while Chinese suppliers are steadily strengthening their presence through capacity expansion, broader product coverage, and import substitution. Officially verifiable information shows that Chinese companies are no longer limited to low end participation. They are now building more complete product portfolios across 2 inch to 8 inch formats, semi insulating and semiconducting types, and both RF and optoelectronic applications. At the same time, international suppliers continue to emphasize global delivery, process maturity, and long term customer relationships. This suggests the industry is unlikely to move toward pure price competition. It is more likely to evolve into a market where technical capability, delivery stability, and application collaboration matter equally. Overall, the outlook for the GaAs polished wafer segment remains positive, because it sits at the intersection of two high momentum application systems, RF and optoelectronics, while also benefiting from high material barriers and long qualification cycles that help preserve margins and market position for manufacturers with real production capability.

This report is a detailed and comprehensive analysis for global GaAs Polished Wafer market. Both quantitative and qualitative analyses are presented by manufacturers, by region & country, by Type and by Application. As the market is constantly changing, this report explores the competition, supply and demand trends, as well as key factors that contribute to its changing demands across many markets. Company profiles and product examples of selected competitors, along with market share estimates of some of the selected leaders for the year 2025, are provided.

Key Features:

Global GaAs Polished Wafer market size and forecasts, in consumption value (\$ Million), sales quantity (K Pcs), and average selling prices (US\$/Pcs), 2021-2032

Global GaAs Polished Wafer market size and forecasts by region and country, in consumption value (\$ Million), sales quantity (K Pcs), and average selling prices (US\$/Pcs), 2021-2032

Global GaAs Polished Wafer market size and forecasts, by Type and by Application, in consumption value (\$ Million), sales quantity (K Pcs), and average selling prices (US\$/Pcs), 2021-2032

Global GaAs Polished Wafer market shares of main players, shipments in revenue (\$

Million), sales quantity (K Pcs), and ASP (US\$/Pcs), 2021-2026

The Primary Objectives in This Report Are:

To determine the size of the total market opportunity of global and key countries

To assess the growth potential for GaAs Polished Wafer

To forecast future growth in each product and end-use market

To assess competitive factors affecting the marketplace

This report profiles key players in the global GaAs Polished Wafer market based on the following parameters - company overview, sales quantity, revenue, price, gross margin, product portfolio, geographical presence, and key developments. Key companies covered as a part of this study include Freiberger Compound Materials, AXT, Sumitomo Electric, Vital Advanced Material, China Crystal Technologies, Yunnan Germanium, DOWA Electronics Materials, Shin-Etsu Chemical Co., Ltd., IQE plc, CMK Ltd., etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals.

Market Segmentation

GaAs Polished Wafer market is split by Type and by Application. For the period 2021-2032, the growth among segments provides accurate calculations and forecasts for consumption value by Type, and by Application in terms of volume and value. This analysis can help you expand your business by targeting qualified niche markets.

Market segment by Type

LEC Grown GaAs

VGF Grown GaAs

Others

Market segment by Electrical Type

Semi-Insulating Type

N-Type

P-Type

Market segment by Size Specification

4 Inches and Below

6 Inches

8 Inches and Above

Market segment by Application

RF

LED

Photovoltaic

Other

Major players covered

Freiberger Compound Materials

AXT

Sumitomo Electric

Vital Advanced Material

China Crystal Technologies

Yunnan Germanium

DOWA Electronics Materials

Shin-Etsu Chemical Co., Ltd.

IQE plc

CMK Ltd.

Xiamen Powerway Advanced Material Co., Ltd.

Market segment by region, regional analysis covers

North America (United States, Canada, and Mexico)

Europe (Germany, France, United Kingdom, Russia, Italy, and Rest of Europe)

Asia-Pacific (China, Japan, Korea, India, Southeast Asia, and Australia)

South America (Brazil, Argentina, Colombia, and Rest of South America)

Middle East & Africa (Saudi Arabia, UAE, Egypt, South Africa, and Rest of Middle East & Africa)

The content of the study subjects, includes a total of 15 chapters:

Chapter 1, to describe GaAs Polished Wafer product scope, market overview, market estimation caveats and base year.

Chapter 2, to profile the top manufacturers of GaAs Polished Wafer, with price, sales quantity, revenue, and global market share of GaAs Polished Wafer from 2021 to 2026.

Chapter 3, the GaAs Polished Wafer competitive situation, sales quantity, revenue, and global market share of top manufacturers are analyzed emphatically by landscape

contrast.

Chapter 4, the GaAs Polished Wafer breakdown data are shown at the regional level, to show the sales quantity, consumption value, and growth by regions, from 2021 to 2032.

Chapter 5 and 6, to segment the sales by Type and by Application, with sales market share and growth rate by Type, by Application, from 2021 to 2032.

Chapter 7, 8, 9, 10 and 11, to break the sales data at the country level, with sales quantity, consumption value, and market share for key countries in the world, from 2021 to 2026. and GaAs Polished Wafer market forecast, by regions, by Type, and by Application, with sales and revenue, from 2027 to 2032.

Chapter 12, market dynamics, drivers, restraints, trends, and Porters Five Forces analysis.

Chapter 13, the key raw materials and key suppliers, and industry chain of GaAs Polished Wafer.

Chapter 14 and 15, to describe GaAs Polished Wafer sales channel, distributors, customers, research findings and conclusion.

Contents

1 MARKET OVERVIEW

1.1 Product Overview and Scope

1.2 Market Estimation Caveats and Base Year

1.3 Market Analysis by Type

1.3.1 Overview: Global GaAs Polished Wafer Consumption Value by Type: 2021 Versus 2025 Versus 2032

1.3.2 LEC Grown GaAs

1.3.3 VGF Grown GaAs

1.3.4 Others

1.4 Market Analysis by Electrical Type

1.4.1 Overview: Global GaAs Polished Wafer Consumption Value by Electrical Type: 2021 Versus 2025 Versus 2032

1.4.2 Semi-Insulating Type

1.4.3 N-Type

1.4.4 P-Type

1.5 Market Analysis by Size Specification

1.5.1 Overview: Global GaAs Polished Wafer Consumption Value by Size Specification: 2021 Versus 2025 Versus 2032

1.5.2 4 Inches and Below

1.5.3 6 Inches

1.5.4 8 Inches and Above

1.6 Market Analysis by Application

1.6.1 Overview: Global GaAs Polished Wafer Consumption Value by Application: 2021 Versus 2025 Versus 2032

1.6.2 RF

1.6.3 LED

1.6.4 Photovoltaic

1.6.5 Other

1.7 Global GaAs Polished Wafer Market Size & Forecast

1.7.1 Global GaAs Polished Wafer Consumption Value (2021 & 2025 & 2032)

1.7.2 Global GaAs Polished Wafer Sales Quantity (2021-2032)

1.7.3 Global GaAs Polished Wafer Average Price (2021-2032)

2 MANUFACTURERS PROFILES

2.1 Freiberger Compound Materials

- 2.1.1 Freiberger Compound Materials Details
- 2.1.2 Freiberger Compound Materials Major Business
- 2.1.3 Freiberger Compound Materials GaAs Polished Wafer Product and Services
- 2.1.4 Freiberger Compound Materials GaAs Polished Wafer Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
- 2.1.5 Freiberger Compound Materials Recent Developments/Updates
- 2.2 AXT
 - 2.2.1 AXT Details
 - 2.2.2 AXT Major Business
 - 2.2.3 AXT GaAs Polished Wafer Product and Services
 - 2.2.4 AXT GaAs Polished Wafer Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
 - 2.2.5 AXT Recent Developments/Updates
- 2.3 Sumitomo Electric
 - 2.3.1 Sumitomo Electric Details
 - 2.3.2 Sumitomo Electric Major Business
 - 2.3.3 Sumitomo Electric GaAs Polished Wafer Product and Services
 - 2.3.4 Sumitomo Electric GaAs Polished Wafer Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
 - 2.3.5 Sumitomo Electric Recent Developments/Updates
- 2.4 Vital Advanced Material
 - 2.4.1 Vital Advanced Material Details
 - 2.4.2 Vital Advanced Material Major Business
 - 2.4.3 Vital Advanced Material GaAs Polished Wafer Product and Services
 - 2.4.4 Vital Advanced Material GaAs Polished Wafer Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
 - 2.4.5 Vital Advanced Material Recent Developments/Updates
- 2.5 China Crystal Technologies
 - 2.5.1 China Crystal Technologies Details
 - 2.5.2 China Crystal Technologies Major Business
 - 2.5.3 China Crystal Technologies GaAs Polished Wafer Product and Services
 - 2.5.4 China Crystal Technologies GaAs Polished Wafer Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
 - 2.5.5 China Crystal Technologies Recent Developments/Updates
- 2.6 Yunnan Germanium
 - 2.6.1 Yunnan Germanium Details
 - 2.6.2 Yunnan Germanium Major Business
 - 2.6.3 Yunnan Germanium GaAs Polished Wafer Product and Services
 - 2.6.4 Yunnan Germanium GaAs Polished Wafer Sales Quantity, Average Price,

Revenue, Gross Margin and Market Share (2021-2026)

2.6.5 Yunnan Germanium Recent Developments/Updates

2.7 DOWA Electronics Materials

2.7.1 DOWA Electronics Materials Details

2.7.2 DOWA Electronics Materials Major Business

2.7.3 DOWA Electronics Materials GaAs Polished Wafer Product and Services

2.7.4 DOWA Electronics Materials GaAs Polished Wafer Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)

2.7.5 DOWA Electronics Materials Recent Developments/Updates

2.8 Shin-Etsu Chemical Co., Ltd.

2.8.1 Shin-Etsu Chemical Co., Ltd. Details

2.8.2 Shin-Etsu Chemical Co., Ltd. Major Business

2.8.3 Shin-Etsu Chemical Co., Ltd. GaAs Polished Wafer Product and Services

2.8.4 Shin-Etsu Chemical Co., Ltd. GaAs Polished Wafer Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)

2.8.5 Shin-Etsu Chemical Co., Ltd. Recent Developments/Updates

2.9 IQE plc

2.9.1 IQE plc Details

2.9.2 IQE plc Major Business

2.9.3 IQE plc GaAs Polished Wafer Product and Services

2.9.4 IQE plc GaAs Polished Wafer Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)

2.9.5 IQE plc Recent Developments/Updates

2.10 CMK Ltd.

2.10.1 CMK Ltd. Details

2.10.2 CMK Ltd. Major Business

2.10.3 CMK Ltd. GaAs Polished Wafer Product and Services

2.10.4 CMK Ltd. GaAs Polished Wafer Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)

2.10.5 CMK Ltd. Recent Developments/Updates

2.11 Xiamen Powerway Advanced Material Co., Ltd.

2.11.1 Xiamen Powerway Advanced Material Co., Ltd. Details

2.11.2 Xiamen Powerway Advanced Material Co., Ltd. Major Business

2.11.3 Xiamen Powerway Advanced Material Co., Ltd. GaAs Polished Wafer Product and Services

2.11.4 Xiamen Powerway Advanced Material Co., Ltd. GaAs Polished Wafer Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)

2.11.5 Xiamen Powerway Advanced Material Co., Ltd. Recent Developments/Updates

3 COMPETITIVE ENVIRONMENT: GAAS POLISHED WAFER BY MANUFACTURER

- 3.1 Global GaAs Polished Wafer Sales Quantity by Manufacturer (2021-2026)
- 3.2 Global GaAs Polished Wafer Revenue by Manufacturer (2021-2026)
- 3.3 Global GaAs Polished Wafer Average Price by Manufacturer (2021-2026)
- 3.4 Market Share Analysis (2025)
 - 3.4.1 Producer Shipments of GaAs Polished Wafer by Manufacturer Revenue (\$MM) and Market Share (%): 2025
 - 3.4.2 Top 3 GaAs Polished Wafer Manufacturer Market Share in 2025
 - 3.4.3 Top 6 GaAs Polished Wafer Manufacturer Market Share in 2025
- 3.5 GaAs Polished Wafer Market: Overall Company Footprint Analysis
 - 3.5.1 GaAs Polished Wafer Market: Region Footprint
 - 3.5.2 GaAs Polished Wafer Market: Company Product Type Footprint
 - 3.5.3 GaAs Polished Wafer Market: Company Product Application Footprint
- 3.6 New Market Entrants and Barriers to Market Entry
- 3.7 Mergers, Acquisition, Agreements, and Collaborations

4 CONSUMPTION ANALYSIS BY REGION

- 4.1 Global GaAs Polished Wafer Market Size by Region
 - 4.1.1 Global GaAs Polished Wafer Sales Quantity by Region (2021-2032)
 - 4.1.2 Global GaAs Polished Wafer Consumption Value by Region (2021-2032)
 - 4.1.3 Global GaAs Polished Wafer Average Price by Region (2021-2032)
- 4.2 North America GaAs Polished Wafer Consumption Value (2021-2032)
- 4.3 Europe GaAs Polished Wafer Consumption Value (2021-2032)
- 4.4 Asia-Pacific GaAs Polished Wafer Consumption Value (2021-2032)
- 4.5 South America GaAs Polished Wafer Consumption Value (2021-2032)
- 4.6 Middle East & Africa GaAs Polished Wafer Consumption Value (2021-2032)

5 MARKET SEGMENT BY TYPE

- 5.1 Global GaAs Polished Wafer Sales Quantity by Type (2021-2032)
- 5.2 Global GaAs Polished Wafer Consumption Value by Type (2021-2032)
- 5.3 Global GaAs Polished Wafer Average Price by Type (2021-2032)

6 MARKET SEGMENT BY APPLICATION

- 6.1 Global GaAs Polished Wafer Sales Quantity by Application (2021-2032)
- 6.2 Global GaAs Polished Wafer Consumption Value by Application (2021-2032)

6.3 Global GaAs Polished Wafer Average Price by Application (2021-2032)

7 NORTH AMERICA

7.1 North America GaAs Polished Wafer Sales Quantity by Type (2021-2032)

7.2 North America GaAs Polished Wafer Sales Quantity by Application (2021-2032)

7.3 North America GaAs Polished Wafer Market Size by Country

7.3.1 North America GaAs Polished Wafer Sales Quantity by Country (2021-2032)

7.3.2 North America GaAs Polished Wafer Consumption Value by Country (2021-2032)

7.3.3 United States Market Size and Forecast (2021-2032)

7.3.4 Canada Market Size and Forecast (2021-2032)

7.3.5 Mexico Market Size and Forecast (2021-2032)

8 EUROPE

8.1 Europe GaAs Polished Wafer Sales Quantity by Type (2021-2032)

8.2 Europe GaAs Polished Wafer Sales Quantity by Application (2021-2032)

8.3 Europe GaAs Polished Wafer Market Size by Country

8.3.1 Europe GaAs Polished Wafer Sales Quantity by Country (2021-2032)

8.3.2 Europe GaAs Polished Wafer Consumption Value by Country (2021-2032)

8.3.3 Germany Market Size and Forecast (2021-2032)

8.3.4 France Market Size and Forecast (2021-2032)

8.3.5 United Kingdom Market Size and Forecast (2021-2032)

8.3.6 Russia Market Size and Forecast (2021-2032)

8.3.7 Italy Market Size and Forecast (2021-2032)

9 ASIA-PACIFIC

9.1 Asia-Pacific GaAs Polished Wafer Sales Quantity by Type (2021-2032)

9.2 Asia-Pacific GaAs Polished Wafer Sales Quantity by Application (2021-2032)

9.3 Asia-Pacific GaAs Polished Wafer Market Size by Region

9.3.1 Asia-Pacific GaAs Polished Wafer Sales Quantity by Region (2021-2032)

9.3.2 Asia-Pacific GaAs Polished Wafer Consumption Value by Region (2021-2032)

9.3.3 China Market Size and Forecast (2021-2032)

9.3.4 Japan Market Size and Forecast (2021-2032)

9.3.5 South Korea Market Size and Forecast (2021-2032)

9.3.6 India Market Size and Forecast (2021-2032)

9.3.7 Southeast Asia Market Size and Forecast (2021-2032)

9.3.8 Australia Market Size and Forecast (2021-2032)

10 SOUTH AMERICA

10.1 South America GaAs Polished Wafer Sales Quantity by Type (2021-2032)

10.2 South America GaAs Polished Wafer Sales Quantity by Application (2021-2032)

10.3 South America GaAs Polished Wafer Market Size by Country

10.3.1 South America GaAs Polished Wafer Sales Quantity by Country (2021-2032)

10.3.2 South America GaAs Polished Wafer Consumption Value by Country (2021-2032)

10.3.3 Brazil Market Size and Forecast (2021-2032)

10.3.4 Argentina Market Size and Forecast (2021-2032)

11 MIDDLE EAST & AFRICA

11.1 Middle East & Africa GaAs Polished Wafer Sales Quantity by Type (2021-2032)

11.2 Middle East & Africa GaAs Polished Wafer Sales Quantity by Application (2021-2032)

11.3 Middle East & Africa GaAs Polished Wafer Market Size by Country

11.3.1 Middle East & Africa GaAs Polished Wafer Sales Quantity by Country (2021-2032)

11.3.2 Middle East & Africa GaAs Polished Wafer Consumption Value by Country (2021-2032)

11.3.3 Turkey Market Size and Forecast (2021-2032)

11.3.4 Egypt Market Size and Forecast (2021-2032)

11.3.5 Saudi Arabia Market Size and Forecast (2021-2032)

11.3.6 South Africa Market Size and Forecast (2021-2032)

12 MARKET DYNAMICS

12.1 GaAs Polished Wafer Market Drivers

12.2 GaAs Polished Wafer Market Restraints

12.3 GaAs Polished Wafer Trends Analysis

12.4 Porters Five Forces Analysis

12.4.1 Threat of New Entrants

12.4.2 Bargaining Power of Suppliers

12.4.3 Bargaining Power of Buyers

12.4.4 Threat of Substitutes

12.4.5 Competitive Rivalry

13 RAW MATERIAL AND INDUSTRY CHAIN

- 13.1 Raw Material of GaAs Polished Wafer and Key Manufacturers
- 13.2 Manufacturing Costs Percentage of GaAs Polished Wafer
- 13.3 GaAs Polished Wafer Production Process
- 13.4 Industry Value Chain Analysis

14 SHIPMENTS BY DISTRIBUTION CHANNEL

- 14.1 Sales Channel
 - 14.1.1 Direct to End-User
 - 14.1.2 Distributors
- 14.2 GaAs Polished Wafer Typical Distributors
- 14.3 GaAs Polished Wafer Typical Customers

15 RESEARCH FINDINGS AND CONCLUSION

16 APPENDIX

- 16.1 Methodology
- 16.2 Research Process and Data Source
- 16.3 Disclaimer

List Of Tables

LIST OF TABLES

Table 1. Global GaAs Polished Wafer Consumption Value by Type, (USD Million), 2021 & 2025 & 2032

Table 2. Global GaAs Polished Wafer Consumption Value by Electrical Type, (USD Million), 2021 & 2025 & 2032

Table 3. Global GaAs Polished Wafer Consumption Value by Size Specification, (USD Million), 2021 & 2025 & 2032

Table 4. Global GaAs Polished Wafer Consumption Value by Application, (USD Million), 2021 & 2025 & 2032

Table 5. Freiberger Compound Materials Basic Information, Manufacturing Base and Competitors

Table 6. Freiberger Compound Materials Major Business

Table 7. Freiberger Compound Materials GaAs Polished Wafer Product and Services

Table 8. Freiberger Compound Materials GaAs Polished Wafer Sales Quantity (K Pcs), Average Price (US\$/Pcs), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 9. Freiberger Compound Materials Recent Developments/Updates

Table 10. AXT Basic Information, Manufacturing Base and Competitors

Table 11. AXT Major Business

Table 12. AXT GaAs Polished Wafer Product and Services

Table 13. AXT GaAs Polished Wafer Sales Quantity (K Pcs), Average Price (US\$/Pcs), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 14. AXT Recent Developments/Updates

Table 15. Sumitomo Electric Basic Information, Manufacturing Base and Competitors

Table 16. Sumitomo Electric Major Business

Table 17. Sumitomo Electric GaAs Polished Wafer Product and Services

Table 18. Sumitomo Electric GaAs Polished Wafer Sales Quantity (K Pcs), Average Price (US\$/Pcs), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 19. Sumitomo Electric Recent Developments/Updates

Table 20. Vital Advanced Material Basic Information, Manufacturing Base and Competitors

Table 21. Vital Advanced Material Major Business

Table 22. Vital Advanced Material GaAs Polished Wafer Product and Services

Table 23. Vital Advanced Material GaAs Polished Wafer Sales Quantity (K Pcs), Average Price (US\$/Pcs), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 24. Vital Advanced Material Recent Developments/Updates

Table 25. China Crystal Technologies Basic Information, Manufacturing Base and Competitors

Table 26. China Crystal Technologies Major Business

Table 27. China Crystal Technologies GaAs Polished Wafer Product and Services

Table 28. China Crystal Technologies GaAs Polished Wafer Sales Quantity (K Pcs), Average Price (US\$/Pcs), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 29. China Crystal Technologies Recent Developments/Updates

Table 30. Yunnan Germanium Basic Information, Manufacturing Base and Competitors

Table 31. Yunnan Germanium Major Business

Table 32. Yunnan Germanium GaAs Polished Wafer Product and Services

Table 33. Yunnan Germanium GaAs Polished Wafer Sales Quantity (K Pcs), Average Price (US\$/Pcs), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 34. Yunnan Germanium Recent Developments/Updates

Table 35. DOWA Electronics Materials Basic Information, Manufacturing Base and Competitors

Table 36. DOWA Electronics Materials Major Business

Table 37. DOWA Electronics Materials GaAs Polished Wafer Product and Services

Table 38. DOWA Electronics Materials GaAs Polished Wafer Sales Quantity (K Pcs), Average Price (US\$/Pcs), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 39. DOWA Electronics Materials Recent Developments/Updates

Table 40. Shin-Etsu Chemical Co., Ltd. Basic Information, Manufacturing Base and Competitors

Table 41. Shin-Etsu Chemical Co., Ltd. Major Business

Table 42. Shin-Etsu Chemical Co., Ltd. GaAs Polished Wafer Product and Services

Table 43. Shin-Etsu Chemical Co., Ltd. GaAs Polished Wafer Sales Quantity (K Pcs), Average Price (US\$/Pcs), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 44. Shin-Etsu Chemical Co., Ltd. Recent Developments/Updates

Table 45. IQE plc Basic Information, Manufacturing Base and Competitors

Table 46. IQE plc Major Business

Table 47. IQE plc GaAs Polished Wafer Product and Services

Table 48. IQE plc GaAs Polished Wafer Sales Quantity (K Pcs), Average Price (US\$/Pcs), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 49. IQE plc Recent Developments/Updates

Table 50. CMK Ltd. Basic Information, Manufacturing Base and Competitors

Table 51. CMK Ltd. Major Business

- Table 52. CMK Ltd. GaAs Polished Wafer Product and Services
- Table 53. CMK Ltd. GaAs Polished Wafer Sales Quantity (K Pcs), Average Price (US\$/Pcs), Revenue (USD Million), Gross Margin and Market Share (2021-2026)
- Table 54. CMK Ltd. Recent Developments/Updates
- Table 55. Xiamen Powerway Advanced Material Co., Ltd. Basic Information, Manufacturing Base and Competitors
- Table 56. Xiamen Powerway Advanced Material Co., Ltd. Major Business
- Table 57. Xiamen Powerway Advanced Material Co., Ltd. GaAs Polished Wafer Product and Services
- Table 58. Xiamen Powerway Advanced Material Co., Ltd. GaAs Polished Wafer Sales Quantity (K Pcs), Average Price (US\$/Pcs), Revenue (USD Million), Gross Margin and Market Share (2021-2026)
- Table 59. Xiamen Powerway Advanced Material Co., Ltd. Recent Developments/Updates
- Table 60. Global GaAs Polished Wafer Sales Quantity by Manufacturer (2021-2026) & (K Pcs)
- Table 61. Global GaAs Polished Wafer Revenue by Manufacturer (2021-2026) & (USD Million)
- Table 62. Global GaAs Polished Wafer Average Price by Manufacturer (2021-2026) & (US\$/Pcs)
- Table 63. Market Position of Manufacturers in GaAs Polished Wafer, (Tier 1, Tier 2, and Tier 3), Based on Revenue in 2025
- Table 64. Head Office and GaAs Polished Wafer Production Site of Key Manufacturer
- Table 65. GaAs Polished Wafer Market: Company Product Type Footprint
- Table 66. GaAs Polished Wafer Market: Company Product Application Footprint
- Table 67. GaAs Polished Wafer New Market Entrants and Barriers to Market Entry
- Table 68. GaAs Polished Wafer Mergers, Acquisition, Agreements, and Collaborations
- Table 69. Global GaAs Polished Wafer Consumption Value by Region (2021-2025-2032) & (USD Million) & CAGR
- Table 70. Global GaAs Polished Wafer Sales Quantity by Region (2021-2026) & (K Pcs)
- Table 71. Global GaAs Polished Wafer Sales Quantity by Region (2027-2032) & (K Pcs)
- Table 72. Global GaAs Polished Wafer Consumption Value by Region (2021-2026) & (USD Million)
- Table 73. Global GaAs Polished Wafer Consumption Value by Region (2027-2032) & (USD Million)
- Table 74. Global GaAs Polished Wafer Average Price by Region (2021-2026) & (US\$/Pcs)
- Table 75. Global GaAs Polished Wafer Average Price by Region (2027-2032) & (US\$/Pcs)

Table 76. Global GaAs Polished Wafer Sales Quantity by Type (2021-2026) & (K Pcs)

Table 77. Global GaAs Polished Wafer Sales Quantity by Type (2027-2032) & (K Pcs)

Table 78. Global GaAs Polished Wafer Consumption Value by Type (2021-2026) & (USD Million)

Table 79. Global GaAs Polished Wafer Consumption Value by Type (2027-2032) & (USD Million)

Table 80. Global GaAs Polished Wafer Average Price by Type (2021-2026) & (US\$/Pcs)

Table 81. Global GaAs Polished Wafer Average Price by Type (2027-2032) & (US\$/Pcs)

Table 82. Global GaAs Polished Wafer Sales Quantity by Application (2021-2026) & (K Pcs)

Table 83. Global GaAs Polished Wafer Sales Quantity by Application (2027-2032) & (K Pcs)

Table 84. Global GaAs Polished Wafer Consumption Value by Application (2021-2026) & (USD Million)

Table 85. Global GaAs Polished Wafer Consumption Value by Application (2027-2032) & (USD Million)

Table 86. Global GaAs Polished Wafer Average Price by Application (2021-2026) & (US\$/Pcs)

Table 87. Global GaAs Polished Wafer Average Price by Application (2027-2032) & (US\$/Pcs)

Table 88. North America GaAs Polished Wafer Sales Quantity by Type (2021-2026) & (K Pcs)

Table 89. North America GaAs Polished Wafer Sales Quantity by Type (2027-2032) & (K Pcs)

Table 90. North America GaAs Polished Wafer Sales Quantity by Application (2021-2026) & (K Pcs)

Table 91. North America GaAs Polished Wafer Sales Quantity by Application (2027-2032) & (K Pcs)

Table 92. North America GaAs Polished Wafer Sales Quantity by Country (2021-2026) & (K Pcs)

Table 93. North America GaAs Polished Wafer Sales Quantity by Country (2027-2032) & (K Pcs)

Table 94. North America GaAs Polished Wafer Consumption Value by Country (2021-2026) & (USD Million)

Table 95. North America GaAs Polished Wafer Consumption Value by Country (2027-2032) & (USD Million)

Table 96. Europe GaAs Polished Wafer Sales Quantity by Type (2021-2026) & (K Pcs)

- Table 97. Europe GaAs Polished Wafer Sales Quantity by Type (2027-2032) & (K Pcs)
- Table 98. Europe GaAs Polished Wafer Sales Quantity by Application (2021-2026) & (K Pcs)
- Table 99. Europe GaAs Polished Wafer Sales Quantity by Application (2027-2032) & (K Pcs)
- Table 100. Europe GaAs Polished Wafer Sales Quantity by Country (2021-2026) & (K Pcs)
- Table 101. Europe GaAs Polished Wafer Sales Quantity by Country (2027-2032) & (K Pcs)
- Table 102. Europe GaAs Polished Wafer Consumption Value by Country (2021-2026) & (USD Million)
- Table 103. Europe GaAs Polished Wafer Consumption Value by Country (2027-2032) & (USD Million)
- Table 104. Asia-Pacific GaAs Polished Wafer Sales Quantity by Type (2021-2026) & (K Pcs)
- Table 105. Asia-Pacific GaAs Polished Wafer Sales Quantity by Type (2027-2032) & (K Pcs)
- Table 106. Asia-Pacific GaAs Polished Wafer Sales Quantity by Application (2021-2026) & (K Pcs)
- Table 107. Asia-Pacific GaAs Polished Wafer Sales Quantity by Application (2027-2032) & (K Pcs)
- Table 108. Asia-Pacific GaAs Polished Wafer Sales Quantity by Region (2021-2026) & (K Pcs)
- Table 109. Asia-Pacific GaAs Polished Wafer Sales Quantity by Region (2027-2032) & (K Pcs)
- Table 110. Asia-Pacific GaAs Polished Wafer Consumption Value by Region (2021-2026) & (USD Million)
- Table 111. Asia-Pacific GaAs Polished Wafer Consumption Value by Region (2027-2032) & (USD Million)
- Table 112. South America GaAs Polished Wafer Sales Quantity by Type (2021-2026) & (K Pcs)
- Table 113. South America GaAs Polished Wafer Sales Quantity by Type (2027-2032) & (K Pcs)
- Table 114. South America GaAs Polished Wafer Sales Quantity by Application (2021-2026) & (K Pcs)
- Table 115. South America GaAs Polished Wafer Sales Quantity by Application (2027-2032) & (K Pcs)
- Table 116. South America GaAs Polished Wafer Sales Quantity by Country (2021-2026) & (K Pcs)

Table 117. South America GaAs Polished Wafer Sales Quantity by Country (2027-2032) & (K Pcs)

Table 118. South America GaAs Polished Wafer Consumption Value by Country (2021-2026) & (USD Million)

Table 119. South America GaAs Polished Wafer Consumption Value by Country (2027-2032) & (USD Million)

Table 120. Middle East & Africa GaAs Polished Wafer Sales Quantity by Type (2021-2026) & (K Pcs)

Table 121. Middle East & Africa GaAs Polished Wafer Sales Quantity by Type (2027-2032) & (K Pcs)

Table 122. Middle East & Africa GaAs Polished Wafer Sales Quantity by Application (2021-2026) & (K Pcs)

Table 123. Middle East & Africa GaAs Polished Wafer Sales Quantity by Application (2027-2032) & (K Pcs)

Table 124. Middle East & Africa GaAs Polished Wafer Sales Quantity by Country (2021-2026) & (K Pcs)

Table 125. Middle East & Africa GaAs Polished Wafer Sales Quantity by Country (2027-2032) & (K Pcs)

Table 126. Middle East & Africa GaAs Polished Wafer Consumption Value by Country (2021-2026) & (USD Million)

Table 127. Middle East & Africa GaAs Polished Wafer Consumption Value by Country (2027-2032) & (USD Million)

Table 128. GaAs Polished Wafer Raw Material

Table 129. Key Manufacturers of GaAs Polished Wafer Raw Materials

Table 130. GaAs Polished Wafer Typical Distributors

Table 131. GaAs Polished Wafer Typical Customers

List Of Figures

LIST OF FIGURES

Figure 1. GaAs Polished Wafer Picture

Figure 2. Global GaAs Polished Wafer Revenue by Type, (USD Million), 2021 & 2025 & 2032

Figure 3. Global GaAs Polished Wafer Revenue Market Share by Type in 2025

Figure 4. LEC Grown GaAs Examples

Figure 5. VGF Grown GaAs Examples

Figure 6. Others Examples

Figure 7. Global GaAs Polished Wafer Revenue by Electrical Type, (USD Million), 2021 & 2025 & 2032

Figure 8. Global GaAs Polished Wafer Revenue Market Share by Electrical Type in 2025

Figure 9. Semi-Insulating Type Examples

Figure 10. N-Type Examples

Figure 11. P-Type Examples

Figure 12. Global GaAs Polished Wafer Revenue by Size Specification, (USD Million), 2021 & 2025 & 2032

Figure 13. Global GaAs Polished Wafer Revenue Market Share by Size Specification in 2025

Figure 14. 4 Inches and Below Examples

Figure 15. 6 Inches Examples

Figure 16. 8 Inches and Above Examples

Figure 17. Global GaAs Polished Wafer Consumption Value by Application, (USD Million), 2021 & 2025 & 2032

Figure 18. Global GaAs Polished Wafer Revenue Market Share by Application in 2025

Figure 19. RF Examples

Figure 20. LED Examples

Figure 21. Photovoltaic Examples

Figure 22. Other Examples

Figure 23. Global GaAs Polished Wafer Consumption Value, (USD Million): 2021 & 2025 & 2032

Figure 24. Global GaAs Polished Wafer Consumption Value and Forecast (2021-2032) & (USD Million)

Figure 25. Global GaAs Polished Wafer Sales Quantity (2021-2032) & (K Pcs)

Figure 26. Global GaAs Polished Wafer Price (2021-2032) & (US\$/Pcs)

Figure 27. Global GaAs Polished Wafer Sales Quantity Market Share by Manufacturer

in 2025

Figure 28. Global GaAs Polished Wafer Revenue Market Share by Manufacturer in 2025

Figure 29. Producer Shipments of GaAs Polished Wafer by Manufacturer Sales (\$MM) and Market Share (%): 2025

Figure 30. Top 3 GaAs Polished Wafer Manufacturer (Revenue) Market Share in 2025

Figure 31. Top 6 GaAs Polished Wafer Manufacturer (Revenue) Market Share in 2025

Figure 32. Global GaAs Polished Wafer Sales Quantity Market Share by Region (2021-2032)

Figure 33. Global GaAs Polished Wafer Consumption Value Market Share by Region (2021-2032)

Figure 34. North America GaAs Polished Wafer Consumption Value (2021-2032) & (USD Million)

Figure 35. Europe GaAs Polished Wafer Consumption Value (2021-2032) & (USD Million)

Figure 36. Asia-Pacific GaAs Polished Wafer Consumption Value (2021-2032) & (USD Million)

Figure 37. South America GaAs Polished Wafer Consumption Value (2021-2032) & (USD Million)

Figure 38. Middle East & Africa GaAs Polished Wafer Consumption Value (2021-2032) & (USD Million)

Figure 39. Global GaAs Polished Wafer Sales Quantity Market Share by Type (2021-2032)

Figure 40. Global GaAs Polished Wafer Consumption Value Market Share by Type (2021-2032)

Figure 41. Global GaAs Polished Wafer Average Price by Type (2021-2032) & (US\$/Pcs)

Figure 42. Global GaAs Polished Wafer Sales Quantity Market Share by Application (2021-2032)

Figure 43. Global GaAs Polished Wafer Revenue Market Share by Application (2021-2032)

Figure 44. Global GaAs Polished Wafer Average Price by Application (2021-2032) & (US\$/Pcs)

Figure 45. North America GaAs Polished Wafer Sales Quantity Market Share by Type (2021-2032)

Figure 46. North America GaAs Polished Wafer Sales Quantity Market Share by Application (2021-2032)

Figure 47. North America GaAs Polished Wafer Sales Quantity Market Share by Country (2021-2032)

Figure 48. North America GaAs Polished Wafer Consumption Value Market Share by Country (2021-2032)

Figure 49. United States GaAs Polished Wafer Consumption Value (2021-2032) & (USD Million)

Figure 50. Canada GaAs Polished Wafer Consumption Value (2021-2032) & (USD Million)

Figure 51. Mexico GaAs Polished Wafer Consumption Value (2021-2032) & (USD Million)

Figure 52. Europe GaAs Polished Wafer Sales Quantity Market Share by Type (2021-2032)

Figure 53. Europe GaAs Polished Wafer Sales Quantity Market Share by Application (2021-2032)

Figure 54. Europe GaAs Polished Wafer Sales Quantity Market Share by Country (2021-2032)

Figure 55. Europe GaAs Polished Wafer Consumption Value Market Share by Country (2021-2032)

Figure 56. Germany GaAs Polished Wafer Consumption Value (2021-2032) & (USD Million)

Figure 57. France GaAs Polished Wafer Consumption Value (2021-2032) & (USD Million)

Figure 58. United Kingdom GaAs Polished Wafer Consumption Value (2021-2032) & (USD Million)

Figure 59. Russia GaAs Polished Wafer Consumption Value (2021-2032) & (USD Million)

Figure 60. Italy GaAs Polished Wafer Consumption Value (2021-2032) & (USD Million)

Figure 61. Asia-Pacific GaAs Polished Wafer Sales Quantity Market Share by Type (2021-2032)

Figure 62. Asia-Pacific GaAs Polished Wafer Sales Quantity Market Share by Application (2021-2032)

Figure 63. Asia-Pacific GaAs Polished Wafer Sales Quantity Market Share by Region (2021-2032)

Figure 64. Asia-Pacific GaAs Polished Wafer Consumption Value Market Share by Region (2021-2032)

Figure 65. China GaAs Polished Wafer Consumption Value (2021-2032) & (USD Million)

Figure 66. Japan GaAs Polished Wafer Consumption Value (2021-2032) & (USD Million)

Figure 67. South Korea GaAs Polished Wafer Consumption Value (2021-2032) & (USD Million)

- Figure 68. India GaAs Polished Wafer Consumption Value (2021-2032) & (USD Million)
- Figure 69. Southeast Asia GaAs Polished Wafer Consumption Value (2021-2032) & (USD Million)
- Figure 70. Australia GaAs Polished Wafer Consumption Value (2021-2032) & (USD Million)
- Figure 71. South America GaAs Polished Wafer Sales Quantity Market Share by Type (2021-2032)
- Figure 72. South America GaAs Polished Wafer Sales Quantity Market Share by Application (2021-2032)
- Figure 73. South America GaAs Polished Wafer Sales Quantity Market Share by Country (2021-2032)
- Figure 74. South America GaAs Polished Wafer Consumption Value Market Share by Country (2021-2032)
- Figure 75. Brazil GaAs Polished Wafer Consumption Value (2021-2032) & (USD Million)
- Figure 76. Argentina GaAs Polished Wafer Consumption Value (2021-2032) & (USD Million)
- Figure 77. Middle East & Africa GaAs Polished Wafer Sales Quantity Market Share by Type (2021-2032)
- Figure 78. Middle East & Africa GaAs Polished Wafer Sales Quantity Market Share by Application (2021-2032)
- Figure 79. Middle East & Africa GaAs Polished Wafer Sales Quantity Market Share by Country (2021-2032)
- Figure 80. Middle East & Africa GaAs Polished Wafer Consumption Value Market Share by Country (2021-2032)
- Figure 81. Turkey GaAs Polished Wafer Consumption Value (2021-2032) & (USD Million)
- Figure 82. Egypt GaAs Polished Wafer Consumption Value (2021-2032) & (USD Million)
- Figure 83. Saudi Arabia GaAs Polished Wafer Consumption Value (2021-2032) & (USD Million)
- Figure 84. South Africa GaAs Polished Wafer Consumption Value (2021-2032) & (USD Million)
- Figure 85. GaAs Polished Wafer Market Drivers
- Figure 86. GaAs Polished Wafer Market Restraints
- Figure 87. GaAs Polished Wafer Market Trends
- Figure 88. Porters Five Forces Analysis
- Figure 89. Manufacturing Cost Structure Analysis of GaAs Polished Wafer in 2025
- Figure 90. Manufacturing Process Analysis of GaAs Polished Wafer
- Figure 91. GaAs Polished Wafer Industrial Chain

Figure 92. Sales Channel: Direct to End-User vs Distributors

Figure 93. Direct Channel Pros & Cons

Figure 94. Indirect Channel Pros & Cons

Figure 95. Methodology

Figure 96. Research Process and Data Source

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