

Global Fully Depleted Silicon-on-insulator (FD-SOI) Technology Market 2018 by Manufacturers, Countries, Type and Application, Forecast to 2023

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Abstracts

Fully depleted silicon-on-insulator (FD-SOI) is a planar process technology that provides an ideal platform for the development of planar transistor architecture on a SOI substrate. FD-SOI helps in delivering low power, improved performance, and reduced silicon geometries. The FD-SOI technology is based on two innovations: an ultra-thin insulator layer known as buried oxide, which is placed at the top of the silicon base, and a thin layer of silicon, which is used for creating a transistor channel. Owing to this thin layer, the doping of the channel is removed. This means that the impurity atoms that lie in the semiconductor are completely removed, making it fully depleted.

SCOPE OF THE REPORT:

This report studies the Fully Depleted Silicon-on-insulator (FD-SOI) Technology market status and outlook of Global and major regions, from angles of players, countries, product types and end industries; this report analyzes the top players in global market, and splits the Fully Depleted Silicon-on-insulator (FD-SOI) Technology market by product type and applications/end industries.

The 28nm segment accounted for the major shares in this market due to its high performance and cost-effective platform. The segment comprises of various design blocks to support the various designs available for designers. Additionally, the 28-nm FD-SOI technology is used in designing low-power general-purpose applications processors which is also installed in the smartwatches and GPS applications.

This market study estimates that in terms of geographic regions, the Americas will be the major revenue contributor to the market. The region comprises of major semiconductor fabless companies and collaboration between the companies has contributed to the growth factor. Additionally, the increasing demand for SoCs across

industries has also attracted more investments towards FD-SOI technology. The global Fully Depleted Silicon-on-insulator (FD-SOI) Technology market is valued at xx million USD in 2017 and is expected to reach xx million USD by the end of 2023, growing at a CAGR of xx% between 2017 and 2023.

The Asia-Pacific will occupy for more market share in following years, especially in China, also fast growing India and Southeast Asia regions.

North America, especially The United States, will still play an important role which cannot be ignored. Any changes from United States might affect the development trend of Fully Depleted Silicon-on-insulator (FD-SOI) Technology.

Europe also play important roles in global market, with market size of xx million USD in 2017 and will be xx million USD in 2023, with a CAGR of xx%.

Market Segment by Companies, this report covers

Globalfoundries

Samsung

STMicroelectronics

SOITEC

Dream Chip Technologies

Invecas

Verisilicon

Market Segment by Regions, regional analysis covers

North America (United States, Canada and Mexico)

Europe (Germany, France, UK, Russia and Italy)

Asia-Pacific (China, Japan, Korea, India and Southeast Asia)

South America (Brazil, Argentina, Colombia)

Middle East and Africa (Saudi Arabia, UAE, Egypt, Nigeria and South Africa)

Market Segment by Type, covers

12/10nm

14nm

22nm

28nm

Market Segment by Applications, can be divided into

Electronic Goods

Aerospace & Defense

Automotive

Other

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