

Global FPGA Vision Processing Boards Market 2026 by Manufacturers, Regions, Type and Application, Forecast to 2032

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Abstracts

According to our (Global Info Research) latest study, the global FPGA Vision Processing Boards market size was valued at US\$ 2969 million in 2025 and is forecast to a readjusted size of US\$ 6115 million by 2032 with a CAGR of 10.8% during review period.

In 2025, global FPGA Vision Processing Boards production reached approximately 2.9 million units, with an average global market price of around US\$ 1000 per unit. Annual production capacity is 3.2 million units. Gross Profit Margin: 35%. FPGA Vision Processing Boards are specialized embedded computing platforms that integrate a field-programmable gate array (FPGA) with image processing interfaces and supporting hardware (such as memory, I/O, and high-speed connectivity) to enable real-time, hardware-accelerated processing of visual data. The FPGA Vision Processing Boards industry chain consists of an upstream layer of semiconductor fabrication and design, including FPGA chip vendors (such as programmable logic ICs), high-speed memory, sensors, and supporting electronic components; a midstream layer of board-level design and manufacturing where these components are integrated into vision processing boards with interfaces for cameras, I/O, and edge computing workloads; and a downstream layer of system integrators, OEMs, and end users across industrial automation, machine vision, robotics, aerospace, defense, and medical imaging, where these boards are deployed for real-time, low-latency image acquisition, processing, and decision-making at the edge.

This report is a detailed and comprehensive analysis for global FPGA Vision Processing Boards market. Both quantitative and qualitative analyses are presented by manufacturers, by region & country, by Type and by Application. As the market is

constantly changing, this report explores the competition, supply and demand trends, as well as key factors that contribute to its changing demands across many markets. Company profiles and product examples of selected competitors, along with market share estimates of some of the selected leaders for the year 2025, are provided.

Key Features:

Global FPGA Vision Processing Boards market size and forecasts, in consumption value (\$ Million), sales quantity (K Units), and average selling prices (US\$/Unit), 2021-2032

Global FPGA Vision Processing Boards market size and forecasts by region and country, in consumption value (\$ Million), sales quantity (K Units), and average selling prices (US\$/Unit), 2021-2032

Global FPGA Vision Processing Boards market size and forecasts, by Type and by Application, in consumption value (\$ Million), sales quantity (K Units), and average selling prices (US\$/Unit), 2021-2032

Global FPGA Vision Processing Boards market shares of main players, shipments in revenue (\$ Million), sales quantity (K Units), and ASP (US\$/Unit), 2021-2026

The Primary Objectives in This Report Are:

To determine the size of the total market opportunity of global and key countries

To assess the growth potential for FPGA Vision Processing Boards

To forecast future growth in each product and end-use market

To assess competitive factors affecting the marketplace

This report profiles key players in the global FPGA Vision Processing Boards market based on the following parameters - company overview, sales quantity, revenue, price, gross margin, product portfolio, geographical presence, and key developments. Key companies covered as a part of this study include AMD Xilinx, Intel Corporation, Lattice Semiconductor, Microchip Technology Inc., Achronix Semiconductor Corporation, Digilent Inc., Terasic Technologies, Trenz Electronic GmbH, Avnet Embedded, Arrow

Electronics, etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals.

Market Segmentation

FPGA Vision Processing Boards market is split by Type and by Application. For the period 2021-2032, the growth among segments provides accurate calculations and forecasts for consumption value by Type, and by Application in terms of volume and value. This analysis can help you expand your business by targeting qualified niche markets.

Market segment by Type

FPGA Development Boards (Evaluation & Prototyping Boards)

Embedded FPGA Vision Processing Boards (Industrial Grade)

System-On-Module (SOM) With FPGA Integration

PCIe FPGA Accelerator Cards

Camera-Integrated Vision Processing Systems

Market segment By FPGA Architecture

SRAM-Based FPGA Boards (Reconfigurable High Performance)

Flash-Based FPGA Boards (Low Power, Instant-On)

Antifuse FPGA Boards (Radiation-Resistant, Aerospace Grade)

SoC FPGA Boards (CPU + FPGA Hybrid Architecture)

Market segment By Power Consumption

Ultra-Low Power (50W)

Market segment by Application

Manufacturing

Automotive

Healthcare

Aerospace & Defense

Consumer Electronics

Smart City Infrastructure

Logistics & Warehousing

Major players covered

AMD Xilinx

Intel Corporation

Lattice Semiconductor

Microchip Technology Inc.

Achronix Semiconductor Corporation

Digilent Inc.

Terasic Technologies

Trenz Electronic GmbH

Avnet Embedded

Arrow Electronics

Gowin Semiconductor Corporation

Shanghai Anlogic Infotech Co., Ltd.

Shenzhen Pango Microsystems Co., Ltd.

Intelligence Silicon Technology Co., Ltd.

MYIR Tech Limited

Market segment by region, regional analysis covers

North America (United States, Canada, and Mexico)

Europe (Germany, France, United Kingdom, Russia, Italy, and Rest of Europe)

Asia-Pacific (China, Japan, Korea, India, Southeast Asia, and Australia)

South America (Brazil, Argentina, Colombia, and Rest of South America)

Middle East & Africa (Saudi Arabia, UAE, Egypt, South Africa, and Rest of Middle East & Africa)

The content of the study subjects, includes a total of 15 chapters:

Chapter 1, to describe FPGA Vision Processing Boards product scope, market overview, market estimation caveats and base year.

Chapter 2, to profile the top manufacturers of FPGA Vision Processing Boards, with price, sales quantity, revenue, and global market share of FPGA Vision Processing Boards from 2021 to 2026.

Chapter 3, the FPGA Vision Processing Boards competitive situation, sales quantity, revenue, and global market share of top manufacturers are analyzed

emphatically by landscape contrast.

Chapter 4, the FPGA Vision Processing Boards breakdown data are shown at the regional level, to show the sales quantity, consumption value, and growth by regions, from 2021 to 2032.

Chapter 5 and 6, to segment the sales by Type and by Application, with sales market share and growth rate by Type, by Application, from 2021 to 2032.

Chapter 7, 8, 9, 10 and 11, to break the sales data at the country level, with sales quantity, consumption value, and market share for key countries in the world, from 2021 to 2026. and FPGA Vision Processing Boards market forecast, by regions, by Type, and by Application, with sales and revenue, from 2027 to 2032.

Chapter 12, market dynamics, drivers, restraints, trends, and Porters Five Forces analysis.

Chapter 13, the key raw materials and key suppliers, and industry chain of FPGA Vision Processing Boards.

Chapter 14 and 15, to describe FPGA Vision Processing Boards sales channel, distributors, customers, research findings and conclusion.

Contents

1 MARKET OVERVIEW

1.1 Product Overview and Scope

1.2 Market Estimation Caveats and Base Year

1.3 Market Analysis by Type

1.3.1 Overview: Global FPGA Vision Processing Boards Consumption Value by Type: 2021 Versus 2025 Versus 2032

1.3.2 FPGA Development Boards (Evaluation & Prototyping Boards)

1.3.3 Embedded FPGA Vision Processing Boards (Industrial Grade)

1.3.4 System-On-Module (SOM) With FPGA Integration

1.3.5 PCIe FPGA Accelerator Cards

1.3.6 Camera-Integrated Vision Processing Systems

1.4 Market Analysis By FPGA Architecture

1.4.1 Overview: Global FPGA Vision Processing Boards Consumption Value By FPGA Architecture: 2021 Versus 2025 Versus 2032

1.4.2 SRAM-Based FPGA Boards (Reconfigurable High Performance)

1.4.3 Flash-Based FPGA Boards (Low Power, Instant-On)

1.4.4 Antifuse FPGA Boards (Radiation-Resistant, Aerospace Grade)

1.4.5 SoC FPGA Boards (CPU + FPGA Hybrid Architecture)

1.5 Market Analysis By Power Consumption

1.5.1 Overview: Global FPGA Vision Processing Boards Consumption Value By Power Consumption: 2021 Versus 2025 Versus 2032

1.5.2 Ultra-Low Power (50W)

1.6 Market Analysis by Application

1.6.1 Overview: Global FPGA Vision Processing Boards Consumption Value by Application: 2021 Versus 2025 Versus 2032

1.6.2 Manufacturing

1.6.3 Automotive

1.6.4 Healthcare

1.6.5 Aerospace & Defense

1.6.6 Consumer Electronics

1.6.7 Smart City Infrastructure

1.6.8 Logistics & Warehousing

1.7 Global FPGA Vision Processing Boards Market Size & Forecast

1.7.1 Global FPGA Vision Processing Boards Consumption Value (2021 & 2025 & 2032)

1.7.2 Global FPGA Vision Processing Boards Sales Quantity (2021-2032)

1.7.3 Global FPGA Vision Processing Boards Average Price (2021-2032)

2 MANUFACTURERS PROFILES

2.1 AMD Xilinx

2.1.1 AMD Xilinx Details

2.1.2 AMD Xilinx Major Business

2.1.3 AMD Xilinx FPGA Vision Processing Boards Product and Services

2.1.4 AMD Xilinx FPGA Vision Processing Boards Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)

2.1.5 AMD Xilinx Recent Developments/Updates

2.2 Intel Corporation

2.2.1 Intel Corporation Details

2.2.2 Intel Corporation Major Business

2.2.3 Intel Corporation FPGA Vision Processing Boards Product and Services

2.2.4 Intel Corporation FPGA Vision Processing Boards Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)

2.2.5 Intel Corporation Recent Developments/Updates

2.3 Lattice Semiconductor

2.3.1 Lattice Semiconductor Details

2.3.2 Lattice Semiconductor Major Business

2.3.3 Lattice Semiconductor FPGA Vision Processing Boards Product and Services

2.3.4 Lattice Semiconductor FPGA Vision Processing Boards Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)

2.3.5 Lattice Semiconductor Recent Developments/Updates

2.4 Microchip Technology Inc.

2.4.1 Microchip Technology Inc. Details

2.4.2 Microchip Technology Inc. Major Business

2.4.3 Microchip Technology Inc. FPGA Vision Processing Boards Product and Services

2.4.4 Microchip Technology Inc. FPGA Vision Processing Boards Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)

2.4.5 Microchip Technology Inc. Recent Developments/Updates

2.5 Achronix Semiconductor Corporation

2.5.1 Achronix Semiconductor Corporation Details

2.5.2 Achronix Semiconductor Corporation Major Business

2.5.3 Achronix Semiconductor Corporation FPGA Vision Processing Boards Product and Services

2.5.4 Achronix Semiconductor Corporation FPGA Vision Processing Boards Sales

Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)

2.5.5 Achronix Semiconductor Corporation Recent Developments/Updates

2.6 Digilent Inc.

2.6.1 Digilent Inc. Details

2.6.2 Digilent Inc. Major Business

2.6.3 Digilent Inc. FPGA Vision Processing Boards Product and Services

2.6.4 Digilent Inc. FPGA Vision Processing Boards Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)

2.6.5 Digilent Inc. Recent Developments/Updates

2.7 Terasic Technologies

2.7.1 Terasic Technologies Details

2.7.2 Terasic Technologies Major Business

2.7.3 Terasic Technologies FPGA Vision Processing Boards Product and Services

2.7.4 Terasic Technologies FPGA Vision Processing Boards Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)

2.7.5 Terasic Technologies Recent Developments/Updates

2.8 Trenz Electronic GmbH

2.8.1 Trenz Electronic GmbH Details

2.8.2 Trenz Electronic GmbH Major Business

2.8.3 Trenz Electronic GmbH FPGA Vision Processing Boards Product and Services

2.8.4 Trenz Electronic GmbH FPGA Vision Processing Boards Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)

2.8.5 Trenz Electronic GmbH Recent Developments/Updates

2.9 Avnet Embedded

2.9.1 Avnet Embedded Details

2.9.2 Avnet Embedded Major Business

2.9.3 Avnet Embedded FPGA Vision Processing Boards Product and Services

2.9.4 Avnet Embedded FPGA Vision Processing Boards Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)

2.9.5 Avnet Embedded Recent Developments/Updates

2.10 Arrow Electronics

2.10.1 Arrow Electronics Details

2.10.2 Arrow Electronics Major Business

2.10.3 Arrow Electronics FPGA Vision Processing Boards Product and Services

2.10.4 Arrow Electronics FPGA Vision Processing Boards Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)

2.10.5 Arrow Electronics Recent Developments/Updates

2.11 Gowin Semiconductor Corporation

2.11.1 Gowin Semiconductor Corporation Details

- 2.11.2 Gowin Semiconductor Corporation Major Business
- 2.11.3 Gowin Semiconductor Corporation FPGA Vision Processing Boards Product and Services
- 2.11.4 Gowin Semiconductor Corporation FPGA Vision Processing Boards Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
- 2.11.5 Gowin Semiconductor Corporation Recent Developments/Updates
- 2.12 Shanghai Anlogic Infotech Co., Ltd.
- 2.12.1 Shanghai Anlogic Infotech Co., Ltd. Details
- 2.12.2 Shanghai Anlogic Infotech Co., Ltd. Major Business
- 2.12.3 Shanghai Anlogic Infotech Co., Ltd. FPGA Vision Processing Boards Product and Services
- 2.12.4 Shanghai Anlogic Infotech Co., Ltd. FPGA Vision Processing Boards Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
- 2.12.5 Shanghai Anlogic Infotech Co., Ltd. Recent Developments/Updates
- 2.13 Shenzhen Pango Microsystems Co., Ltd.
- 2.13.1 Shenzhen Pango Microsystems Co., Ltd. Details
- 2.13.2 Shenzhen Pango Microsystems Co., Ltd. Major Business
- 2.13.3 Shenzhen Pango Microsystems Co., Ltd. FPGA Vision Processing Boards Product and Services
- 2.13.4 Shenzhen Pango Microsystems Co., Ltd. FPGA Vision Processing Boards Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
- 2.13.5 Shenzhen Pango Microsystems Co., Ltd. Recent Developments/Updates
- 2.14 Intelligence Silicon Technology Co., Ltd.
- 2.14.1 Intelligence Silicon Technology Co., Ltd. Details
- 2.14.2 Intelligence Silicon Technology Co., Ltd. Major Business
- 2.14.3 Intelligence Silicon Technology Co., Ltd. FPGA Vision Processing Boards Product and Services
- 2.14.4 Intelligence Silicon Technology Co., Ltd. FPGA Vision Processing Boards Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
- 2.14.5 Intelligence Silicon Technology Co., Ltd. Recent Developments/Updates
- 2.15 MYIR Tech Limited
- 2.15.1 MYIR Tech Limited Details
- 2.15.2 MYIR Tech Limited Major Business
- 2.15.3 MYIR Tech Limited FPGA Vision Processing Boards Product and Services
- 2.15.4 MYIR Tech Limited FPGA Vision Processing Boards Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
- 2.15.5 MYIR Tech Limited Recent Developments/Updates

3 COMPETITIVE ENVIRONMENT: FPGA VISION PROCESSING BOARDS BY

MANUFACTURER

- 3.1 Global FPGA Vision Processing Boards Sales Quantity by Manufacturer (2021-2026)
- 3.2 Global FPGA Vision Processing Boards Revenue by Manufacturer (2021-2026)
- 3.3 Global FPGA Vision Processing Boards Average Price by Manufacturer (2021-2026)
- 3.4 Market Share Analysis (2025)
 - 3.4.1 Producer Shipments of FPGA Vision Processing Boards by Manufacturer Revenue (\$MM) and Market Share (%): 2025
 - 3.4.2 Top 3 FPGA Vision Processing Boards Manufacturer Market Share in 2025
 - 3.4.3 Top 6 FPGA Vision Processing Boards Manufacturer Market Share in 2025
- 3.5 FPGA Vision Processing Boards Market: Overall Company Footprint Analysis
 - 3.5.1 FPGA Vision Processing Boards Market: Region Footprint
 - 3.5.2 FPGA Vision Processing Boards Market: Company Product Type Footprint
 - 3.5.3 FPGA Vision Processing Boards Market: Company Product Application Footprint
- 3.6 New Market Entrants and Barriers to Market Entry
- 3.7 Mergers, Acquisition, Agreements, and Collaborations

4 CONSUMPTION ANALYSIS BY REGION

- 4.1 Global FPGA Vision Processing Boards Market Size by Region
 - 4.1.1 Global FPGA Vision Processing Boards Sales Quantity by Region (2021-2032)
 - 4.1.2 Global FPGA Vision Processing Boards Consumption Value by Region (2021-2032)
 - 4.1.3 Global FPGA Vision Processing Boards Average Price by Region (2021-2032)
- 4.2 North America FPGA Vision Processing Boards Consumption Value (2021-2032)
- 4.3 Europe FPGA Vision Processing Boards Consumption Value (2021-2032)
- 4.4 Asia-Pacific FPGA Vision Processing Boards Consumption Value (2021-2032)
- 4.5 South America FPGA Vision Processing Boards Consumption Value (2021-2032)
- 4.6 Middle East & Africa FPGA Vision Processing Boards Consumption Value (2021-2032)

5 MARKET SEGMENT BY TYPE

- 5.1 Global FPGA Vision Processing Boards Sales Quantity by Type (2021-2032)
- 5.2 Global FPGA Vision Processing Boards Consumption Value by Type (2021-2032)
- 5.3 Global FPGA Vision Processing Boards Average Price by Type (2021-2032)

6 MARKET SEGMENT BY APPLICATION

6.1 Global FPGA Vision Processing Boards Sales Quantity by Application (2021-2032)

6.2 Global FPGA Vision Processing Boards Consumption Value by Application (2021-2032)

6.3 Global FPGA Vision Processing Boards Average Price by Application (2021-2032)

7 NORTH AMERICA

7.1 North America FPGA Vision Processing Boards Sales Quantity by Type (2021-2032)

7.2 North America FPGA Vision Processing Boards Sales Quantity by Application (2021-2032)

7.3 North America FPGA Vision Processing Boards Market Size by Country

7.3.1 North America FPGA Vision Processing Boards Sales Quantity by Country (2021-2032)

7.3.2 North America FPGA Vision Processing Boards Consumption Value by Country (2021-2032)

7.3.3 United States Market Size and Forecast (2021-2032)

7.3.4 Canada Market Size and Forecast (2021-2032)

7.3.5 Mexico Market Size and Forecast (2021-2032)

8 EUROPE

8.1 Europe FPGA Vision Processing Boards Sales Quantity by Type (2021-2032)

8.2 Europe FPGA Vision Processing Boards Sales Quantity by Application (2021-2032)

8.3 Europe FPGA Vision Processing Boards Market Size by Country

8.3.1 Europe FPGA Vision Processing Boards Sales Quantity by Country (2021-2032)

8.3.2 Europe FPGA Vision Processing Boards Consumption Value by Country (2021-2032)

8.3.3 Germany Market Size and Forecast (2021-2032)

8.3.4 France Market Size and Forecast (2021-2032)

8.3.5 United Kingdom Market Size and Forecast (2021-2032)

8.3.6 Russia Market Size and Forecast (2021-2032)

8.3.7 Italy Market Size and Forecast (2021-2032)

9 ASIA-PACIFIC

9.1 Asia-Pacific FPGA Vision Processing Boards Sales Quantity by Type (2021-2032)

9.2 Asia-Pacific FPGA Vision Processing Boards Sales Quantity by Application (2021-2032)

9.3 Asia-Pacific FPGA Vision Processing Boards Market Size by Region

9.3.1 Asia-Pacific FPGA Vision Processing Boards Sales Quantity by Region (2021-2032)

9.3.2 Asia-Pacific FPGA Vision Processing Boards Consumption Value by Region (2021-2032)

9.3.3 China Market Size and Forecast (2021-2032)

9.3.4 Japan Market Size and Forecast (2021-2032)

9.3.5 South Korea Market Size and Forecast (2021-2032)

9.3.6 India Market Size and Forecast (2021-2032)

9.3.7 Southeast Asia Market Size and Forecast (2021-2032)

9.3.8 Australia Market Size and Forecast (2021-2032)

10 SOUTH AMERICA

10.1 South America FPGA Vision Processing Boards Sales Quantity by Type (2021-2032)

10.2 South America FPGA Vision Processing Boards Sales Quantity by Application (2021-2032)

10.3 South America FPGA Vision Processing Boards Market Size by Country

10.3.1 South America FPGA Vision Processing Boards Sales Quantity by Country (2021-2032)

10.3.2 South America FPGA Vision Processing Boards Consumption Value by Country (2021-2032)

10.3.3 Brazil Market Size and Forecast (2021-2032)

10.3.4 Argentina Market Size and Forecast (2021-2032)

11 MIDDLE EAST & AFRICA

11.1 Middle East & Africa FPGA Vision Processing Boards Sales Quantity by Type (2021-2032)

11.2 Middle East & Africa FPGA Vision Processing Boards Sales Quantity by Application (2021-2032)

11.3 Middle East & Africa FPGA Vision Processing Boards Market Size by Country

11.3.1 Middle East & Africa FPGA Vision Processing Boards Sales Quantity by Country (2021-2032)

11.3.2 Middle East & Africa FPGA Vision Processing Boards Consumption Value by Country (2021-2032)

- 11.3.3 Turkey Market Size and Forecast (2021-2032)
- 11.3.4 Egypt Market Size and Forecast (2021-2032)
- 11.3.5 Saudi Arabia Market Size and Forecast (2021-2032)
- 11.3.6 South Africa Market Size and Forecast (2021-2032)

12 MARKET DYNAMICS

- 12.1 FPGA Vision Processing Boards Market Drivers
- 12.2 FPGA Vision Processing Boards Market Restraints
- 12.3 FPGA Vision Processing Boards Trends Analysis
- 12.4 Porters Five Forces Analysis
 - 12.4.1 Threat of New Entrants
 - 12.4.2 Bargaining Power of Suppliers
 - 12.4.3 Bargaining Power of Buyers
 - 12.4.4 Threat of Substitutes
 - 12.4.5 Competitive Rivalry

13 RAW MATERIAL AND INDUSTRY CHAIN

- 13.1 Raw Material of FPGA Vision Processing Boards and Key Manufacturers
- 13.2 Manufacturing Costs Percentage of FPGA Vision Processing Boards
- 13.3 FPGA Vision Processing Boards Production Process
- 13.4 Industry Value Chain Analysis

14 SHIPMENTS BY DISTRIBUTION CHANNEL

- 14.1 Sales Channel
 - 14.1.1 Direct to End-User
 - 14.1.2 Distributors
- 14.2 FPGA Vision Processing Boards Typical Distributors
- 14.3 FPGA Vision Processing Boards Typical Customers

15 RESEARCH FINDINGS AND CONCLUSION

16 APPENDIX

- 16.1 Methodology
- 16.2 Research Process and Data Source
- 16.3 Disclaimer

List Of Tables

LIST OF TABLES

Table 1. Global FPGA Vision Processing Boards Consumption Value by Type, (USD Million), 2021 & 2025 & 2032

Table 2. Global FPGA Vision Processing Boards Consumption Value By FPGA Architecture, (USD Million), 2021 & 2025 & 2032

Table 3. Global FPGA Vision Processing Boards Consumption Value By Power Consumption, (USD Million), 2021 & 2025 & 2032

Table 4. Global FPGA Vision Processing Boards Consumption Value by Application, (USD Million), 2021 & 2025 & 2032

Table 5. AMD Xilinx Basic Information, Manufacturing Base and Competitors

Table 6. AMD Xilinx Major Business

Table 7. AMD Xilinx FPGA Vision Processing Boards Product and Services

Table 8. AMD Xilinx FPGA Vision Processing Boards Sales Quantity (K Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 9. AMD Xilinx Recent Developments/Updates

Table 10. Intel Corporation Basic Information, Manufacturing Base and Competitors

Table 11. Intel Corporation Major Business

Table 12. Intel Corporation FPGA Vision Processing Boards Product and Services

Table 13. Intel Corporation FPGA Vision Processing Boards Sales Quantity (K Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 14. Intel Corporation Recent Developments/Updates

Table 15. Lattice Semiconductor Basic Information, Manufacturing Base and Competitors

Table 16. Lattice Semiconductor Major Business

Table 17. Lattice Semiconductor FPGA Vision Processing Boards Product and Services

Table 18. Lattice Semiconductor FPGA Vision Processing Boards Sales Quantity (K Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 19. Lattice Semiconductor Recent Developments/Updates

Table 20. Microchip Technology Inc. Basic Information, Manufacturing Base and Competitors

Table 21. Microchip Technology Inc. Major Business

Table 22. Microchip Technology Inc. FPGA Vision Processing Boards Product and Services

Table 23. Microchip Technology Inc. FPGA Vision Processing Boards Sales Quantity (K

Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 24. Microchip Technology Inc. Recent Developments/Updates

Table 25. Achronix Semiconductor Corporation Basic Information, Manufacturing Base and Competitors

Table 26. Achronix Semiconductor Corporation Major Business

Table 27. Achronix Semiconductor Corporation FPGA Vision Processing Boards Product and Services

Table 28. Achronix Semiconductor Corporation FPGA Vision Processing Boards Sales Quantity (K Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 29. Achronix Semiconductor Corporation Recent Developments/Updates

Table 30. Digilent Inc. Basic Information, Manufacturing Base and Competitors

Table 31. Digilent Inc. Major Business

Table 32. Digilent Inc. FPGA Vision Processing Boards Product and Services

Table 33. Digilent Inc. FPGA Vision Processing Boards Sales Quantity (K Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 34. Digilent Inc. Recent Developments/Updates

Table 35. Terasic Technologies Basic Information, Manufacturing Base and Competitors

Table 36. Terasic Technologies Major Business

Table 37. Terasic Technologies FPGA Vision Processing Boards Product and Services

Table 38. Terasic Technologies FPGA Vision Processing Boards Sales Quantity (K Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 39. Terasic Technologies Recent Developments/Updates

Table 40. Trenz Electronic GmbH Basic Information, Manufacturing Base and Competitors

Table 41. Trenz Electronic GmbH Major Business

Table 42. Trenz Electronic GmbH FPGA Vision Processing Boards Product and Services

Table 43. Trenz Electronic GmbH FPGA Vision Processing Boards Sales Quantity (K Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 44. Trenz Electronic GmbH Recent Developments/Updates

Table 45. Avnet Embedded Basic Information, Manufacturing Base and Competitors

Table 46. Avnet Embedded Major Business

Table 47. Avnet Embedded FPGA Vision Processing Boards Product and Services

Table 48. Avnet Embedded FPGA Vision Processing Boards Sales Quantity (K Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 49. Avnet Embedded Recent Developments/Updates

Table 50. Arrow Electronics Basic Information, Manufacturing Base and Competitors

Table 51. Arrow Electronics Major Business

Table 52. Arrow Electronics FPGA Vision Processing Boards Product and Services

Table 53. Arrow Electronics FPGA Vision Processing Boards Sales Quantity (K Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 54. Arrow Electronics Recent Developments/Updates

Table 55. Gowin Semiconductor Corporation Basic Information, Manufacturing Base and Competitors

Table 56. Gowin Semiconductor Corporation Major Business

Table 57. Gowin Semiconductor Corporation FPGA Vision Processing Boards Product and Services

Table 58. Gowin Semiconductor Corporation FPGA Vision Processing Boards Sales Quantity (K Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 59. Gowin Semiconductor Corporation Recent Developments/Updates

Table 60. Shanghai Anlogic Infotech Co., Ltd. Basic Information, Manufacturing Base and Competitors

Table 61. Shanghai Anlogic Infotech Co., Ltd. Major Business

Table 62. Shanghai Anlogic Infotech Co., Ltd. FPGA Vision Processing Boards Product and Services

Table 63. Shanghai Anlogic Infotech Co., Ltd. FPGA Vision Processing Boards Sales Quantity (K Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 64. Shanghai Anlogic Infotech Co., Ltd. Recent Developments/Updates

Table 65. Shenzhen Pango Microsystems Co., Ltd. Basic Information, Manufacturing Base and Competitors

Table 66. Shenzhen Pango Microsystems Co., Ltd. Major Business

Table 67. Shenzhen Pango Microsystems Co., Ltd. FPGA Vision Processing Boards Product and Services

Table 68. Shenzhen Pango Microsystems Co., Ltd. FPGA Vision Processing Boards Sales Quantity (K Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 69. Shenzhen Pango Microsystems Co., Ltd. Recent Developments/Updates

Table 70. Intelligence Silicon Technology Co., Ltd. Basic Information, Manufacturing

Base and Competitors

Table 71. Intelligence Silicon Technology Co., Ltd. Major Business

Table 72. Intelligence Silicon Technology Co., Ltd. FPGA Vision Processing Boards Product and Services

Table 73. Intelligence Silicon Technology Co., Ltd. FPGA Vision Processing Boards Sales Quantity (K Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 74. Intelligence Silicon Technology Co., Ltd. Recent Developments/Updates

Table 75. MYIR Tech Limited Basic Information, Manufacturing Base and Competitors

Table 76. MYIR Tech Limited Major Business

Table 77. MYIR Tech Limited FPGA Vision Processing Boards Product and Services

Table 78. MYIR Tech Limited FPGA Vision Processing Boards Sales Quantity (K Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 79. MYIR Tech Limited Recent Developments/Updates

Table 80. Global FPGA Vision Processing Boards Sales Quantity by Manufacturer (2021-2026) & (K Units)

Table 81. Global FPGA Vision Processing Boards Revenue by Manufacturer (2021-2026) & (USD Million)

Table 82. Global FPGA Vision Processing Boards Average Price by Manufacturer (2021-2026) & (US\$/Unit)

Table 83. Market Position of Manufacturers in FPGA Vision Processing Boards, (Tier 1, Tier 2, and Tier 3), Based on Revenue in 2025

Table 84. Head Office and FPGA Vision Processing Boards Production Site of Key Manufacturer

Table 85. FPGA Vision Processing Boards Market: Company Product Type Footprint

Table 86. FPGA Vision Processing Boards Market: Company Product Application Footprint

Table 87. FPGA Vision Processing Boards New Market Entrants and Barriers to Market Entry

Table 88. FPGA Vision Processing Boards Mergers, Acquisition, Agreements, and Collaborations

Table 89. Global FPGA Vision Processing Boards Consumption Value by Region (2021-2025-2032) & (USD Million) & CAGR

Table 90. Global FPGA Vision Processing Boards Sales Quantity by Region (2021-2026) & (K Units)

Table 91. Global FPGA Vision Processing Boards Sales Quantity by Region (2027-2032) & (K Units)

Table 92. Global FPGA Vision Processing Boards Consumption Value by Region

(2021-2026) & (USD Million)

Table 93. Global FPGA Vision Processing Boards Consumption Value by Region

(2027-2032) & (USD Million)

Table 94. Global FPGA Vision Processing Boards Average Price by Region

(2021-2026) & (US\$/Unit)

Table 95. Global FPGA Vision Processing Boards Average Price by Region

(2027-2032) & (US\$/Unit)

Table 96. Global FPGA Vision Processing Boards Sales Quantity by Type (2021-2026)
& (K Units)

Table 97. Global FPGA Vision Processing Boards Sales Quantity by Type (2027-2032)
& (K Units)

Table 98. Global FPGA Vision Processing Boards Consumption Value by Type
(2021-2026) & (USD Million)

Table 99. Global FPGA Vision Processing Boards Consumption Value by Type
(2027-2032) & (USD Million)

Table 100. Global FPGA Vision Processing Boards Average Price by Type (2021-2026)
& (US\$/Unit)

Table 101. Global FPGA Vision Processing Boards Average Price by Type (2027-2032)
& (US\$/Unit)

Table 102. Global FPGA Vision Processing Boards Sales Quantity by Application
(2021-2026) & (K Units)

Table 103. Global FPGA Vision Processing Boards Sales Quantity by Application
(2027-2032) & (K Units)

Table 104. Global FPGA Vision Processing Boards Consumption Value by Application
(2021-2026) & (USD Million)

Table 105. Global FPGA Vision Processing Boards Consumption Value by Application
(2027-2032) & (USD Million)

Table 106. Global FPGA Vision Processing Boards Average Price by Application
(2021-2026) & (US\$/Unit)

Table 107. Global FPGA Vision Processing Boards Average Price by Application
(2027-2032) & (US\$/Unit)

Table 108. North America FPGA Vision Processing Boards Sales Quantity by Type
(2021-2026) & (K Units)

Table 109. North America FPGA Vision Processing Boards Sales Quantity by Type
(2027-2032) & (K Units)

Table 110. North America FPGA Vision Processing Boards Sales Quantity by
Application (2021-2026) & (K Units)

Table 111. North America FPGA Vision Processing Boards Sales Quantity by
Application (2027-2032) & (K Units)

Table 112. North America FPGA Vision Processing Boards Sales Quantity by Country (2021-2026) & (K Units)

Table 113. North America FPGA Vision Processing Boards Sales Quantity by Country (2027-2032) & (K Units)

Table 114. North America FPGA Vision Processing Boards Consumption Value by Country (2021-2026) & (USD Million)

Table 115. North America FPGA Vision Processing Boards Consumption Value by Country (2027-2032) & (USD Million)

Table 116. Europe FPGA Vision Processing Boards Sales Quantity by Type (2021-2026) & (K Units)

Table 117. Europe FPGA Vision Processing Boards Sales Quantity by Type (2027-2032) & (K Units)

Table 118. Europe FPGA Vision Processing Boards Sales Quantity by Application (2021-2026) & (K Units)

Table 119. Europe FPGA Vision Processing Boards Sales Quantity by Application (2027-2032) & (K Units)

Table 120. Europe FPGA Vision Processing Boards Sales Quantity by Country (2021-2026) & (K Units)

Table 121. Europe FPGA Vision Processing Boards Sales Quantity by Country (2027-2032) & (K Units)

Table 122. Europe FPGA Vision Processing Boards Consumption Value by Country (2021-2026) & (USD Million)

Table 123. Europe FPGA Vision Processing Boards Consumption Value by Country (2027-2032) & (USD Million)

Table 124. Asia-Pacific FPGA Vision Processing Boards Sales Quantity by Type (2021-2026) & (K Units)

Table 125. Asia-Pacific FPGA Vision Processing Boards Sales Quantity by Type (2027-2032) & (K Units)

Table 126. Asia-Pacific FPGA Vision Processing Boards Sales Quantity by Application (2021-2026) & (K Units)

Table 127. Asia-Pacific FPGA Vision Processing Boards Sales Quantity by Application (2027-2032) & (K Units)

Table 128. Asia-Pacific FPGA Vision Processing Boards Sales Quantity by Region (2021-2026) & (K Units)

Table 129. Asia-Pacific FPGA Vision Processing Boards Sales Quantity by Region (2027-2032) & (K Units)

Table 130. Asia-Pacific FPGA Vision Processing Boards Consumption Value by Region (2021-2026) & (USD Million)

Table 131. Asia-Pacific FPGA Vision Processing Boards Consumption Value by Region

(2027-2032) & (USD Million)

Table 132. South America FPGA Vision Processing Boards Sales Quantity by Type (2021-2026) & (K Units)

Table 133. South America FPGA Vision Processing Boards Sales Quantity by Type (2027-2032) & (K Units)

Table 134. South America FPGA Vision Processing Boards Sales Quantity by Application (2021-2026) & (K Units)

Table 135. South America FPGA Vision Processing Boards Sales Quantity by Application (2027-2032) & (K Units)

Table 136. South America FPGA Vision Processing Boards Sales Quantity by Country (2021-2026) & (K Units)

Table 137. South America FPGA Vision Processing Boards Sales Quantity by Country (2027-2032) & (K Units)

Table 138. South America FPGA Vision Processing Boards Consumption Value by Country (2021-2026) & (USD Million)

Table 139. South America FPGA Vision Processing Boards Consumption Value by Country (2027-2032) & (USD Million)

Table 140. Middle East & Africa FPGA Vision Processing Boards Sales Quantity by Type (2021-2026) & (K Units)

Table 141. Middle East & Africa FPGA Vision Processing Boards Sales Quantity by Type (2027-2032) & (K Units)

Table 142. Middle East & Africa FPGA Vision Processing Boards Sales Quantity by Application (2021-2026) & (K Units)

Table 143. Middle East & Africa FPGA Vision Processing Boards Sales Quantity by Application (2027-2032) & (K Units)

Table 144. Middle East & Africa FPGA Vision Processing Boards Sales Quantity by Country (2021-2026) & (K Units)

Table 145. Middle East & Africa FPGA Vision Processing Boards Sales Quantity by Country (2027-2032) & (K Units)

Table 146. Middle East & Africa FPGA Vision Processing Boards Consumption Value by Country (2021-2026) & (USD Million)

Table 147. Middle East & Africa FPGA Vision Processing Boards Consumption Value by Country (2027-2032) & (USD Million)

Table 148. FPGA Vision Processing Boards Raw Material

Table 149. Key Manufacturers of FPGA Vision Processing Boards Raw Materials

Table 150. FPGA Vision Processing Boards Typical Distributors

Table 151. FPGA Vision Processing Boards Typical Customers

List Of Figures

LIST OF FIGURES

Figure 1. FPGA Vision Processing Boards Picture

Figure 2. Global FPGA Vision Processing Boards Revenue by Type, (USD Million), 2021 & 2025 & 2032

Figure 3. Global FPGA Vision Processing Boards Revenue Market Share by Type in 2025

Figure 4. FPGA Development Boards (Evaluation & Prototyping Boards) Examples

Figure 5. Embedded FPGA Vision Processing Boards (Industrial Grade) Examples

Figure 6. System-On-Module (SOM) With FPGA Integration Examples

Figure 7. PCIe FPGA Accelerator Cards Examples

Figure 8. Camera-Integrated Vision Processing Systems Examples

Figure 9. Global FPGA Vision Processing Boards Revenue By FPGA Architecture, (USD Million), 2021 & 2025 & 2032

Figure 10. Global FPGA Vision Processing Boards Revenue Market Share By FPGA Architecture in 2025

Figure 11. SRAM-Based FPGA Boards (Reconfigurable High Performance) Examples

Figure 12. Flash-Based FPGA Boards (Low Power, Instant-On) Examples

Figure 13. Antifuse FPGA Boards (Radiation-Resistant, Aerospace Grade) Examples

Figure 14. SoC FPGA Boards (CPU + FPGA Hybrid Architecture) Examples

Figure 15. Global FPGA Vision Processing Boards Revenue By Power Consumption, (USD Million), 2021 & 2025 & 2032

Figure 16. Global FPGA Vision Processing Boards Revenue Market Share By Power Consumption in 2025

Figure 17. Ultra-Low Power (50W) Examples

Figure 21. Global FPGA Vision Processing Boards Consumption Value by Application, (USD Million), 2021 & 2025 & 2032

Figure 22. Global FPGA Vision Processing Boards Revenue Market Share by Application in 2025

Figure 23. Manufacturing Examples

Figure 24. Automotive Examples

Figure 25. Healthcare Examples

Figure 26. Aerospace & Defense Examples

Figure 27. Consumer Electronics Examples

Figure 28. Smart City Infrastructure Examples

Figure 29. Logistics & Warehousing Examples

Figure 30. Global FPGA Vision Processing Boards Consumption Value, (USD Million):

2021 & 2025 & 2032

Figure 31. Global FPGA Vision Processing Boards Consumption Value and Forecast (2021-2032) & (USD Million)

Figure 32. Global FPGA Vision Processing Boards Sales Quantity (2021-2032) & (K Units)

Figure 33. Global FPGA Vision Processing Boards Price (2021-2032) & (US\$/Unit)

Figure 34. Global FPGA Vision Processing Boards Sales Quantity Market Share by Manufacturer in 2025

Figure 35. Global FPGA Vision Processing Boards Revenue Market Share by Manufacturer in 2025

Figure 36. Producer Shipments of FPGA Vision Processing Boards by Manufacturer Sales (\$MM) and Market Share (%): 2025

Figure 37. Top 3 FPGA Vision Processing Boards Manufacturer (Revenue) Market Share in 2025

Figure 38. Top 6 FPGA Vision Processing Boards Manufacturer (Revenue) Market Share in 2025

Figure 39. Global FPGA Vision Processing Boards Sales Quantity Market Share by Region (2021-2032)

Figure 40. Global FPGA Vision Processing Boards Consumption Value Market Share by Region (2021-2032)

Figure 41. North America FPGA Vision Processing Boards Consumption Value (2021-2032) & (USD Million)

Figure 42. Europe FPGA Vision Processing Boards Consumption Value (2021-2032) & (USD Million)

Figure 43. Asia-Pacific FPGA Vision Processing Boards Consumption Value (2021-2032) & (USD Million)

Figure 44. South America FPGA Vision Processing Boards Consumption Value (2021-2032) & (USD Million)

Figure 45. Middle East & Africa FPGA Vision Processing Boards Consumption Value (2021-2032) & (USD Million)

Figure 46. Global FPGA Vision Processing Boards Sales Quantity Market Share by Type (2021-2032)

Figure 47. Global FPGA Vision Processing Boards Consumption Value Market Share by Type (2021-2032)

Figure 48. Global FPGA Vision Processing Boards Average Price by Type (2021-2032) & (US\$/Unit)

Figure 49. Global FPGA Vision Processing Boards Sales Quantity Market Share by Application (2021-2032)

Figure 50. Global FPGA Vision Processing Boards Revenue Market Share by

Application (2021-2032)

Figure 51. Global FPGA Vision Processing Boards Average Price by Application (2021-2032) & (US\$/Unit)

Figure 52. North America FPGA Vision Processing Boards Sales Quantity Market Share by Type (2021-2032)

Figure 53. North America FPGA Vision Processing Boards Sales Quantity Market Share by Application (2021-2032)

Figure 54. North America FPGA Vision Processing Boards Sales Quantity Market Share by Country (2021-2032)

Figure 55. North America FPGA Vision Processing Boards Consumption Value Market Share by Country (2021-2032)

Figure 56. United States FPGA Vision Processing Boards Consumption Value (2021-2032) & (USD Million)

Figure 57. Canada FPGA Vision Processing Boards Consumption Value (2021-2032) & (USD Million)

Figure 58. Mexico FPGA Vision Processing Boards Consumption Value (2021-2032) & (USD Million)

Figure 59. Europe FPGA Vision Processing Boards Sales Quantity Market Share by Type (2021-2032)

Figure 60. Europe FPGA Vision Processing Boards Sales Quantity Market Share by Application (2021-2032)

Figure 61. Europe FPGA Vision Processing Boards Sales Quantity Market Share by Country (2021-2032)

Figure 62. Europe FPGA Vision Processing Boards Consumption Value Market Share by Country (2021-2032)

Figure 63. Germany FPGA Vision Processing Boards Consumption Value (2021-2032) & (USD Million)

Figure 64. France FPGA Vision Processing Boards Consumption Value (2021-2032) & (USD Million)

Figure 65. United Kingdom FPGA Vision Processing Boards Consumption Value (2021-2032) & (USD Million)

Figure 66. Russia FPGA Vision Processing Boards Consumption Value (2021-2032) & (USD Million)

Figure 67. Italy FPGA Vision Processing Boards Consumption Value (2021-2032) & (USD Million)

Figure 68. Asia-Pacific FPGA Vision Processing Boards Sales Quantity Market Share by Type (2021-2032)

Figure 69. Asia-Pacific FPGA Vision Processing Boards Sales Quantity Market Share by Application (2021-2032)

Figure 70. Asia-Pacific FPGA Vision Processing Boards Sales Quantity Market Share by Region (2021-2032)

Figure 71. Asia-Pacific FPGA Vision Processing Boards Consumption Value Market Share by Region (2021-2032)

Figure 72. China FPGA Vision Processing Boards Consumption Value (2021-2032) & (USD Million)

Figure 73. Japan FPGA Vision Processing Boards Consumption Value (2021-2032) & (USD Million)

Figure 74. South Korea FPGA Vision Processing Boards Consumption Value (2021-2032) & (USD Million)

Figure 75. India FPGA Vision Processing Boards Consumption Value (2021-2032) & (USD Million)

Figure 76. Southeast Asia FPGA Vision Processing Boards Consumption Value (2021-2032) & (USD Million)

Figure 77. Australia FPGA Vision Processing Boards Consumption Value (2021-2032) & (USD Million)

Figure 78. South America FPGA Vision Processing Boards Sales Quantity Market Share by Type (2021-2032)

Figure 79. South America FPGA Vision Processing Boards Sales Quantity Market Share by Application (2021-2032)

Figure 80. South America FPGA Vision Processing Boards Sales Quantity Market Share by Country (2021-2032)

Figure 81. South America FPGA Vision Processing Boards Consumption Value Market Share by Country (2021-2032)

Figure 82. Brazil FPGA Vision Processing Boards Consumption Value (2021-2032) & (USD Million)

Figure 83. Argentina FPGA Vision Processing Boards Consumption Value (2021-2032) & (USD Million)

Figure 84. Middle East & Africa FPGA Vision Processing Boards Sales Quantity Market Share by Type (2021-2032)

Figure 85. Middle East & Africa FPGA Vision Processing Boards Sales Quantity Market Share by Application (2021-2032)

Figure 86. Middle East & Africa FPGA Vision Processing Boards Sales Quantity Market Share by Country (2021-2032)

Figure 87. Middle East & Africa FPGA Vision Processing Boards Consumption Value Market Share by Country (2021-2032)

Figure 88. Turkey FPGA Vision Processing Boards Consumption Value (2021-2032) & (USD Million)

Figure 89. Egypt FPGA Vision Processing Boards Consumption Value (2021-2032) &

(USD Million)

Figure 90. Saudi Arabia FPGA Vision Processing Boards Consumption Value (2021-2032) & (USD Million)

Figure 91. South Africa FPGA Vision Processing Boards Consumption Value (2021-2032) & (USD Million)

Figure 92. FPGA Vision Processing Boards Market Drivers

Figure 93. FPGA Vision Processing Boards Market Restraints

Figure 94. FPGA Vision Processing Boards Market Trends

Figure 95. Porters Five Forces Analysis

Figure 96. Manufacturing Cost Structure Analysis of FPGA Vision Processing Boards in 2025

Figure 97. Manufacturing Process Analysis of FPGA Vision Processing Boards

Figure 98. FPGA Vision Processing Boards Industrial Chain

Figure 99. Sales Channel: Direct to End-User vs Distributors

Figure 100. Direct Channel Pros & Cons

Figure 101. Indirect Channel Pros & Cons

Figure 102. Methodology

Figure 103. Research Process and Data Source

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