

Global FPGA Acceleration Cards Supply, Demand and Key Producers, 2026-2032

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Abstracts

The global FPGA Acceleration Cards market size is expected to reach \$ 826 million by 2032, rising at a market growth of 9.4% CAGR during the forecast period (2026-2032).

An FPGA acceleration card is a reconfigurable hardware acceleration product centered around an FPGA chip as its core computing unit. Typically taking the form of a PCIe add-in card, it is installed within servers, workstations, edge computing devices, or specialized network equipment. Its core value lies in leveraging resources such as programmable logic, DSPs, on-chip memory, high-speed SerDes, PCIe, Ethernet, and DDR/HBM to offload specific tasks originally executed by CPUs or GPUs directly into hardware logic. These tasks typically involve high parallelism, low latency, data streaming, or protocol processing. By doing so, it achieves lower latency, superior energy efficiency, and more deterministic throughput performance.

FPGA accelerator cards should be positioned as specialized, reconfigurable acceleration hardware within high-performance heterogeneous computing, rather than as direct substitutes for GPU accelerator cards. Their core value lies in low latency, protocol programmability, dataflow processing, network/storage/security offloading, financial trading, video processing, and selected high-bandwidth computing scenarios. AMD Alveo officially covers applications such as Data Analytics, FinTech, HPC, Media Acceleration, and Infrastructure Acceleration, and its product portfolio is categorized into General Compute, FinTech, Networking, Media, and AI Inference. This indicates that the market is essentially scenario-driven rather than general-purpose compute-driven.

From the supply-side perspective, the FPGA accelerator card industry chain shows a two-layer structure. The first layer consists of FPGA/adaptive computing chip platform vendors such as AMD/Xilinx, Altera, and Achronix. The second layer consists of board-

level, SmartNIC, or system-level hardware vendors such as BittWare, Silicom, Alpha Data, Advantech, Shenzhen JetIO Technology, Zhejiang Raymax, and Sugon. BittWare's official materials indicate that its products cover compute, network, storage, and sensor processing accelerators, and that it provides production-grade boards based on Achronix, Altera, and AMD FPGA technologies. Silicom's BlueFjord FPGA SmartNIC, meanwhile, is positioned for server offloading tasks such as encryption, compression, and 5G RAN FEC/LDPC acceleration.

The market growth logic mainly comes from three types of demand. First, cloud data centers are shifting from CPU-centric processing toward offloading of network, storage, security, and virtualization workloads. Second, financial trading, cybersecurity, and communication baseband applications require deterministic low latency and rapid protocol iteration. Third, localization and industry-specific applications are driving Chinese vendors to enter FPGA accelerator cards, domestic SmartNICs, and security-isolation board markets. Sugon's NetFirm-5E2000 is explicitly positioned for smart NICs, machine learning inference, video transcoding, NFV/SDN, and big data analytics. Zhejiang Raymax also lists multiple xFast FPGA accelerator cards on its official website, suggesting that the Chinese market is more strongly driven by industry-specific projects, network/security applications, and localization requirements.

However, this market also faces clear constraints. First, FPGA development has a high technical threshold, requiring customers to have capabilities in RTL, HLS, OpenCL, oneAPI, P4, or related development environments, which makes large-scale adoption slower than GPUs. Second, AI training and large-model inference remain dominated by GPUs and ASICs, while FPGAs are better suited to low-latency inference, network-assisted acceleration, video processing, and customized dataflow workloads. Third, FPGA chips and accelerator cards are relatively expensive.

This report studies the global FPGA Acceleration Cards production, demand, key manufacturers, and key regions.

This report is a detailed and comprehensive analysis of the world market for FPGA Acceleration Cards and provides market size (US\$ million) and Year-over-Year (YoY) Growth, considering 2025 as the base year. This report explores demand trends and competition, as well as details the characteristics of FPGA Acceleration Cards that contribute to its increasing demand across many markets.

Highlights and key features of the study

Global FPGA Acceleration Cards total production and demand, 2021-2032, (K Units)

Global FPGA Acceleration Cards Supply, Demand and Key Producers, 2026-2032

Global FPGA Acceleration Cards total production value, 2021-2032, (USD Million)
Global FPGA Acceleration Cards production by region & country, production, value, CAGR, 2021-2032, (USD Million) & (K Units), (based on production site)
Global FPGA Acceleration Cards consumption by region & country, CAGR, 2021-2032 & (K Units)
U.S. VS China: FPGA Acceleration Cards domestic production, consumption, key domestic manufacturers and share
Global FPGA Acceleration Cards production by manufacturer, production, price, value and market share 2021-2026, (USD Million) & (K Units)
Global FPGA Acceleration Cards production by Type, production, value, CAGR, 2021-2032, (USD Million) & (K Units)
Global FPGA Acceleration Cards production by Application, production, value, CAGR, 2021-2032, (USD Million) & (K Units)

This report profiles key players in the global FPGA Acceleration Cards market based on the following parameters - company overview, production, value, price, gross margin, product portfolio, geographical presence, and key developments. Key companies covered as a part of this study include AMD (Xilinx), Intel (Altera), Achronix Semiconductor Corporation, Advantech, JetIO, Dawning Information Industry Co., Ltd, BittWare, Silicom Ltd, Alpha Data, Zhejiang Raymax Technology, etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals.

Stakeholders would have ease in decision-making through various strategy matrices used in analyzing the World FPGA Acceleration Cards market

Detailed Segmentation:

Each section contains quantitative market data including market by value (US\$ Millions), volume (production, consumption) & (K Units) and average price (US\$/Unit) by manufacturer, by Type, and by Application. Data is given for the years 2021-2032 by year with 2025 as the base year, 2026 as the estimate year, and 2027-2032 as the forecast year.

Global FPGA Acceleration Cards Market, By Region:

United States

China

Europe

Japan

South Korea

ASEAN

India

Rest of World

Global FPGA Acceleration Cards Market, Segmentation by Type:

General-Purpose FPGA Acceleration Cards

AI Inference/Edge AI Acceleration Cards

Others

Global FPGA Acceleration Cards Market, Segmentation by Channel:

PCIe x4

PCIe x8

PCIe x16

Global FPGA Acceleration Cards Market, Segmentation by Application:

Cloud Computing / Data Centers

5G / Telecommunications

Finance

Other

Companies Profiled:

AMD (Xilinx)

Intel (Altera)

Achronix Semiconductor Corporation

Advantech

JetIO

Dawning Information Industry Co., Ltd

BittWare

Silicom Ltd

Alpha Data

Zhejiang Raymax Technology

Resnics

Luisuan Technology

Key Questions Answered:

1. How big is the global FPGA Acceleration Cards market?
2. What is the demand of the global FPGA Acceleration Cards market?
3. What is the year over year growth of the global FPGA Acceleration Cards market?
4. What is the production and production value of the global FPGA Acceleration Cards market?
5. Who are the key producers in the global FPGA Acceleration Cards market?

6. What are the growth factors driving the market demand?

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