

Global FPGA Acceleration Cards Market 2026 by Manufacturers, Regions, Type and Application, Forecast to 2032

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Abstracts

According to our (Global Info Research) latest study, the global FPGA Acceleration Cards market size was valued at US\$ 451 million in 2025 and is forecast to a readjusted size of US\$ 826 million by 2032 with a CAGR of 9.4% during review period.

An FPGA acceleration card is a reconfigurable hardware acceleration product centered around an FPGA chip as its core computing unit. Typically taking the form of a PCIe add-in card, it is installed within servers, workstations, edge computing devices, or specialized network equipment. Its core value lies in leveraging resources such as programmable logic, DSPs, on-chip memory, high-speed SerDes, PCIe, Ethernet, and DDR/HBM to offload specific tasks originally executed by CPUs or GPUs directly into hardware logic. These tasks typically involve high parallelism, low latency, data streaming, or protocol processing. By doing so, it achieves lower latency, superior energy efficiency, and more deterministic throughput performance.

FPGA accelerator cards should be positioned as specialized, reconfigurable acceleration hardware within high-performance heterogeneous computing, rather than as direct substitutes for GPU accelerator cards. Their core value lies in low latency, protocol programmability, dataflow processing, network/storage/security offloading, financial trading, video processing, and selected high-bandwidth computing scenarios. AMD Alveo officially covers applications such as Data Analytics, FinTech, HPC, Media Acceleration, and Infrastructure Acceleration, and its product portfolio is categorized into General Compute, FinTech, Networking, Media, and AI Inference. This indicates that the market is essentially scenario-driven rather than general-purpose compute-driven.

From the supply-side perspective, the FPGA accelerator card industry chain shows a

two-layer structure. The first layer consists of FPGA/adaptive computing chip platform vendors such as AMD/Xilinx, Altera, and Achronix. The second layer consists of board-level, SmartNIC, or system-level hardware vendors such as BittWare, Silicom, Alpha Data, Advantech, Shenzhen JetIO Technology, Zhejiang Raymax, and Sugon. BittWare's official materials indicate that its products cover compute, network, storage, and sensor processing accelerators, and that it provides production-grade boards based on Achronix, Altera, and AMD FPGA technologies. Silicom's BlueFjord FPGA SmartNIC, meanwhile, is positioned for server offloading tasks such as encryption, compression, and 5G RAN FEC/LDPC acceleration.

The market growth logic mainly comes from three types of demand. First, cloud data centers are shifting from CPU-centric processing toward offloading of network, storage, security, and virtualization workloads. Second, financial trading, cybersecurity, and communication baseband applications require deterministic low latency and rapid protocol iteration. Third, localization and industry-specific applications are driving Chinese vendors to enter FPGA accelerator cards, domestic SmartNICs, and security-isolation board markets. Sugon's NetFirm-5E2000 is explicitly positioned for smart NICs, machine learning inference, video transcoding, NFV/SDN, and big data analytics. Zhejiang Raymax also lists multiple xFast FPGA accelerator cards on its official website, suggesting that the Chinese market is more strongly driven by industry-specific projects, network/security applications, and localization requirements.

However, this market also faces clear constraints. First, FPGA development has a high technical threshold, requiring customers to have capabilities in RTL, HLS, OpenCL, oneAPI, P4, or related development environments, which makes large-scale adoption slower than GPUs. Second, AI training and large-model inference remain dominated by GPUs and ASICs, while FPGAs are better suited to low-latency inference, network-assisted acceleration, video processing, and customized dataflow workloads. Third, FPGA chips and accelerator cards are relatively expensive.

This report is a detailed and comprehensive analysis for global FPGA Acceleration Cards market. Both quantitative and qualitative analyses are presented by manufacturers, by region & country, by Type and by Application. As the market is constantly changing, this report explores the competition, supply and demand trends, as well as key factors that contribute to its changing demands across many markets. Company profiles and product examples of selected competitors, along with market share estimates of some of the selected leaders for the year 2025, are provided.

Key Features:

Global FPGA Acceleration Cards market size and forecasts, in consumption value (\$ Million), sales quantity (K Units), and average selling prices (US\$/Unit), 2021-2032

Global FPGA Acceleration Cards market size and forecasts by region and country, in consumption value (\$ Million), sales quantity (K Units), and average selling prices (US\$/Unit), 2021-2032

Global FPGA Acceleration Cards market size and forecasts, by Type and by Application, in consumption value (\$ Million), sales quantity (K Units), and average selling prices (US\$/Unit), 2021-2032

Global FPGA Acceleration Cards market shares of main players, shipments in revenue (\$ Million), sales quantity (K Units), and ASP (US\$/Unit), 2021-2026

The Primary Objectives in This Report Are:

- To determine the size of the total market opportunity of global and key countries

- To assess the growth potential for FPGA Acceleration Cards

- To forecast future growth in each product and end-use market

- To assess competitive factors affecting the marketplace

This report profiles key players in the global FPGA Acceleration Cards market based on the following parameters - company overview, sales quantity, revenue, price, gross margin, product portfolio, geographical presence, and key developments. Key companies covered as a part of this study include AMD (Xilinx), Intel (Altera), Achronix Semiconductor Corporation, Advantech, JetIO, Dawning Information Industry Co., Ltd, BittWare, Silicom Ltd, Alpha Data, Zhejiang Raymax Technology, etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals.

Market Segmentation

FPGA Acceleration Cards market is split by Type and by Application. For the period

2021-2032, the growth among segments provides accurate calculations and forecasts for consumption value by Type, and by Application in terms of volume and value. This analysis can help you expand your business by targeting qualified niche markets.

Market segment by Type

General-Purpose FPGA Acceleration Cards

AI Inference/Edge AI Acceleration Cards

Others

Market segment by Channel

PCIe x4

PCIe x8

PCIe x16

Market segment by Application

Cloud Computing / Data Centers

5G / Telecommunications

Finance

Other

Major players covered

AMD (Xilinx)

Intel (Altera)

Achronix Semiconductor Corporation

Advantech

JetIO

Dawning Information Industry Co., Ltd

BittWare

Silicom Ltd

Alpha Data

Zhejiang Raymax Technology

Resnics

Luisuan Technology

Market segment by region, regional analysis covers

North America (United States, Canada, and Mexico)

Europe (Germany, France, United Kingdom, Russia, Italy, and Rest of Europe)

Asia-Pacific (China, Japan, Korea, India, Southeast Asia, and Australia)

South America (Brazil, Argentina, Colombia, and Rest of South America)

Middle East & Africa (Saudi Arabia, UAE, Egypt, South Africa, and Rest of Middle East & Africa)

The content of the study subjects, includes a total of 15 chapters:

Chapter 1, to describe FPGA Acceleration Cards product scope, market overview, market estimation caveats and base year.

Chapter 2, to profile the top manufacturers of FPGA Acceleration Cards, with price, sales quantity, revenue, and global market share of FPGA Acceleration Cards from 2021 to 2026.

Chapter 3, the FPGA Acceleration Cards competitive situation, sales quantity, revenue, and global market share of top manufacturers are analyzed emphatically by landscape contrast.

Chapter 4, the FPGA Acceleration Cards breakdown data are shown at the regional level, to show the sales quantity, consumption value, and growth by regions, from 2021 to 2032.

Chapter 5 and 6, to segment the sales by Type and by Application, with sales market share and growth rate by Type, by Application, from 2021 to 2032.

Chapter 7, 8, 9, 10 and 11, to break the sales data at the country level, with sales quantity, consumption value, and market share for key countries in the world, from 2021 to 2026. and FPGA Acceleration Cards market forecast, by regions, by Type, and by Application, with sales and revenue, from 2027 to 2032.

Chapter 12, market dynamics, drivers, restraints, trends, and Porters Five Forces analysis.

Chapter 13, the key raw materials and key suppliers, and industry chain of FPGA Acceleration Cards.

Chapter 14 and 15, to describe FPGA Acceleration Cards sales channel, distributors, customers, research findings and conclusion.

Contents

1 MARKET OVERVIEW

1.1 Product Overview and Scope

1.2 Market Estimation Caveats and Base Year

1.3 Market Analysis by Type

1.3.1 Overview: Global FPGA Acceleration Cards Consumption Value by Type: 2021 Versus 2025 Versus 2032

1.3.2 General-Purpose FPGA Acceleration Cards

1.3.3 AI Inference/Edge AI Acceleration Cards

1.3.4 Others

1.4 Market Analysis by Channel

1.4.1 Overview: Global FPGA Acceleration Cards Consumption Value by Channel: 2021 Versus 2025 Versus 2032

1.4.2 PCIe x4

1.4.3 PCIe x8

1.4.4 PCIe x16

1.5 Market Analysis by Application

1.5.1 Overview: Global FPGA Acceleration Cards Consumption Value by Application: 2021 Versus 2025 Versus 2032

1.5.2 Cloud Computing / Data Centers

1.5.3 5G / Telecommunications

1.5.4 Finance

1.5.5 Other

1.6 Global FPGA Acceleration Cards Market Size & Forecast

1.6.1 Global FPGA Acceleration Cards Consumption Value (2021 & 2025 & 2032)

1.6.2 Global FPGA Acceleration Cards Sales Quantity (2021-2032)

1.6.3 Global FPGA Acceleration Cards Average Price (2021-2032)

2 MANUFACTURERS PROFILES

2.1 AMD (Xilinx)

2.1.1 AMD (Xilinx) Details

2.1.2 AMD (Xilinx) Major Business

2.1.3 AMD (Xilinx) FPGA Acceleration Cards Product and Services

2.1.4 AMD (Xilinx) FPGA Acceleration Cards Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)

2.1.5 AMD (Xilinx) Recent Developments/Updates

2.2 Intel (Altera)

2.2.1 Intel (Altera) Details

2.2.2 Intel (Altera) Major Business

2.2.3 Intel (Altera) FPGA Acceleration Cards Product and Services

2.2.4 Intel (Altera) FPGA Acceleration Cards Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)

2.2.5 Intel (Altera) Recent Developments/Updates

2.3 Achronix Semiconductor Corporation

2.3.1 Achronix Semiconductor Corporation Details

2.3.2 Achronix Semiconductor Corporation Major Business

2.3.3 Achronix Semiconductor Corporation FPGA Acceleration Cards Product and Services

2.3.4 Achronix Semiconductor Corporation FPGA Acceleration Cards Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)

2.3.5 Achronix Semiconductor Corporation Recent Developments/Updates

2.4 Advantech

2.4.1 Advantech Details

2.4.2 Advantech Major Business

2.4.3 Advantech FPGA Acceleration Cards Product and Services

2.4.4 Advantech FPGA Acceleration Cards Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)

2.4.5 Advantech Recent Developments/Updates

2.5 JetIO

2.5.1 JetIO Details

2.5.2 JetIO Major Business

2.5.3 JetIO FPGA Acceleration Cards Product and Services

2.5.4 JetIO FPGA Acceleration Cards Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)

2.5.5 JetIO Recent Developments/Updates

2.6 Dawning Information Industry Co., Ltd

2.6.1 Dawning Information Industry Co., Ltd Details

2.6.2 Dawning Information Industry Co., Ltd Major Business

2.6.3 Dawning Information Industry Co., Ltd FPGA Acceleration Cards Product and Services

2.6.4 Dawning Information Industry Co., Ltd FPGA Acceleration Cards Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)

2.6.5 Dawning Information Industry Co., Ltd Recent Developments/Updates

2.7 BittWare

2.7.1 BittWare Details

- 2.7.2 BittWare Major Business
- 2.7.3 BittWare FPGA Acceleration Cards Product and Services
- 2.7.4 BittWare FPGA Acceleration Cards Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
- 2.7.5 BittWare Recent Developments/Updates
- 2.8 Silicom Ltd
 - 2.8.1 Silicom Ltd Details
 - 2.8.2 Silicom Ltd Major Business
 - 2.8.3 Silicom Ltd FPGA Acceleration Cards Product and Services
 - 2.8.4 Silicom Ltd FPGA Acceleration Cards Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
 - 2.8.5 Silicom Ltd Recent Developments/Updates
- 2.9 Alpha Data
 - 2.9.1 Alpha Data Details
 - 2.9.2 Alpha Data Major Business
 - 2.9.3 Alpha Data FPGA Acceleration Cards Product and Services
 - 2.9.4 Alpha Data FPGA Acceleration Cards Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
 - 2.9.5 Alpha Data Recent Developments/Updates
- 2.10 Zhejiang Raymax Technology
 - 2.10.1 Zhejiang Raymax Technology Details
 - 2.10.2 Zhejiang Raymax Technology Major Business
 - 2.10.3 Zhejiang Raymax Technology FPGA Acceleration Cards Product and Services
 - 2.10.4 Zhejiang Raymax Technology FPGA Acceleration Cards Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
 - 2.10.5 Zhejiang Raymax Technology Recent Developments/Updates
- 2.11 Resnics
 - 2.11.1 Resnics Details
 - 2.11.2 Resnics Major Business
 - 2.11.3 Resnics FPGA Acceleration Cards Product and Services
 - 2.11.4 Resnics FPGA Acceleration Cards Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
 - 2.11.5 Resnics Recent Developments/Updates
- 2.12 Luisuan Technology
 - 2.12.1 Luisuan Technology Details
 - 2.12.2 Luisuan Technology Major Business
 - 2.12.3 Luisuan Technology FPGA Acceleration Cards Product and Services
 - 2.12.4 Luisuan Technology FPGA Acceleration Cards Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)

2.12.5 Luisuan Technology Recent Developments/Updates

3 COMPETITIVE ENVIRONMENT: FPGA ACCELERATION CARDS BY MANUFACTURER

- 3.1 Global FPGA Acceleration Cards Sales Quantity by Manufacturer (2021-2026)
- 3.2 Global FPGA Acceleration Cards Revenue by Manufacturer (2021-2026)
- 3.3 Global FPGA Acceleration Cards Average Price by Manufacturer (2021-2026)
- 3.4 Market Share Analysis (2025)
 - 3.4.1 Producer Shipments of FPGA Acceleration Cards by Manufacturer Revenue (\$MM) and Market Share (%): 2025
 - 3.4.2 Top 3 FPGA Acceleration Cards Manufacturer Market Share in 2025
 - 3.4.3 Top 6 FPGA Acceleration Cards Manufacturer Market Share in 2025
- 3.5 FPGA Acceleration Cards Market: Overall Company Footprint Analysis
 - 3.5.1 FPGA Acceleration Cards Market: Region Footprint
 - 3.5.2 FPGA Acceleration Cards Market: Company Product Type Footprint
 - 3.5.3 FPGA Acceleration Cards Market: Company Product Application Footprint
- 3.6 New Market Entrants and Barriers to Market Entry
- 3.7 Mergers, Acquisition, Agreements, and Collaborations

4 CONSUMPTION ANALYSIS BY REGION

- 4.1 Global FPGA Acceleration Cards Market Size by Region
 - 4.1.1 Global FPGA Acceleration Cards Sales Quantity by Region (2021-2032)
 - 4.1.2 Global FPGA Acceleration Cards Consumption Value by Region (2021-2032)
 - 4.1.3 Global FPGA Acceleration Cards Average Price by Region (2021-2032)
- 4.2 North America FPGA Acceleration Cards Consumption Value (2021-2032)
- 4.3 Europe FPGA Acceleration Cards Consumption Value (2021-2032)
- 4.4 Asia-Pacific FPGA Acceleration Cards Consumption Value (2021-2032)
- 4.5 South America FPGA Acceleration Cards Consumption Value (2021-2032)
- 4.6 Middle East & Africa FPGA Acceleration Cards Consumption Value (2021-2032)

5 MARKET SEGMENT BY TYPE

- 5.1 Global FPGA Acceleration Cards Sales Quantity by Type (2021-2032)
- 5.2 Global FPGA Acceleration Cards Consumption Value by Type (2021-2032)
- 5.3 Global FPGA Acceleration Cards Average Price by Type (2021-2032)

6 MARKET SEGMENT BY APPLICATION

- 6.1 Global FPGA Acceleration Cards Sales Quantity by Application (2021-2032)
- 6.2 Global FPGA Acceleration Cards Consumption Value by Application (2021-2032)
- 6.3 Global FPGA Acceleration Cards Average Price by Application (2021-2032)

7 NORTH AMERICA

- 7.1 North America FPGA Acceleration Cards Sales Quantity by Type (2021-2032)
- 7.2 North America FPGA Acceleration Cards Sales Quantity by Application (2021-2032)
- 7.3 North America FPGA Acceleration Cards Market Size by Country
 - 7.3.1 North America FPGA Acceleration Cards Sales Quantity by Country (2021-2032)
 - 7.3.2 North America FPGA Acceleration Cards Consumption Value by Country (2021-2032)
 - 7.3.3 United States Market Size and Forecast (2021-2032)
 - 7.3.4 Canada Market Size and Forecast (2021-2032)
 - 7.3.5 Mexico Market Size and Forecast (2021-2032)

8 EUROPE

- 8.1 Europe FPGA Acceleration Cards Sales Quantity by Type (2021-2032)
- 8.2 Europe FPGA Acceleration Cards Sales Quantity by Application (2021-2032)
- 8.3 Europe FPGA Acceleration Cards Market Size by Country
 - 8.3.1 Europe FPGA Acceleration Cards Sales Quantity by Country (2021-2032)
 - 8.3.2 Europe FPGA Acceleration Cards Consumption Value by Country (2021-2032)
 - 8.3.3 Germany Market Size and Forecast (2021-2032)
 - 8.3.4 France Market Size and Forecast (2021-2032)
 - 8.3.5 United Kingdom Market Size and Forecast (2021-2032)
 - 8.3.6 Russia Market Size and Forecast (2021-2032)
 - 8.3.7 Italy Market Size and Forecast (2021-2032)

9 ASIA-PACIFIC

- 9.1 Asia-Pacific FPGA Acceleration Cards Sales Quantity by Type (2021-2032)
- 9.2 Asia-Pacific FPGA Acceleration Cards Sales Quantity by Application (2021-2032)
- 9.3 Asia-Pacific FPGA Acceleration Cards Market Size by Region
 - 9.3.1 Asia-Pacific FPGA Acceleration Cards Sales Quantity by Region (2021-2032)
 - 9.3.2 Asia-Pacific FPGA Acceleration Cards Consumption Value by Region (2021-2032)
 - 9.3.3 China Market Size and Forecast (2021-2032)

- 9.3.4 Japan Market Size and Forecast (2021-2032)
- 9.3.5 South Korea Market Size and Forecast (2021-2032)
- 9.3.6 India Market Size and Forecast (2021-2032)
- 9.3.7 Southeast Asia Market Size and Forecast (2021-2032)
- 9.3.8 Australia Market Size and Forecast (2021-2032)

10 SOUTH AMERICA

- 10.1 South America FPGA Acceleration Cards Sales Quantity by Type (2021-2032)
- 10.2 South America FPGA Acceleration Cards Sales Quantity by Application (2021-2032)
- 10.3 South America FPGA Acceleration Cards Market Size by Country
 - 10.3.1 South America FPGA Acceleration Cards Sales Quantity by Country (2021-2032)
 - 10.3.2 South America FPGA Acceleration Cards Consumption Value by Country (2021-2032)
 - 10.3.3 Brazil Market Size and Forecast (2021-2032)
 - 10.3.4 Argentina Market Size and Forecast (2021-2032)

11 MIDDLE EAST & AFRICA

- 11.1 Middle East & Africa FPGA Acceleration Cards Sales Quantity by Type (2021-2032)
- 11.2 Middle East & Africa FPGA Acceleration Cards Sales Quantity by Application (2021-2032)
- 11.3 Middle East & Africa FPGA Acceleration Cards Market Size by Country
 - 11.3.1 Middle East & Africa FPGA Acceleration Cards Sales Quantity by Country (2021-2032)
 - 11.3.2 Middle East & Africa FPGA Acceleration Cards Consumption Value by Country (2021-2032)
 - 11.3.3 Turkey Market Size and Forecast (2021-2032)
 - 11.3.4 Egypt Market Size and Forecast (2021-2032)
 - 11.3.5 Saudi Arabia Market Size and Forecast (2021-2032)
 - 11.3.6 South Africa Market Size and Forecast (2021-2032)

12 MARKET DYNAMICS

- 12.1 FPGA Acceleration Cards Market Drivers
- 12.2 FPGA Acceleration Cards Market Restraints

12.3 FPGA Acceleration Cards Trends Analysis

12.4 Porters Five Forces Analysis

12.4.1 Threat of New Entrants

12.4.2 Bargaining Power of Suppliers

12.4.3 Bargaining Power of Buyers

12.4.4 Threat of Substitutes

12.4.5 Competitive Rivalry

13 RAW MATERIAL AND INDUSTRY CHAIN

13.1 Raw Material of FPGA Acceleration Cards and Key Manufacturers

13.2 Manufacturing Costs Percentage of FPGA Acceleration Cards

13.3 FPGA Acceleration Cards Production Process

13.4 Industry Value Chain Analysis

14 SHIPMENTS BY DISTRIBUTION CHANNEL

14.1 Sales Channel

14.1.1 Direct to End-User

14.1.2 Distributors

14.2 FPGA Acceleration Cards Typical Distributors

14.3 FPGA Acceleration Cards Typical Customers

15 RESEARCH FINDINGS AND CONCLUSION

16 APPENDIX

16.1 Methodology

16.2 Research Process and Data Source

16.3 Disclaimer

List Of Tables

LIST OF TABLES

Table 1. Global FPGA Acceleration Cards Consumption Value by Type, (USD Million), 2021 & 2025 & 2032

Table 2. Global FPGA Acceleration Cards Consumption Value by Channel, (USD Million), 2021 & 2025 & 2032

Table 3. Global FPGA Acceleration Cards Consumption Value by Application, (USD Million), 2021 & 2025 & 2032

Table 4. AMD (Xilinx) Basic Information, Manufacturing Base and Competitors

Table 5. AMD (Xilinx) Major Business

Table 6. AMD (Xilinx) FPGA Acceleration Cards Product and Services

Table 7. AMD (Xilinx) FPGA Acceleration Cards Sales Quantity (K Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 8. AMD (Xilinx) Recent Developments/Updates

Table 9. Intel (Altera) Basic Information, Manufacturing Base and Competitors

Table 10. Intel (Altera) Major Business

Table 11. Intel (Altera) FPGA Acceleration Cards Product and Services

Table 12. Intel (Altera) FPGA Acceleration Cards Sales Quantity (K Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 13. Intel (Altera) Recent Developments/Updates

Table 14. Achronix Semiconductor Corporation Basic Information, Manufacturing Base and Competitors

Table 15. Achronix Semiconductor Corporation Major Business

Table 16. Achronix Semiconductor Corporation FPGA Acceleration Cards Product and Services

Table 17. Achronix Semiconductor Corporation FPGA Acceleration Cards Sales Quantity (K Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 18. Achronix Semiconductor Corporation Recent Developments/Updates

Table 19. Advantech Basic Information, Manufacturing Base and Competitors

Table 20. Advantech Major Business

Table 21. Advantech FPGA Acceleration Cards Product and Services

Table 22. Advantech FPGA Acceleration Cards Sales Quantity (K Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 23. Advantech Recent Developments/Updates

Table 24. JetIO Basic Information, Manufacturing Base and Competitors

Table 25. JetIO Major Business

Table 26. JetIO FPGA Acceleration Cards Product and Services
Table 27. JetIO FPGA Acceleration Cards Sales Quantity (K Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)
Table 28. JetIO Recent Developments/Updates
Table 29. Dawning Information Industry Co., Ltd Basic Information, Manufacturing Base and Competitors
Table 30. Dawning Information Industry Co., Ltd Major Business
Table 31. Dawning Information Industry Co., Ltd FPGA Acceleration Cards Product and Services
Table 32. Dawning Information Industry Co., Ltd FPGA Acceleration Cards Sales Quantity (K Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)
Table 33. Dawning Information Industry Co., Ltd Recent Developments/Updates
Table 34. BittWare Basic Information, Manufacturing Base and Competitors
Table 35. BittWare Major Business
Table 36. BittWare FPGA Acceleration Cards Product and Services
Table 37. BittWare FPGA Acceleration Cards Sales Quantity (K Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)
Table 38. BittWare Recent Developments/Updates
Table 39. Silicom Ltd Basic Information, Manufacturing Base and Competitors
Table 40. Silicom Ltd Major Business
Table 41. Silicom Ltd FPGA Acceleration Cards Product and Services
Table 42. Silicom Ltd FPGA Acceleration Cards Sales Quantity (K Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)
Table 43. Silicom Ltd Recent Developments/Updates
Table 44. Alpha Data Basic Information, Manufacturing Base and Competitors
Table 45. Alpha Data Major Business
Table 46. Alpha Data FPGA Acceleration Cards Product and Services
Table 47. Alpha Data FPGA Acceleration Cards Sales Quantity (K Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)
Table 48. Alpha Data Recent Developments/Updates
Table 49. Zhejiang Raymax Technology Basic Information, Manufacturing Base and Competitors
Table 50. Zhejiang Raymax Technology Major Business
Table 51. Zhejiang Raymax Technology FPGA Acceleration Cards Product and Services
Table 52. Zhejiang Raymax Technology FPGA Acceleration Cards Sales Quantity (K Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 53. Zhejiang Raymax Technology Recent Developments/Updates
Table 54. Resnics Basic Information, Manufacturing Base and Competitors
Table 55. Resnics Major Business
Table 56. Resnics FPGA Acceleration Cards Product and Services
Table 57. Resnics FPGA Acceleration Cards Sales Quantity (K Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)
Table 58. Resnics Recent Developments/Updates
Table 59. Luisuan Technology Basic Information, Manufacturing Base and Competitors
Table 60. Luisuan Technology Major Business
Table 61. Luisuan Technology FPGA Acceleration Cards Product and Services
Table 62. Luisuan Technology FPGA Acceleration Cards Sales Quantity (K Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)
Table 63. Luisuan Technology Recent Developments/Updates
Table 64. Global FPGA Acceleration Cards Sales Quantity by Manufacturer (2021-2026) & (K Units)
Table 65. Global FPGA Acceleration Cards Revenue by Manufacturer (2021-2026) & (USD Million)
Table 66. Global FPGA Acceleration Cards Average Price by Manufacturer (2021-2026) & (US\$/Unit)
Table 67. Market Position of Manufacturers in FPGA Acceleration Cards, (Tier 1, Tier 2, and Tier 3), Based on Revenue in 2025
Table 68. Head Office and FPGA Acceleration Cards Production Site of Key Manufacturer
Table 69. FPGA Acceleration Cards Market: Company Product Type Footprint
Table 70. FPGA Acceleration Cards Market: Company Product Application Footprint
Table 71. FPGA Acceleration Cards New Market Entrants and Barriers to Market Entry
Table 72. FPGA Acceleration Cards Mergers, Acquisition, Agreements, and Collaborations
Table 73. Global FPGA Acceleration Cards Consumption Value by Region (2021-2025-2032) & (USD Million) & CAGR
Table 74. Global FPGA Acceleration Cards Sales Quantity by Region (2021-2026) & (K Units)
Table 75. Global FPGA Acceleration Cards Sales Quantity by Region (2027-2032) & (K Units)
Table 76. Global FPGA Acceleration Cards Consumption Value by Region (2021-2026) & (USD Million)
Table 77. Global FPGA Acceleration Cards Consumption Value by Region (2027-2032) & (USD Million)

Table 78. Global FPGA Acceleration Cards Average Price by Region (2021-2026) & (US\$/Unit)

Table 79. Global FPGA Acceleration Cards Average Price by Region (2027-2032) & (US\$/Unit)

Table 80. Global FPGA Acceleration Cards Sales Quantity by Type (2021-2026) & (K Units)

Table 81. Global FPGA Acceleration Cards Sales Quantity by Type (2027-2032) & (K Units)

Table 82. Global FPGA Acceleration Cards Consumption Value by Type (2021-2026) & (USD Million)

Table 83. Global FPGA Acceleration Cards Consumption Value by Type (2027-2032) & (USD Million)

Table 84. Global FPGA Acceleration Cards Average Price by Type (2021-2026) & (US\$/Unit)

Table 85. Global FPGA Acceleration Cards Average Price by Type (2027-2032) & (US\$/Unit)

Table 86. Global FPGA Acceleration Cards Sales Quantity by Application (2021-2026) & (K Units)

Table 87. Global FPGA Acceleration Cards Sales Quantity by Application (2027-2032) & (K Units)

Table 88. Global FPGA Acceleration Cards Consumption Value by Application (2021-2026) & (USD Million)

Table 89. Global FPGA Acceleration Cards Consumption Value by Application (2027-2032) & (USD Million)

Table 90. Global FPGA Acceleration Cards Average Price by Application (2021-2026) & (US\$/Unit)

Table 91. Global FPGA Acceleration Cards Average Price by Application (2027-2032) & (US\$/Unit)

Table 92. North America FPGA Acceleration Cards Sales Quantity by Type (2021-2026) & (K Units)

Table 93. North America FPGA Acceleration Cards Sales Quantity by Type (2027-2032) & (K Units)

Table 94. North America FPGA Acceleration Cards Sales Quantity by Application (2021-2026) & (K Units)

Table 95. North America FPGA Acceleration Cards Sales Quantity by Application (2027-2032) & (K Units)

Table 96. North America FPGA Acceleration Cards Sales Quantity by Country (2021-2026) & (K Units)

Table 97. North America FPGA Acceleration Cards Sales Quantity by Country

(2027-2032) & (K Units)

Table 98. North America FPGA Acceleration Cards Consumption Value by Country (2021-2026) & (USD Million)

Table 99. North America FPGA Acceleration Cards Consumption Value by Country (2027-2032) & (USD Million)

Table 100. Europe FPGA Acceleration Cards Sales Quantity by Type (2021-2026) & (K Units)

Table 101. Europe FPGA Acceleration Cards Sales Quantity by Type (2027-2032) & (K Units)

Table 102. Europe FPGA Acceleration Cards Sales Quantity by Application (2021-2026) & (K Units)

Table 103. Europe FPGA Acceleration Cards Sales Quantity by Application (2027-2032) & (K Units)

Table 104. Europe FPGA Acceleration Cards Sales Quantity by Country (2021-2026) & (K Units)

Table 105. Europe FPGA Acceleration Cards Sales Quantity by Country (2027-2032) & (K Units)

Table 106. Europe FPGA Acceleration Cards Consumption Value by Country (2021-2026) & (USD Million)

Table 107. Europe FPGA Acceleration Cards Consumption Value by Country (2027-2032) & (USD Million)

Table 108. Asia-Pacific FPGA Acceleration Cards Sales Quantity by Type (2021-2026) & (K Units)

Table 109. Asia-Pacific FPGA Acceleration Cards Sales Quantity by Type (2027-2032) & (K Units)

Table 110. Asia-Pacific FPGA Acceleration Cards Sales Quantity by Application (2021-2026) & (K Units)

Table 111. Asia-Pacific FPGA Acceleration Cards Sales Quantity by Application (2027-2032) & (K Units)

Table 112. Asia-Pacific FPGA Acceleration Cards Sales Quantity by Region (2021-2026) & (K Units)

Table 113. Asia-Pacific FPGA Acceleration Cards Sales Quantity by Region (2027-2032) & (K Units)

Table 114. Asia-Pacific FPGA Acceleration Cards Consumption Value by Region (2021-2026) & (USD Million)

Table 115. Asia-Pacific FPGA Acceleration Cards Consumption Value by Region (2027-2032) & (USD Million)

Table 116. South America FPGA Acceleration Cards Sales Quantity by Type (2021-2026) & (K Units)

Table 117. South America FPGA Acceleration Cards Sales Quantity by Type
(2027-2032) & (K Units)

Table 118. South America FPGA Acceleration Cards Sales Quantity by Application
(2021-2026) & (K Units)

Table 119. South America FPGA Acceleration Cards Sales Quantity by Application
(2027-2032) & (K Units)

Table 120. South America FPGA Acceleration Cards Sales Quantity by Country
(2021-2026) & (K Units)

Table 121. South America FPGA Acceleration Cards Sales Quantity by Country
(2027-2032) & (K Units)

Table 122. South America FPGA Acceleration Cards Consumption Value by Country
(2021-2026) & (USD Million)

Table 123. South America FPGA Acceleration Cards Consumption Value by Country
(2027-2032) & (USD Million)

Table 124. Middle East & Africa FPGA Acceleration Cards Sales Quantity by Type
(2021-2026) & (K Units)

Table 125. Middle East & Africa FPGA Acceleration Cards Sales Quantity by Type
(2027-2032) & (K Units)

Table 126. Middle East & Africa FPGA Acceleration Cards Sales Quantity by
Application (2021-2026) & (K Units)

Table 127. Middle East & Africa FPGA Acceleration Cards Sales Quantity by
Application (2027-2032) & (K Units)

Table 128. Middle East & Africa FPGA Acceleration Cards Sales Quantity by Country
(2021-2026) & (K Units)

Table 129. Middle East & Africa FPGA Acceleration Cards Sales Quantity by Country
(2027-2032) & (K Units)

Table 130. Middle East & Africa FPGA Acceleration Cards Consumption Value by
Country (2021-2026) & (USD Million)

Table 131. Middle East & Africa FPGA Acceleration Cards Consumption Value by
Country (2027-2032) & (USD Million)

Table 132. FPGA Acceleration Cards Raw Material

Table 133. Key Manufacturers of FPGA Acceleration Cards Raw Materials

Table 134. FPGA Acceleration Cards Typical Distributors

Table 135. FPGA Acceleration Cards Typical Customers

List Of Figures

LIST OF FIGURES

Figure 1. FPGA Acceleration Cards Picture

Figure 2. Global FPGA Acceleration Cards Revenue by Type, (USD Million), 2021 & 2025 & 2032

Figure 3. Global FPGA Acceleration Cards Revenue Market Share by Type in 2025

Figure 4. General-Purpose FPGA Acceleration Cards Examples

Figure 5. AI Inference/Edge AI Acceleration Cards Examples

Figure 6. Others Examples

Figure 7. Global FPGA Acceleration Cards Revenue by Channel, (USD Million), 2021 & 2025 & 2032

Figure 8. Global FPGA Acceleration Cards Revenue Market Share by Channel in 2025

Figure 9. PCIe x4 Examples

Figure 10. PCIe x8 Examples

Figure 11. PCIe x16 Examples

Figure 12. Global FPGA Acceleration Cards Consumption Value by Application, (USD Million), 2021 & 2025 & 2032

Figure 13. Global FPGA Acceleration Cards Revenue Market Share by Application in 2025

Figure 14. Cloud Computing / Data Centers Examples

Figure 15. 5G / Telecommunications Examples

Figure 16. Finance Examples

Figure 17. Other Examples

Figure 18. Global FPGA Acceleration Cards Consumption Value, (USD Million): 2021 & 2025 & 2032

Figure 19. Global FPGA Acceleration Cards Consumption Value and Forecast (2021-2032) & (USD Million)

Figure 20. Global FPGA Acceleration Cards Sales Quantity (2021-2032) & (K Units)

Figure 21. Global FPGA Acceleration Cards Price (2021-2032) & (US\$/Unit)

Figure 22. Global FPGA Acceleration Cards Sales Quantity Market Share by Manufacturer in 2025

Figure 23. Global FPGA Acceleration Cards Revenue Market Share by Manufacturer in 2025

Figure 24. Producer Shipments of FPGA Acceleration Cards by Manufacturer Sales (\$MM) and Market Share (%): 2025

Figure 25. Top 3 FPGA Acceleration Cards Manufacturer (Revenue) Market Share in 2025

Figure 26. Top 6 FPGA Acceleration Cards Manufacturer (Revenue) Market Share in 2025

Figure 27. Global FPGA Acceleration Cards Sales Quantity Market Share by Region (2021-2032)

Figure 28. Global FPGA Acceleration Cards Consumption Value Market Share by Region (2021-2032)

Figure 29. North America FPGA Acceleration Cards Consumption Value (2021-2032) & (USD Million)

Figure 30. Europe FPGA Acceleration Cards Consumption Value (2021-2032) & (USD Million)

Figure 31. Asia-Pacific FPGA Acceleration Cards Consumption Value (2021-2032) & (USD Million)

Figure 32. South America FPGA Acceleration Cards Consumption Value (2021-2032) & (USD Million)

Figure 33. Middle East & Africa FPGA Acceleration Cards Consumption Value (2021-2032) & (USD Million)

Figure 34. Global FPGA Acceleration Cards Sales Quantity Market Share by Type (2021-2032)

Figure 35. Global FPGA Acceleration Cards Consumption Value Market Share by Type (2021-2032)

Figure 36. Global FPGA Acceleration Cards Average Price by Type (2021-2032) & (US\$/Unit)

Figure 37. Global FPGA Acceleration Cards Sales Quantity Market Share by Application (2021-2032)

Figure 38. Global FPGA Acceleration Cards Revenue Market Share by Application (2021-2032)

Figure 39. Global FPGA Acceleration Cards Average Price by Application (2021-2032) & (US\$/Unit)

Figure 40. North America FPGA Acceleration Cards Sales Quantity Market Share by Type (2021-2032)

Figure 41. North America FPGA Acceleration Cards Sales Quantity Market Share by Application (2021-2032)

Figure 42. North America FPGA Acceleration Cards Sales Quantity Market Share by Country (2021-2032)

Figure 43. North America FPGA Acceleration Cards Consumption Value Market Share by Country (2021-2032)

Figure 44. United States FPGA Acceleration Cards Consumption Value (2021-2032) & (USD Million)

Figure 45. Canada FPGA Acceleration Cards Consumption Value (2021-2032) & (USD

Million)

Figure 46. Mexico FPGA Acceleration Cards Consumption Value (2021-2032) & (USD Million)

Figure 47. Europe FPGA Acceleration Cards Sales Quantity Market Share by Type (2021-2032)

Figure 48. Europe FPGA Acceleration Cards Sales Quantity Market Share by Application (2021-2032)

Figure 49. Europe FPGA Acceleration Cards Sales Quantity Market Share by Country (2021-2032)

Figure 50. Europe FPGA Acceleration Cards Consumption Value Market Share by Country (2021-2032)

Figure 51. Germany FPGA Acceleration Cards Consumption Value (2021-2032) & (USD Million)

Figure 52. France FPGA Acceleration Cards Consumption Value (2021-2032) & (USD Million)

Figure 53. United Kingdom FPGA Acceleration Cards Consumption Value (2021-2032) & (USD Million)

Figure 54. Russia FPGA Acceleration Cards Consumption Value (2021-2032) & (USD Million)

Figure 55. Italy FPGA Acceleration Cards Consumption Value (2021-2032) & (USD Million)

Figure 56. Asia-Pacific FPGA Acceleration Cards Sales Quantity Market Share by Type (2021-2032)

Figure 57. Asia-Pacific FPGA Acceleration Cards Sales Quantity Market Share by Application (2021-2032)

Figure 58. Asia-Pacific FPGA Acceleration Cards Sales Quantity Market Share by Region (2021-2032)

Figure 59. Asia-Pacific FPGA Acceleration Cards Consumption Value Market Share by Region (2021-2032)

Figure 60. China FPGA Acceleration Cards Consumption Value (2021-2032) & (USD Million)

Figure 61. Japan FPGA Acceleration Cards Consumption Value (2021-2032) & (USD Million)

Figure 62. South Korea FPGA Acceleration Cards Consumption Value (2021-2032) & (USD Million)

Figure 63. India FPGA Acceleration Cards Consumption Value (2021-2032) & (USD Million)

Figure 64. Southeast Asia FPGA Acceleration Cards Consumption Value (2021-2032) & (USD Million)

Figure 65. Australia FPGA Acceleration Cards Consumption Value (2021-2032) & (USD Million)

Figure 66. South America FPGA Acceleration Cards Sales Quantity Market Share by Type (2021-2032)

Figure 67. South America FPGA Acceleration Cards Sales Quantity Market Share by Application (2021-2032)

Figure 68. South America FPGA Acceleration Cards Sales Quantity Market Share by Country (2021-2032)

Figure 69. South America FPGA Acceleration Cards Consumption Value Market Share by Country (2021-2032)

Figure 70. Brazil FPGA Acceleration Cards Consumption Value (2021-2032) & (USD Million)

Figure 71. Argentina FPGA Acceleration Cards Consumption Value (2021-2032) & (USD Million)

Figure 72. Middle East & Africa FPGA Acceleration Cards Sales Quantity Market Share by Type (2021-2032)

Figure 73. Middle East & Africa FPGA Acceleration Cards Sales Quantity Market Share by Application (2021-2032)

Figure 74. Middle East & Africa FPGA Acceleration Cards Sales Quantity Market Share by Country (2021-2032)

Figure 75. Middle East & Africa FPGA Acceleration Cards Consumption Value Market Share by Country (2021-2032)

Figure 76. Turkey FPGA Acceleration Cards Consumption Value (2021-2032) & (USD Million)

Figure 77. Egypt FPGA Acceleration Cards Consumption Value (2021-2032) & (USD Million)

Figure 78. Saudi Arabia FPGA Acceleration Cards Consumption Value (2021-2032) & (USD Million)

Figure 79. South Africa FPGA Acceleration Cards Consumption Value (2021-2032) & (USD Million)

Figure 80. FPGA Acceleration Cards Market Drivers

Figure 81. FPGA Acceleration Cards Market Restraints

Figure 82. FPGA Acceleration Cards Market Trends

Figure 83. Porters Five Forces Analysis

Figure 84. Manufacturing Cost Structure Analysis of FPGA Acceleration Cards in 2025

Figure 85. Manufacturing Process Analysis of FPGA Acceleration Cards

Figure 86. FPGA Acceleration Cards Industrial Chain

Figure 87. Sales Channel: Direct to End-User vs Distributors

Figure 88. Direct Channel Pros & Cons

Figure 89. Indirect Channel Pros & Cons

Figure 90. Methodology

Figure 91. Research Process and Data Source

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