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Global EDA Tools for IC Design Supply, Demand and Key Producers, 2026-2032

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Abstracts

The global EDA Tools for IC Design market size is expected to reach $ 9420 million by 2032, rising at a market growth of 6.1% CAGR during the forecast period (2026-2032).

Electronic Design Automation (EDA) is primarily a software business. Very sophisticated and complex software programs function primarily in one of three ways to assist with the design and manufacture of chips:

Simulation tools take a description of a proposed circuit and predict its behavior before is it implemented.

Design tools take a description of a proposed circuit function and assemble the collection of circuit elements that implement that function. This is both a logical process (assemble and connect the circuit elements) and a physical process (create the interconnected geometric shapes that will implement the circuit during manufacturing). These tools are delivered as a combination of fully automated and interactively guided capabilities.

Verification tools examine either the logical or physical representation of the chip to determine if the resultant design is connected correctly and will deliver the required performance.

The IC design EDA tool market continues its rapid growth, primarily driven by multiple factors including advanced process technology evolution, soaring chip complexity, accelerated system-level innovation, and the restructuring of the global semiconductor supply chain. As process nodes enter 3nm and below, design rules become increasingly complex, and physical effects have a greater impact on chip performance,

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forcing design teams to rely on smarter, more precise EDA tools to achieve timing convergence, power optimization, and reliability verification. At the same time, emerging applications such as artificial intelligence, high-performance computing, 5G/6G, and autonomous driving are generating a large demand for customized chips, driving the automation and intelligent upgrade of the entire process from architecture exploration to physical implementation.

This report studies the global EDA Tools for IC Design demand, key companies, and key regions.

This report is a detailed and comprehensive analysis of the world market for EDA Tools for IC Design, and provides market size (US$ million) and Year-over-Year (YoY) growth, considering 2025 as the base year. This report explores demand trends and competition, as well as details the characteristics of EDA Tools for IC Design that contribute to its increasing demand across many markets.

Highlights and key features of the study
Global EDA Tools for IC Design total market, 2021-2032, (USD Million)
Global EDA Tools for IC Design total market by region & country, CAGR, 2021-2032, (USD Million)
U.S. VS China: EDA Tools for IC Design total market, key domestic companies, and share, (USD Million)
Global EDA Tools for IC Design revenue by player, revenue and market share 2021-2026, (USD Million)
Global EDA Tools for IC Design total market by Type, CAGR, 2021-2032, (USD Million)
Global EDA Tools for IC Design total market by Application, CAGR, 2021-2032, (USD Million)

This report profiles major players in the global EDA Tools for IC Design market based on the following parameters - company overview, revenue, gross margin, product portfolio, geographical presence, and key developments. Key companies covered as a part of this study include Synopsys, Cadence, Siemens EDA, Silvaco, Concept Engineering, Defacto Technologies, Agnisys, AMIQ EDA, Infinisim, Arteris, etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals.

Stakeholders would have ease in decision-making through various strategy matrices used in analyzing the world EDA Tools for IC Design market

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Detailed Segmentation:

Each section contains quantitative market data including market by value (US$ Millions), by player, by regions, by Type, and by Application. Data is given for the years 2021-2032 by year with 2025 as the base year, 2026 as the estimate year, and 2027-2032 as the forecast year.

Global EDA Tools for IC Design Market, By Region:

United States

China

Europe

Japan

South Korea

ASEAN

India

Rest of World

Global EDA Tools for IC Design Market, Segmentation by Type:

Digital IC Frontend (FE) Design

Digital IC Backend (BE) Design

Analog IC Design

Global EDA Tools for IC Design Market, Segmentation by Deployment Mode:

Cloud-based

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On-premises

Global EDA Tools for IC Design Market, Segmentation by Business Model:

Perpetual License

Subscription

Others

Global EDA Tools for IC Design Market, Segmentation by Application:

Automotive

IT and Telecommunications

Industrial Automation

Consumer Electronics

Healthcare Devices

Others

Companies Profiled:

Synopsys

Cadence

Siemens EDA

Silvaco

Concept Engineering

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Defacto Technologies

Agnisys

AMIQ EDA

Infinisim

Arteris

Lorentz Solution

Empyrean Technology

Xpeedic

Semitronix

Faraday Dynamics

MircoScape Technology

Primarius Technologies

Arcas-tech

UniVista Industrial Software

Shanghai LEDA Technology

Phlexing Technology

Robei EDA

HyperSilicon

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X-EPIC

Huaxin Jushu

Key Questions Answered

1. How big is the global EDA Tools for IC Design market?
2. What is the demand of the global EDA Tools for IC Design market?
3. What is the year over year growth of the global EDA Tools for IC Design market?
4. What is the total value of the global EDA Tools for IC Design market?
5. Who are the Major Players in the global EDA Tools for IC Design market?
6. What are the growth factors driving the market demand?

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