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Global EDA Tools for Digital IC Design Market 2026 by Company, Regions, Type and Application, Forecast to 2032

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Abstracts

According to our (Global Info Research) latest study, the global EDA Tools for Digital IC Design market size was valued at US$ 4329 million in 2025 and is forecast to a readjusted size of US$ 6629 million by 2032 with a CAGR of 6.3% during review period.

Electronic Design Automation (EDA) is primarily a software business. Very sophisticated and complex software programs function primarily in one of three ways to assist with the design and manufacture of chips:

Simulation tools take a description of a proposed circuit and predict its behavior before is it implemented.

Design tools take a description of a proposed circuit function and assemble the collection of circuit elements that implement that function. This is both a logical process (assemble and connect the circuit elements) and a physical process (create the interconnected geometric shapes that will implement the circuit during manufacturing). These tools are delivered as a combination of fully automated and interactively guided capabilities.

Verification tools examine either the logical or physical representation of the chip to determine if the resultant design is connected correctly and will deliver the required performance.

The digital IC design EDA tool market continues its rapid growth, primarily driven by multiple factors including advanced process technology evolution, soaring chip complexity, accelerated system-level innovation, and the restructuring of the global

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semiconductor supply chain. As process nodes enter 3nm and below, design rules become increasingly complex, and physical effects have a greater impact on chip performance, forcing design teams to rely on smarter, more precise EDA tools to achieve timing convergence, power optimization, and reliability verification. At the same time, emerging applications such as artificial intelligence, high-performance computing, 5G/6G, and autonomous driving are generating a large demand for customized chips, driving the automation and intelligent upgrade of the entire process from architecture exploration to physical implementation.

This report is a detailed and comprehensive analysis for global EDA Tools for Digital IC Design market. Both quantitative and qualitative analyses are presented by company, by region & country, by Type and by Application. As the market is constantly changing, this report explores the competition, supply and demand trends, as well as key factors that contribute to its changing demands across many markets. Company profiles and product examples of selected competitors, along with market share estimates of some of the selected leaders for the year 2025, are provided.

Key Features:

Global EDA Tools for Digital IC Design market size and forecasts, in consumption value ($ Million), 2021-2032

Global EDA Tools for Digital IC Design market size and forecasts by region and country, in consumption value ($ Million), 2021-2032

Global EDA Tools for Digital IC Design market size and forecasts, by Type and by Application, in consumption value ($ Million), 2021-2032

Global EDA Tools for Digital IC Design market shares of main players, in revenue ($ Million), 2021-2026

The Primary Objectives in This Report Are:
To determine the size of the total market opportunity of global and key countries
To assess the growth potential for EDA Tools for Digital IC Design
To forecast future growth in each product and end-use market
To assess competitive factors affecting the marketplace

This report profiles key players in the global EDA Tools for Digital IC Design market based on the following parameters - company overview, revenue, gross margin, product

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portfolio, geographical presence, and key developments. Key companies covered as a part of this study include Synopsys, Cadence, Siemens EDA, Silvaco, Agnisys, Empyrean Technology, Xpeedic, Semitronix, Faraday Dynamics, MircoScape Technology, etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals.

Market segmentation

EDA Tools for Digital IC Design market is split by Type and by Application. For the period 2021-2032, the growth among segments provides accurate calculations and forecasts for Consumption Value by Type and by Application. This analysis can help you expand your business by targeting qualified niche markets.

Market segment by Type

Digital IC Frontend (FE) Design

Digital IC Backend (BE) Design

Market segment by Deployment Mode

Cloud-based

On-premises

Market segment by Business Model

Perpetual License

Subscription

Others

Market segment by Application

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Automotive

IT and Telecommunications

Industrial Automation

Consumer Electronics

Healthcare Devices

Others

Market segment by players, this report covers

Synopsys

Cadence

Siemens EDA

Silvaco

Agnisys

Empyrean Technology

Xpeedic

Semitronix

Faraday Dynamics

MircoScape Technology

Primarius Technologies

Arcas-tech

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UniVista Industrial Software

Shanghai LEDA Technology

Phlexing Technology

Robei EDA

HyperSilicon

S2C

X-EPIC

Huaxin Jushu

Market segment by regions, regional analysis covers
North America (United States, Canada and Mexico)
Europe (Germany, France, UK, Russia, Italy and Rest of Europe)
Asia-Pacific (China, Japan, South Korea, India, Southeast Asia and Rest of Asia-Pacific)
South America (Brazil, Rest of South America)
Middle East & Africa (Turkey, Saudi Arabia, UAE, Rest of Middle East & Africa)

The content of the study subjects, includes a total of 13 chapters:

Chapter 1, to describe EDA Tools for Digital IC Design product scope, market overview, market estimation caveats and base year.

Chapter 2, to profile the top players of EDA Tools for Digital IC Design, with revenue, gross margin, and global market share of EDA Tools for Digital IC Design from 2021 to 2026.

Chapter 3, the EDA Tools for Digital IC Design competitive situation, revenue, and global market share of top players are analyzed emphatically by landscape contrast.

Chapter 4 and 5, to segment the market size by Type and by Application, with consumption value and growth rate by Type, by Application, from 2021 to 2032.

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Chapter 6, 7, 8, 9, and 10, to break the market size data at the country level, with revenue and market share for key countries in the world, from 2021 to 2026.and EDA Tools for Digital IC Design market forecast, by regions, by Type and by Application, with consumption value, from 2027 to 2032.

Chapter 11, market dynamics, drivers, restraints, trends, Porters Five Forces analysis.

Chapter 12, the key raw materials and key suppliers, and industry chain of EDA Tools for Digital IC Design.

Chapter 13, to describe EDA Tools for Digital IC Design research findings and conclusion.

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Table 151. Global EDA Tools for Digital IC Design Typical Customers

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