

# Global CuNiAu Bumping Market 2026 by Manufacturers, Regions, Type and Application, Forecast to 2032

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## Abstracts

According to our (Global Info Research) latest study, the global CuNiAu Bumping market size was valued at US\$ 510 million in 2025 and is forecast to a readjusted size of US\$ 798 million by 2032 with a CAGR of 6.7% during review period.

CuNiAu bumping refers to a high-reliability, multi-layer metallization process used in advanced semiconductor packaging, where a copper (Cu) pillar is formed on the wafer surface, followed by a nickel (Ni) barrier layer and a gold (Au) capping layer. This bumping structure offers excellent electrical conductivity, oxidation resistance, and thermal stability, making it ideal for fine-pitch interconnects and advanced integration schemes. CuNiAu bumps are typically fabricated via electroplating and can be classified into standard-size bumps (>100??m), micro-bumps (40–100??m), and ultra-fine-pitch bumps (

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