

Global Cloud AI Training Accelerator Chips Supply, Demand and Key Producers, 2026-2032

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Abstracts

The global Cloud AI Training Accelerator Chips market size is expected to reach \$ 797078 million by 2032, rising at a market growth of 18.1% CAGR during the forecast period (2026-2032).

Cloud AI Training Accelerator Chips refer to high-performance semiconductor devices, accelerator cards, OAM modules and system-level compute units deployed in cloud data centers, AI supercomputers, hyperscale AI factories and enterprise-scale training clusters to accelerate pre-training, continued training, fine-tuning, reinforcement learning, mixture-of-experts training and selected training-inference converged workloads. These products are typically implemented as data center GPUs, GPGPUs, custom ASICs, TPUs, NPU, XPU, RDU, IPU or wafer-scale engines, and are characterized by high matrix-compute throughput, low-precision floating-point support, high-bandwidth memory, high-speed chip-to-chip interconnect, distributed training communication, software stacks, compilers and compatibility with mainstream AI frameworks.

From an industry structure perspective, cloud AI training accelerator chips have moved beyond single-chip peak performance competition into a full-stack, system-level efficiency race. Large-scale AI training requires not only tensor throughput, but also high-bandwidth memory, reliable inter-chip communication, distributed training software, framework compatibility, cluster orchestration and power-efficient deployment. Therefore, the core scope of this market should focus on training-capable cloud accelerators, including data center GPUs, GPGPUs, TPUs, NPU, XPU, RDU, IPU and wafer-scale engines. Servers, CPUs, networking chips, edge AI chips and inference-only devices should be separated from the narrow revenue model. This definition is important because many AI accelerators are increasingly marketed as ?training and

inference? products, while a meaningful market model must still distinguish chips that can support pre-training, continued training or large-scale fine-tuning from chips optimized only for inference serving.

Demand growth is being driven by hyperscale cloud capex, AI-native model companies, sovereign AI programs, AI cloud providers and enterprise/private model adoption. In 2025 and 2026, the market is still heavily weighted toward training-capable GPUs, but the mix is changing as custom ASICs, TPUs, Trainium-class chips and regional alternatives enter large-scale deployment. The line between training and inference is becoming less rigid because model providers increasingly need the same infrastructure for pre-training, post-training, fine-tuning, reinforcement learning, batch inference and model refresh cycles. As a result, the most competitive products will be those that combine high compute density, large memory capacity, high scale-up bandwidth, efficient low-precision formats, robust software migration tools and credible supply continuity.

From a policy and technology perspective, export controls on advanced computing chips have accelerated regional supply-chain divergence. They raise the cost and complexity of AI infrastructure in China, while simultaneously increasing the strategic value of domestic AI accelerators. However, local substitution is not a simple chip-for-chip replacement: software ecosystem maturity, compiler quality, collective communication performance, HBM availability, advanced packaging capacity and customer validation remain decisive. The sector remains structurally attractive, but the next phase of competition will be defined by usable platform performance, not headline FLOPS alone.

This report studies the global Cloud AI Training Accelerator Chips production, demand, key manufacturers, and key regions.

This report is a detailed and comprehensive analysis of the world market for Cloud AI Training Accelerator Chips and provides market size (US\$ million) and Year-over-Year (YoY) Growth, considering 2025 as the base year. This report explores demand trends and competition, as well as details the characteristics of Cloud AI Training Accelerator Chips that contribute to its increasing demand across many markets.

Highlights and key features of the study

Global Cloud AI Training Accelerator Chips total production and demand, 2021-2032, (Million Units)

Global Cloud AI Training Accelerator Chips Supply, Demand and Key Producers, 2026-2032

Global Cloud AI Training Accelerator Chips total production value, 2021-2032, (USD Million)

Global Cloud AI Training Accelerator Chips production by region & country, production, value, CAGR, 2021-2032, (USD Million) & (Million Units), (based on production site)

Global Cloud AI Training Accelerator Chips consumption by region & country, CAGR, 2021-2032 & (Million Units)

U.S. VS China: Cloud AI Training Accelerator Chips domestic production, consumption, key domestic manufacturers and share

Global Cloud AI Training Accelerator Chips production by manufacturer, production, price, value and market share 2021-2026, (USD Million) & (Million Units)

Global Cloud AI Training Accelerator Chips production by Type, production, value, CAGR, 2021-2032, (USD Million) & (Million Units)

Global Cloud AI Training Accelerator Chips production by Application, production, value, CAGR, 2021-2032, (USD Million) & (Million Units)

This report profiles key players in the global Cloud AI Training Accelerator Chips market based on the following parameters - company overview, production, value, price, gross margin, product portfolio, geographical presence, and key developments. Key companies covered as a part of this study include NVIDIA Corporation, Advanced Micro Devices, Inc., Broadcom Inc., Google LLC, Amazon Web Services, Inc., Huawei Technologies Co., Ltd., Intel Corporation, Marvell Technology, Inc., Cambricon Technologies Corporation Limited, Kunlunxin Technology, etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals.

Stakeholders would have ease in decision-making through various strategy matrices used in analyzing the World Cloud AI Training Accelerator Chips market

Detailed Segmentation:

Each section contains quantitative market data including market by value (US\$ Millions), volume (production, consumption) & (Million Units) and average price (US\$/Unit) by manufacturer, by Type, and by Application. Data is given for the years 2021-2032 by year with 2025 as the base year, 2026 as the estimate year, and 2027-2032 as the forecast year.

Global Cloud AI Training Accelerator Chips Market, By Region:

United States

China

Europe

Japan

South Korea

ASEAN

India

Rest of World

Global Cloud AI Training Accelerator Chips Market, Segmentation by Type:

Data Center GPU / GPGPU

AI ASIC / XPU

Other

Global Cloud AI Training Accelerator Chips Market, Segmentation by Numeric Precision:

FP64 / FP32 Focused

TF32 / BF16 Training

FP16 Training

FP8 / Low-precision Training

INT8 / Mixed Precision

Other Precision Formats

Global Cloud AI Training Accelerator Chips Market, Segmentation by Interconnect Capability:

Single-card / Standalone Accelerator

Multi-card Server Interconnect

Other

Global Cloud AI Training Accelerator Chips Market, Segmentation by Application:

Cloud & AI Infrastructure

Scientific Research & Healthcare

Automotive & Industrial AI

Other

Companies Profiled:

NVIDIA Corporation

Advanced Micro Devices, Inc.

Broadcom Inc.

Google LLC

Amazon Web Services, Inc.

Huawei Technologies Co., Ltd.

Intel Corporation

Marvell Technology, Inc.

Cambricon Technologies Corporation Limited

Kunlunxin Technology

MetaX Integrated Circuits Co., Ltd.

Moore Threads Technology Co., Ltd.

Hygon Information Technology Co., Ltd.

Iluvatar CoreX

Shanghai Biren Technology Co., Ltd.

Shanghai Enflame Technology Co., Ltd.

Cerebras Systems, Inc.

Graphcore Limited

Preferred Networks, Inc.

SambaNova Systems, Inc.

Tenstorrent Inc.

Microsoft Corporation

Meta Platforms, Inc.

Key Questions Answered:

1. How big is the global Cloud AI Training Accelerator Chips market?
2. What is the demand of the global Cloud AI Training Accelerator Chips market?
3. What is the year over year growth of the global Cloud AI Training Accelerator Chips market?

4. What is the production and production value of the global Cloud AI Training Accelerator Chips market?
5. Who are the key producers in the global Cloud AI Training Accelerator Chips market?
6. What are the growth factors driving the market demand?

Contents

1 SUPPLY SUMMARY

- 1.1 Cloud AI Training Accelerator Chips Introduction
- 1.2 World Cloud AI Training Accelerator Chips Supply & Forecast
 - 1.2.1 World Cloud AI Training Accelerator Chips Production Value (2021 & 2025 & 2032)
 - 1.2.2 World Cloud AI Training Accelerator Chips Production (2021-2032)
 - 1.2.3 World Cloud AI Training Accelerator Chips Pricing Trends (2021-2032)
- 1.3 World Cloud AI Training Accelerator Chips Production by Region (Based on Production Site)
 - 1.3.1 World Cloud AI Training Accelerator Chips Production Value by Region (2021-2032)
 - 1.3.2 World Cloud AI Training Accelerator Chips Production by Region (2021-2032)
 - 1.3.3 World Cloud AI Training Accelerator Chips Average Price by Region (2021-2032)
 - 1.3.4 North America Cloud AI Training Accelerator Chips Production (2021-2032)
 - 1.3.5 China Cloud AI Training Accelerator Chips Production (2021-2032)
 - 1.3.6 Japan Cloud AI Training Accelerator Chips Production (2021-2032)
- 1.4 Market Drivers, Restraints and Trends
 - 1.4.1 Cloud AI Training Accelerator Chips Market Drivers
 - 1.4.2 Factors Affecting Demand
 - 1.4.3 Cloud AI Training Accelerator Chips Major Market Trends

2 DEMAND SUMMARY

- 2.1 World Cloud AI Training Accelerator Chips Demand (2021-2032)
- 2.2 World Cloud AI Training Accelerator Chips Consumption by Region
 - 2.2.1 World Cloud AI Training Accelerator Chips Consumption by Region (2021-2026)
 - 2.2.2 World Cloud AI Training Accelerator Chips Consumption Forecast by Region (2027-2032)
- 2.3 United States Cloud AI Training Accelerator Chips Consumption (2021-2032)
- 2.4 China Cloud AI Training Accelerator Chips Consumption (2021-2032)
- 2.5 Europe Cloud AI Training Accelerator Chips Consumption (2021-2032)
- 2.6 Japan Cloud AI Training Accelerator Chips Consumption (2021-2032)
- 2.7 South Korea Cloud AI Training Accelerator Chips Consumption (2021-2032)
- 2.8 ASEAN Cloud AI Training Accelerator Chips Consumption (2021-2032)
- 2.9 India Cloud AI Training Accelerator Chips Consumption (2021-2032)

3 WORLD MANUFACTURERS COMPETITIVE ANALYSIS

3.1 World Cloud AI Training Accelerator Chips Production Value by Manufacturer (2021-2026)

3.2 World Cloud AI Training Accelerator Chips Production by Manufacturer (2021-2026)

3.3 World Cloud AI Training Accelerator Chips Average Price by Manufacturer (2021-2026)

3.4 Cloud AI Training Accelerator Chips Company Evaluation Quadrant

3.5 Industry Rank and Concentration Rate (CR)

3.5.1 Global Cloud AI Training Accelerator Chips Industry Rank of Major Manufacturers

3.5.2 Global Concentration Ratios (CR4) for Cloud AI Training Accelerator Chips in 2025

3.5.3 Global Concentration Ratios (CR8) for Cloud AI Training Accelerator Chips in 2025

3.6 Cloud AI Training Accelerator Chips Market: Overall Company Footprint Analysis

3.6.1 Cloud AI Training Accelerator Chips Market: Region Footprint

3.6.2 Cloud AI Training Accelerator Chips Market: Company Product Type Footprint

3.6.3 Cloud AI Training Accelerator Chips Market: Company Product Application Footprint

3.7 Competitive Environment

3.7.1 Historical Structure of the Industry

3.7.2 Barriers of Market Entry

3.7.3 Factors of Competition

3.8 New Entrant and Capacity Expansion Plans

3.9 Mergers, Acquisition, Agreements, and Collaborations

4 UNITED STATES VS CHINA VS REST OF THE WORLD

4.1 United States VS China: Cloud AI Training Accelerator Chips Production Value Comparison

4.1.1 United States VS China: Cloud AI Training Accelerator Chips Production Value Comparison (2021 & 2025 & 2032)

4.1.2 United States VS China: Cloud AI Training Accelerator Chips Production Value Market Share Comparison (2021 & 2025 & 2032)

4.2 United States VS China: Cloud AI Training Accelerator Chips Production Comparison

4.2.1 United States VS China: Cloud AI Training Accelerator Chips Production Comparison (2021 & 2025 & 2032)

4.2.2 United States VS China: Cloud AI Training Accelerator Chips Production Market Share Comparison (2021 & 2025 & 2032)

4.3 United States VS China: Cloud AI Training Accelerator Chips Consumption Comparison

4.3.1 United States VS China: Cloud AI Training Accelerator Chips Consumption Comparison (2021 & 2025 & 2032)

4.3.2 United States VS China: Cloud AI Training Accelerator Chips Consumption Market Share Comparison (2021 & 2025 & 2032)

4.4 United States Based Cloud AI Training Accelerator Chips Manufacturers and Market Share, 2021-2026

4.4.1 United States Based Cloud AI Training Accelerator Chips Manufacturers, Headquarters and Production Site (States, Country)

4.4.2 United States Based Manufacturers Cloud AI Training Accelerator Chips Production Value (2021-2026)

4.4.3 United States Based Manufacturers Cloud AI Training Accelerator Chips Production (2021-2026)

4.5 China Based Cloud AI Training Accelerator Chips Manufacturers and Market Share

4.5.1 China Based Cloud AI Training Accelerator Chips Manufacturers, Headquarters and Production Site (Province, Country)

4.5.2 China Based Manufacturers Cloud AI Training Accelerator Chips Production Value (2021-2026)

4.5.3 China Based Manufacturers Cloud AI Training Accelerator Chips Production (2021-2026)

4.6 Rest of World Based Cloud AI Training Accelerator Chips Manufacturers and Market Share, 2021-2026

4.6.1 Rest of World Based Cloud AI Training Accelerator Chips Manufacturers, Headquarters and Production Site (State, Country)

4.6.2 Rest of World Based Manufacturers Cloud AI Training Accelerator Chips Production Value (2021-2026)

4.6.3 Rest of World Based Manufacturers Cloud AI Training Accelerator Chips Production (2021-2026)

5 MARKET ANALYSIS BY TYPE

5.1 World Cloud AI Training Accelerator Chips Market Size Overview by Type: 2021 VS 2025 VS 2032

5.2 Segment Introduction by Type

5.2.1 Data Center GPU / GPGPU

5.2.2 AI ASIC / XPU

5.2.3 Other

5.3 Market Segment by Type

5.3.1 World Cloud AI Training Accelerator Chips Production by Type (2021-2032)

5.3.2 World Cloud AI Training Accelerator Chips Production Value by Type (2021-2032)

5.3.3 World Cloud AI Training Accelerator Chips Average Price by Type (2021-2032)

6 MARKET ANALYSIS BY NUMERIC PRECISION

6.1 World Cloud AI Training Accelerator Chips Market Size Overview by Numeric Precision: 2021 VS 2025 VS 2032

6.2 Segment Introduction by Numeric Precision

6.2.1 FP64 / FP32 Focused

6.2.2 TF32 / BF16 Training

6.2.3 FP16 Training

6.2.4 FP8 / Low-precision Training

6.2.5 INT8 / Mixed Precision

6.2.6 Other Precision Formats

6.3 Market Segment by Numeric Precision

6.3.1 World Cloud AI Training Accelerator Chips Production by Numeric Precision (2021-2032)

6.3.2 World Cloud AI Training Accelerator Chips Production Value by Numeric Precision (2021-2032)

6.3.3 World Cloud AI Training Accelerator Chips Average Price by Numeric Precision (2021-2032)

7 MARKET ANALYSIS BY INTERCONNECT CAPABILITY

7.1 World Cloud AI Training Accelerator Chips Market Size Overview by Interconnect Capability: 2021 VS 2025 VS 2032

7.2 Segment Introduction by Interconnect Capability

7.2.1 Single-card / Standalone Accelerator

7.2.2 Multi-card Server Interconnect

7.2.3 Other

7.3 Market Segment by Interconnect Capability

7.3.1 World Cloud AI Training Accelerator Chips Production by Interconnect Capability (2021-2032)

7.3.2 World Cloud AI Training Accelerator Chips Production Value by Interconnect Capability (2021-2032)

7.3.3 World Cloud AI Training Accelerator Chips Average Price by Interconnect Capability (2021-2032)

8 MARKET ANALYSIS BY APPLICATION

8.1 World Cloud AI Training Accelerator Chips Market Size Overview by Application: 2021 VS 2025 VS 2032

8.2 Segment Introduction by Application

8.2.1 Cloud & AI Infrastructure

8.2.2 Scientific Research & Healthcare

8.2.3 Automotive & Industrial AI

8.2.4 Other

8.3 Market Segment by Application

8.3.1 World Cloud AI Training Accelerator Chips Production by Application (2021-2032)

8.3.2 World Cloud AI Training Accelerator Chips Production Value by Application (2021-2032)

8.3.3 World Cloud AI Training Accelerator Chips Average Price by Application (2021-2032)

9 COMPANY PROFILES

9.1 NVIDIA Corporation

9.1.1 NVIDIA Corporation Details

9.1.2 NVIDIA Corporation Major Business

9.1.3 NVIDIA Corporation Cloud AI Training Accelerator Chips Product and Services

9.1.4 NVIDIA Corporation Cloud AI Training Accelerator Chips Production, Price, Value, Gross Margin and Market Share (2021-2026)

9.1.5 NVIDIA Corporation Recent Developments/Updates

9.1.6 NVIDIA Corporation Competitive Strengths & Weaknesses

9.2 Advanced Micro Devices, Inc.

9.2.1 Advanced Micro Devices, Inc. Details

9.2.2 Advanced Micro Devices, Inc. Major Business

9.2.3 Advanced Micro Devices, Inc. Cloud AI Training Accelerator Chips Product and Services

9.2.4 Advanced Micro Devices, Inc. Cloud AI Training Accelerator Chips Production, Price, Value, Gross Margin and Market Share (2021-2026)

9.2.5 Advanced Micro Devices, Inc. Recent Developments/Updates

9.2.6 Advanced Micro Devices, Inc. Competitive Strengths & Weaknesses

9.3 Broadcom Inc.

9.3.1 Broadcom Inc. Details

9.3.2 Broadcom Inc. Major Business

9.3.3 Broadcom Inc. Cloud AI Training Accelerator Chips Product and Services

9.3.4 Broadcom Inc. Cloud AI Training Accelerator Chips Production, Price, Value, Gross Margin and Market Share (2021-2026)

9.3.5 Broadcom Inc. Recent Developments/Updates

9.3.6 Broadcom Inc. Competitive Strengths & Weaknesses

9.4 Google LLC

9.4.1 Google LLC Details

9.4.2 Google LLC Major Business

9.4.3 Google LLC Cloud AI Training Accelerator Chips Product and Services

9.4.4 Google LLC Cloud AI Training Accelerator Chips Production, Price, Value, Gross Margin and Market Share (2021-2026)

9.4.5 Google LLC Recent Developments/Updates

9.4.6 Google LLC Competitive Strengths & Weaknesses

9.5 Amazon Web Services, Inc.

9.5.1 Amazon Web Services, Inc. Details

9.5.2 Amazon Web Services, Inc. Major Business

9.5.3 Amazon Web Services, Inc. Cloud AI Training Accelerator Chips Product and Services

9.5.4 Amazon Web Services, Inc. Cloud AI Training Accelerator Chips Production, Price, Value, Gross Margin and Market Share (2021-2026)

9.5.5 Amazon Web Services, Inc. Recent Developments/Updates

9.5.6 Amazon Web Services, Inc. Competitive Strengths & Weaknesses

9.6 Huawei Technologies Co., Ltd.

9.6.1 Huawei Technologies Co., Ltd. Details

9.6.2 Huawei Technologies Co., Ltd. Major Business

9.6.3 Huawei Technologies Co., Ltd. Cloud AI Training Accelerator Chips Product and Services

9.6.4 Huawei Technologies Co., Ltd. Cloud AI Training Accelerator Chips Production, Price, Value, Gross Margin and Market Share (2021-2026)

9.6.5 Huawei Technologies Co., Ltd. Recent Developments/Updates

9.6.6 Huawei Technologies Co., Ltd. Competitive Strengths & Weaknesses

9.7 Intel Corporation

9.7.1 Intel Corporation Details

9.7.2 Intel Corporation Major Business

9.7.3 Intel Corporation Cloud AI Training Accelerator Chips Product and Services

9.7.4 Intel Corporation Cloud AI Training Accelerator Chips Production, Price, Value,

Gross Margin and Market Share (2021-2026)

9.7.5 Intel Corporation Recent Developments/Updates

9.7.6 Intel Corporation Competitive Strengths & Weaknesses

9.8 Marvell Technology, Inc.

9.8.1 Marvell Technology, Inc. Details

9.8.2 Marvell Technology, Inc. Major Business

9.8.3 Marvell Technology, Inc. Cloud AI Training Accelerator Chips Product and Services

9.8.4 Marvell Technology, Inc. Cloud AI Training Accelerator Chips Production, Price, Value, Gross Margin and Market Share (2021-2026)

9.8.5 Marvell Technology, Inc. Recent Developments/Updates

9.8.6 Marvell Technology, Inc. Competitive Strengths & Weaknesses

9.9 Cambricon Technologies Corporation Limited

9.9.1 Cambricon Technologies Corporation Limited Details

9.9.2 Cambricon Technologies Corporation Limited Major Business

9.9.3 Cambricon Technologies Corporation Limited Cloud AI Training Accelerator Chips Product and Services

9.9.4 Cambricon Technologies Corporation Limited Cloud AI Training Accelerator Chips Production, Price, Value, Gross Margin and Market Share (2021-2026)

9.9.5 Cambricon Technologies Corporation Limited Recent Developments/Updates

9.9.6 Cambricon Technologies Corporation Limited Competitive Strengths & Weaknesses

9.10 Kunlunxin Technology

9.10.1 Kunlunxin Technology Details

9.10.2 Kunlunxin Technology Major Business

9.10.3 Kunlunxin Technology Cloud AI Training Accelerator Chips Product and Services

9.10.4 Kunlunxin Technology Cloud AI Training Accelerator Chips Production, Price, Value, Gross Margin and Market Share (2021-2026)

9.10.5 Kunlunxin Technology Recent Developments/Updates

9.10.6 Kunlunxin Technology Competitive Strengths & Weaknesses

9.11 MetaX Integrated Circuits Co., Ltd.

9.11.1 MetaX Integrated Circuits Co., Ltd. Details

9.11.2 MetaX Integrated Circuits Co., Ltd. Major Business

9.11.3 MetaX Integrated Circuits Co., Ltd. Cloud AI Training Accelerator Chips Product and Services

9.11.4 MetaX Integrated Circuits Co., Ltd. Cloud AI Training Accelerator Chips Production, Price, Value, Gross Margin and Market Share (2021-2026)

9.11.5 MetaX Integrated Circuits Co., Ltd. Recent Developments/Updates

- 9.11.6 MetaX Integrated Circuits Co., Ltd. Competitive Strengths & Weaknesses
- 9.12 Moore Threads Technology Co., Ltd.
 - 9.12.1 Moore Threads Technology Co., Ltd. Details
 - 9.12.2 Moore Threads Technology Co., Ltd. Major Business
 - 9.12.3 Moore Threads Technology Co., Ltd. Cloud AI Training Accelerator Chips Product and Services
 - 9.12.4 Moore Threads Technology Co., Ltd. Cloud AI Training Accelerator Chips Production, Price, Value, Gross Margin and Market Share (2021-2026)
 - 9.12.5 Moore Threads Technology Co., Ltd. Recent Developments/Updates
 - 9.12.6 Moore Threads Technology Co., Ltd. Competitive Strengths & Weaknesses
- 9.13 Hygon Information Technology Co., Ltd.
 - 9.13.1 Hygon Information Technology Co., Ltd. Details
 - 9.13.2 Hygon Information Technology Co., Ltd. Major Business
 - 9.13.3 Hygon Information Technology Co., Ltd. Cloud AI Training Accelerator Chips Product and Services
 - 9.13.4 Hygon Information Technology Co., Ltd. Cloud AI Training Accelerator Chips Production, Price, Value, Gross Margin and Market Share (2021-2026)
 - 9.13.5 Hygon Information Technology Co., Ltd. Recent Developments/Updates
 - 9.13.6 Hygon Information Technology Co., Ltd. Competitive Strengths & Weaknesses
- 9.14 Iluvatar CoreX
 - 9.14.1 Iluvatar CoreX Details
 - 9.14.2 Iluvatar CoreX Major Business
 - 9.14.3 Iluvatar CoreX Cloud AI Training Accelerator Chips Product and Services
 - 9.14.4 Iluvatar CoreX Cloud AI Training Accelerator Chips Production, Price, Value, Gross Margin and Market Share (2021-2026)
 - 9.14.5 Iluvatar CoreX Recent Developments/Updates
 - 9.14.6 Iluvatar CoreX Competitive Strengths & Weaknesses
- 9.15 Shanghai Biren Technology Co., Ltd.
 - 9.15.1 Shanghai Biren Technology Co., Ltd. Details
 - 9.15.2 Shanghai Biren Technology Co., Ltd. Major Business
 - 9.15.3 Shanghai Biren Technology Co., Ltd. Cloud AI Training Accelerator Chips Product and Services
 - 9.15.4 Shanghai Biren Technology Co., Ltd. Cloud AI Training Accelerator Chips Production, Price, Value, Gross Margin and Market Share (2021-2026)
 - 9.15.5 Shanghai Biren Technology Co., Ltd. Recent Developments/Updates
 - 9.15.6 Shanghai Biren Technology Co., Ltd. Competitive Strengths & Weaknesses
- 9.16 Shanghai Enflame Technology Co., Ltd.
 - 9.16.1 Shanghai Enflame Technology Co., Ltd. Details
 - 9.16.2 Shanghai Enflame Technology Co., Ltd. Major Business

9.16.3 Shanghai Enflame Technology Co., Ltd. Cloud AI Training Accelerator Chips Product and Services

9.16.4 Shanghai Enflame Technology Co., Ltd. Cloud AI Training Accelerator Chips Production, Price, Value, Gross Margin and Market Share (2021-2026)

9.16.5 Shanghai Enflame Technology Co., Ltd. Recent Developments/Updates

9.16.6 Shanghai Enflame Technology Co., Ltd. Competitive Strengths & Weaknesses

9.17 Cerebras Systems, Inc.

9.17.1 Cerebras Systems, Inc. Details

9.17.2 Cerebras Systems, Inc. Major Business

9.17.3 Cerebras Systems, Inc. Cloud AI Training Accelerator Chips Product and Services

9.17.4 Cerebras Systems, Inc. Cloud AI Training Accelerator Chips Production, Price, Value, Gross Margin and Market Share (2021-2026)

9.17.5 Cerebras Systems, Inc. Recent Developments/Updates

9.17.6 Cerebras Systems, Inc. Competitive Strengths & Weaknesses

9.18 Graphcore Limited

9.18.1 Graphcore Limited Details

9.18.2 Graphcore Limited Major Business

9.18.3 Graphcore Limited Cloud AI Training Accelerator Chips Product and Services

9.18.4 Graphcore Limited Cloud AI Training Accelerator Chips Production, Price, Value, Gross Margin and Market Share (2021-2026)

9.18.5 Graphcore Limited Recent Developments/Updates

9.18.6 Graphcore Limited Competitive Strengths & Weaknesses

9.19 Preferred Networks, Inc.

9.19.1 Preferred Networks, Inc. Details

9.19.2 Preferred Networks, Inc. Major Business

9.19.3 Preferred Networks, Inc. Cloud AI Training Accelerator Chips Product and Services

9.19.4 Preferred Networks, Inc. Cloud AI Training Accelerator Chips Production, Price, Value, Gross Margin and Market Share (2021-2026)

9.19.5 Preferred Networks, Inc. Recent Developments/Updates

9.19.6 Preferred Networks, Inc. Competitive Strengths & Weaknesses

9.20 SambaNova Systems, Inc.

9.20.1 SambaNova Systems, Inc. Details

9.20.2 SambaNova Systems, Inc. Major Business

9.20.3 SambaNova Systems, Inc. Cloud AI Training Accelerator Chips Product and Services

9.20.4 SambaNova Systems, Inc. Cloud AI Training Accelerator Chips Production, Price, Value, Gross Margin and Market Share (2021-2026)

- 9.20.5 SambaNova Systems, Inc. Recent Developments/Updates
- 9.20.6 SambaNova Systems, Inc. Competitive Strengths & Weaknesses
- 9.21 Tenstorrent Inc.
 - 9.21.1 Tenstorrent Inc. Details
 - 9.21.2 Tenstorrent Inc. Major Business
 - 9.21.3 Tenstorrent Inc. Cloud AI Training Accelerator Chips Product and Services
 - 9.21.4 Tenstorrent Inc. Cloud AI Training Accelerator Chips Production, Price, Value, Gross Margin and Market Share (2021-2026)
 - 9.21.5 Tenstorrent Inc. Recent Developments/Updates
 - 9.21.6 Tenstorrent Inc. Competitive Strengths & Weaknesses
- 9.22 Microsoft Corporation
 - 9.22.1 Microsoft Corporation Details
 - 9.22.2 Microsoft Corporation Major Business
 - 9.22.3 Microsoft Corporation Cloud AI Training Accelerator Chips Product and Services
 - 9.22.4 Microsoft Corporation Cloud AI Training Accelerator Chips Production, Price, Value, Gross Margin and Market Share (2021-2026)
 - 9.22.5 Microsoft Corporation Recent Developments/Updates
 - 9.22.6 Microsoft Corporation Competitive Strengths & Weaknesses
- 9.23 Meta Platforms, Inc.
 - 9.23.1 Meta Platforms, Inc. Details
 - 9.23.2 Meta Platforms, Inc. Major Business
 - 9.23.3 Meta Platforms, Inc. Cloud AI Training Accelerator Chips Product and Services
 - 9.23.4 Meta Platforms, Inc. Cloud AI Training Accelerator Chips Production, Price, Value, Gross Margin and Market Share (2021-2026)
 - 9.23.5 Meta Platforms, Inc. Recent Developments/Updates
 - 9.23.6 Meta Platforms, Inc. Competitive Strengths & Weaknesses

10 INDUSTRY CHAIN ANALYSIS

- 10.1 Cloud AI Training Accelerator Chips Industry Chain
- 10.2 Cloud AI Training Accelerator Chips Upstream Analysis
 - 10.2.1 Cloud AI Training Accelerator Chips Core Raw Materials
 - 10.2.2 Main Manufacturers of Cloud AI Training Accelerator Chips Core Raw Materials
- 10.3 Midstream Analysis
- 10.4 Downstream Analysis
- 10.5 Cloud AI Training Accelerator Chips Production Mode
- 10.6 Cloud AI Training Accelerator Chips Procurement Model
- 10.7 Cloud AI Training Accelerator Chips Industry Sales Model and Sales Channels

10.7.1 Cloud AI Training Accelerator Chips Sales Model

10.7.2 Cloud AI Training Accelerator Chips Typical Distributors

11 RESEARCH FINDINGS AND CONCLUSION

12 APPENDIX

12.1 Methodology

12.2 Research Process and Data Source

12.3 Disclaimer

List Of Tables

LIST OF TABLES

- Table 1. World Cloud AI Training Accelerator Chips Production Value by Region (2021, 2025 and 2032) & (USD Million)
- Table 2. World Cloud AI Training Accelerator Chips Production Value by Region (2021-2026) & (USD Million)
- Table 3. World Cloud AI Training Accelerator Chips Production Value by Region (2027-2032) & (USD Million)
- Table 4. World Cloud AI Training Accelerator Chips Production Value Market Share by Region (2021-2026)
- Table 5. World Cloud AI Training Accelerator Chips Production Value Market Share by Region (2027-2032)
- Table 6. World Cloud AI Training Accelerator Chips Production by Region (2021-2026) & (Million Units)
- Table 7. World Cloud AI Training Accelerator Chips Production by Region (2027-2032) & (Million Units)
- Table 8. World Cloud AI Training Accelerator Chips Production Market Share by Region (2021-2026)
- Table 9. World Cloud AI Training Accelerator Chips Production Market Share by Region (2027-2032)
- Table 10. World Cloud AI Training Accelerator Chips Average Price by Region (2021-2026) & (US\$/Unit)
- Table 11. World Cloud AI Training Accelerator Chips Average Price by Region (2027-2032) & (US\$/Unit)
- Table 12. Cloud AI Training Accelerator Chips Major Market Trends
- Table 13. World Cloud AI Training Accelerator Chips Consumption Growth Rate Forecast by Region (2021 & 2025 & 2032) & (Million Units)
- Table 14. World Cloud AI Training Accelerator Chips Consumption by Region (2021-2026) & (Million Units)
- Table 15. World Cloud AI Training Accelerator Chips Consumption Forecast by Region (2027-2032) & (Million Units)
- Table 16. World Cloud AI Training Accelerator Chips Production Value by Manufacturer (2021-2026) & (USD Million)
- Table 17. Production Value Market Share of Key Cloud AI Training Accelerator Chips Producers in 2025
- Table 18. World Cloud AI Training Accelerator Chips Production by Manufacturer (2021-2026) & (Million Units)

Table 19. Production Market Share of Key Cloud AI Training Accelerator Chips Producers in 2025

Table 20. World Cloud AI Training Accelerator Chips Average Price by Manufacturer (2021-2026) & (US\$/Unit)

Table 21. Global Cloud AI Training Accelerator Chips Company Evaluation Quadrant

Table 22. World Cloud AI Training Accelerator Chips Industry Rank of Major Manufacturers, Based on Production Value in 2025

Table 23. Head Office and Cloud AI Training Accelerator Chips Production Site of Key Manufacturer

Table 24. Cloud AI Training Accelerator Chips Market: Company Product Type Footprint

Table 25. Cloud AI Training Accelerator Chips Market: Company Product Application Footprint

Table 26. Cloud AI Training Accelerator Chips Competitive Factors

Table 27. Cloud AI Training Accelerator Chips New Entrant and Capacity Expansion Plans

Table 28. Cloud AI Training Accelerator Chips Mergers & Acquisitions Activity

Table 29. United States VS China Cloud AI Training Accelerator Chips Production Value Comparison, (2021 & 2025 & 2032) & (USD Million)

Table 30. United States VS China Cloud AI Training Accelerator Chips Production Comparison, (2021 & 2025 & 2032) & (Million Units)

Table 31. United States VS China Cloud AI Training Accelerator Chips Consumption Comparison, (2021 & 2025 & 2032) & (Million Units)

Table 32. United States Based Cloud AI Training Accelerator Chips Manufacturers, Headquarters and Production Site (States, Country)

Table 33. United States Based Manufacturers Cloud AI Training Accelerator Chips Production Value, (2021-2026) & (USD Million)

Table 34. United States Based Manufacturers Cloud AI Training Accelerator Chips Production Value Market Share (2021-2026)

Table 35. United States Based Manufacturers Cloud AI Training Accelerator Chips Production (2021-2026) & (Million Units)

Table 36. United States Based Manufacturers Cloud AI Training Accelerator Chips Production Market Share (2021-2026)

Table 37. China Based Cloud AI Training Accelerator Chips Manufacturers, Headquarters and Production Site (Province, Country)

Table 38. China Based Manufacturers Cloud AI Training Accelerator Chips Production Value, (2021-2026) & (USD Million)

Table 39. China Based Manufacturers Cloud AI Training Accelerator Chips Production Value Market Share (2021-2026)

Table 40. China Based Manufacturers Cloud AI Training Accelerator Chips Production,

(2021-2026) & (Million Units)

Table 41. China Based Manufacturers Cloud AI Training Accelerator Chips Production Market Share (2021-2026)

Table 42. Rest of World Based Cloud AI Training Accelerator Chips Manufacturers, Headquarters and Production Site (State, Country)

Table 43. Rest of World Based Manufacturers Cloud AI Training Accelerator Chips Production Value, (2021-2026) & (USD Million)

Table 44. Rest of World Based Manufacturers Cloud AI Training Accelerator Chips Production Value Market Share (2021-2026)

Table 45. Rest of World Based Manufacturers Cloud AI Training Accelerator Chips Production, (2021-2026) & (Million Units)

Table 46. Rest of World Based Manufacturers Cloud AI Training Accelerator Chips Production Market Share (2021-2026)

Table 47. World Cloud AI Training Accelerator Chips Production Value by Type, (USD Million), 2021 & 2025 & 2032

Table 48. World Cloud AI Training Accelerator Chips Production by Type (2021-2026) & (Million Units)

Table 49. World Cloud AI Training Accelerator Chips Production by Type (2027-2032) & (Million Units)

Table 50. World Cloud AI Training Accelerator Chips Production Value by Type (2021-2026) & (USD Million)

Table 51. World Cloud AI Training Accelerator Chips Production Value by Type (2027-2032) & (USD Million)

Table 52. World Cloud AI Training Accelerator Chips Average Price by Type (2021-2026) & (US\$/Unit)

Table 53. World Cloud AI Training Accelerator Chips Average Price by Type (2027-2032) & (US\$/Unit)

Table 54. World Cloud AI Training Accelerator Chips Production Value by Numeric Precision, (USD Million), 2021 & 2025 & 2032

Table 55. World Cloud AI Training Accelerator Chips Production by Numeric Precision (2021-2026) & (Million Units)

Table 56. World Cloud AI Training Accelerator Chips Production by Numeric Precision (2027-2032) & (Million Units)

Table 57. World Cloud AI Training Accelerator Chips Production Value by Numeric Precision (2021-2026) & (USD Million)

Table 58. World Cloud AI Training Accelerator Chips Production Value by Numeric Precision (2027-2032) & (USD Million)

Table 59. World Cloud AI Training Accelerator Chips Average Price by Numeric Precision (2021-2026) & (US\$/Unit)

Table 60. World Cloud AI Training Accelerator Chips Average Price by Numeric Precision (2027-2032) & (US\$/Unit)

Table 61. World Cloud AI Training Accelerator Chips Production Value by Interconnect Capability, (USD Million), 2021 & 2025 & 2032

Table 62. World Cloud AI Training Accelerator Chips Production by Interconnect Capability (2021-2026) & (Million Units)

Table 63. World Cloud AI Training Accelerator Chips Production by Interconnect Capability (2027-2032) & (Million Units)

Table 64. World Cloud AI Training Accelerator Chips Production Value by Interconnect Capability (2021-2026) & (USD Million)

Table 65. World Cloud AI Training Accelerator Chips Production Value by Interconnect Capability (2027-2032) & (USD Million)

Table 66. World Cloud AI Training Accelerator Chips Average Price by Interconnect Capability (2021-2026) & (US\$/Unit)

Table 67. World Cloud AI Training Accelerator Chips Average Price by Interconnect Capability (2027-2032) & (US\$/Unit)

Table 68. World Cloud AI Training Accelerator Chips Production Value by Application, (USD Million), 2021 & 2025 & 2032

Table 69. World Cloud AI Training Accelerator Chips Production by Application (2021-2026) & (Million Units)

Table 70. World Cloud AI Training Accelerator Chips Production by Application (2027-2032) & (Million Units)

Table 71. World Cloud AI Training Accelerator Chips Production Value by Application (2021-2026) & (USD Million)

Table 72. World Cloud AI Training Accelerator Chips Production Value by Application (2027-2032) & (USD Million)

Table 73. World Cloud AI Training Accelerator Chips Average Price by Application (2021-2026) & (US\$/Unit)

Table 74. World Cloud AI Training Accelerator Chips Average Price by Application (2027-2032) & (US\$/Unit)

Table 75. NVIDIA Corporation Basic Information, Manufacturing Base and Competitors

Table 76. NVIDIA Corporation Major Business

Table 77. NVIDIA Corporation Cloud AI Training Accelerator Chips Product and Services

Table 78. NVIDIA Corporation Cloud AI Training Accelerator Chips Production (Million Units), Price (US\$/Unit), Production Value (USD Million), Gross Margin and Market Share (2021-2026)

Table 79. NVIDIA Corporation Recent Developments/Updates

Table 80. NVIDIA Corporation Competitive Strengths & Weaknesses

Table 81. Advanced Micro Devices, Inc. Basic Information, Manufacturing Base and Competitors

Table 82. Advanced Micro Devices, Inc. Major Business

Table 83. Advanced Micro Devices, Inc. Cloud AI Training Accelerator Chips Product and Services

Table 84. Advanced Micro Devices, Inc. Cloud AI Training Accelerator Chips Production (Million Units), Price (US\$/Unit), Production Value (USD Million), Gross Margin and Market Share (2021-2026)

Table 85. Advanced Micro Devices, Inc. Recent Developments/Updates

Table 86. Advanced Micro Devices, Inc. Competitive Strengths & Weaknesses

Table 87. Broadcom Inc. Basic Information, Manufacturing Base and Competitors

Table 88. Broadcom Inc. Major Business

Table 89. Broadcom Inc. Cloud AI Training Accelerator Chips Product and Services

Table 90. Broadcom Inc. Cloud AI Training Accelerator Chips Production (Million Units), Price (US\$/Unit), Production Value (USD Million), Gross Margin and Market Share (2021-2026)

Table 91. Broadcom Inc. Recent Developments/Updates

Table 92. Broadcom Inc. Competitive Strengths & Weaknesses

Table 93. Google LLC Basic Information, Manufacturing Base and Competitors

Table 94. Google LLC Major Business

Table 95. Google LLC Cloud AI Training Accelerator Chips Product and Services

Table 96. Google LLC Cloud AI Training Accelerator Chips Production (Million Units), Price (US\$/Unit), Production Value (USD Million), Gross Margin and Market Share (2021-2026)

Table 97. Google LLC Recent Developments/Updates

Table 98. Google LLC Competitive Strengths & Weaknesses

Table 99. Amazon Web Services, Inc. Basic Information, Manufacturing Base and Competitors

Table 100. Amazon Web Services, Inc. Major Business

Table 101. Amazon Web Services, Inc. Cloud AI Training Accelerator Chips Product and Services

Table 102. Amazon Web Services, Inc. Cloud AI Training Accelerator Chips Production (Million Units), Price (US\$/Unit), Production Value (USD Million), Gross Margin and Market Share (2021-2026)

Table 103. Amazon Web Services, Inc. Recent Developments/Updates

Table 104. Amazon Web Services, Inc. Competitive Strengths & Weaknesses

Table 105. Huawei Technologies Co., Ltd. Basic Information, Manufacturing Base and Competitors

Table 106. Huawei Technologies Co., Ltd. Major Business

Table 107. Huawei Technologies Co., Ltd. Cloud AI Training Accelerator Chips Product and Services

Table 108. Huawei Technologies Co., Ltd. Cloud AI Training Accelerator Chips Production (Million Units), Price (US\$/Unit), Production Value (USD Million), Gross Margin and Market Share (2021-2026)

Table 109. Huawei Technologies Co., Ltd. Recent Developments/Updates

Table 110. Huawei Technologies Co., Ltd. Competitive Strengths & Weaknesses

Table 111. Intel Corporation Basic Information, Manufacturing Base and Competitors

Table 112. Intel Corporation Major Business

Table 113. Intel Corporation Cloud AI Training Accelerator Chips Product and Services

Table 114. Intel Corporation Cloud AI Training Accelerator Chips Production (Million Units), Price (US\$/Unit), Production Value (USD Million), Gross Margin and Market Share (2021-2026)

Table 115. Intel Corporation Recent Developments/Updates

Table 116. Intel Corporation Competitive Strengths & Weaknesses

Table 117. Marvell Technology, Inc. Basic Information, Manufacturing Base and Competitors

Table 118. Marvell Technology, Inc. Major Business

Table 119. Marvell Technology, Inc. Cloud AI Training Accelerator Chips Product and Services

Table 120. Marvell Technology, Inc. Cloud AI Training Accelerator Chips Production (Million Units), Price (US\$/Unit), Production Value (USD Million), Gross Margin and Market Share (2021-2026)

Table 121. Marvell Technology, Inc. Recent Developments/Updates

Table 122. Marvell Technology, Inc. Competitive Strengths & Weaknesses

Table 123. Cambricon Technologies Corporation Limited Basic Information, Manufacturing Base and Competitors

Table 124. Cambricon Technologies Corporation Limited Major Business

Table 125. Cambricon Technologies Corporation Limited Cloud AI Training Accelerator Chips Product and Services

Table 126. Cambricon Technologies Corporation Limited Cloud AI Training Accelerator Chips Production (Million Units), Price (US\$/Unit), Production Value (USD Million), Gross Margin and Market Share (2021-2026)

Table 127. Cambricon Technologies Corporation Limited Recent Developments/Updates

Table 128. Cambricon Technologies Corporation Limited Competitive Strengths & Weaknesses

Table 129. Kunlunxin Technology Basic Information, Manufacturing Base and Competitors

Table 130. Kunlunxin Technology Major Business

Table 131. Kunlunxin Technology Cloud AI Training Accelerator Chips Product and Services

Table 132. Kunlunxin Technology Cloud AI Training Accelerator Chips Production (Million Units), Price (US\$/Unit), Production Value (USD Million), Gross Margin and Market Share (2021-2026)

Table 133. Kunlunxin Technology Recent Developments/Updates

Table 134. Kunlunxin Technology Competitive Strengths & Weaknesses

Table 135. MetaX Integrated Circuits Co., Ltd. Basic Information, Manufacturing Base and Competitors

Table 136. MetaX Integrated Circuits Co., Ltd. Major Business

Table 137. MetaX Integrated Circuits Co., Ltd. Cloud AI Training Accelerator Chips Product and Services

Table 138. MetaX Integrated Circuits Co., Ltd. Cloud AI Training Accelerator Chips Production (Million Units), Price (US\$/Unit), Production Value (USD Million), Gross Margin and Market Share (2021-2026)

Table 139. MetaX Integrated Circuits Co., Ltd. Recent Developments/Updates

Table 140. MetaX Integrated Circuits Co., Ltd. Competitive Strengths & Weaknesses

Table 141. Moore Threads Technology Co., Ltd. Basic Information, Manufacturing Base and Competitors

Table 142. Moore Threads Technology Co., Ltd. Major Business

Table 143. Moore Threads Technology Co., Ltd. Cloud AI Training Accelerator Chips Product and Services

Table 144. Moore Threads Technology Co., Ltd. Cloud AI Training Accelerator Chips Production (Million Units), Price (US\$/Unit), Production Value (USD Million), Gross Margin and Market Share (2021-2026)

Table 145. Moore Threads Technology Co., Ltd. Recent Developments/Updates

Table 146. Moore Threads Technology Co., Ltd. Competitive Strengths & Weaknesses

Table 147. Hygon Information Technology Co., Ltd. Basic Information, Manufacturing Base and Competitors

Table 148. Hygon Information Technology Co., Ltd. Major Business

Table 149. Hygon Information Technology Co., Ltd. Cloud AI Training Accelerator Chips Product and Services

Table 150. Hygon Information Technology Co., Ltd. Cloud AI Training Accelerator Chips Production (Million Units), Price (US\$/Unit), Production Value (USD Million), Gross Margin and Market Share (2021-2026)

Table 151. Hygon Information Technology Co., Ltd. Recent Developments/Updates

Table 152. Hygon Information Technology Co., Ltd. Competitive Strengths & Weaknesses

Table 153. Iluvatar CoreX Basic Information, Manufacturing Base and Competitors

Table 154. Iluvatar CoreX Major Business

Table 155. Iluvatar CoreX Cloud AI Training Accelerator Chips Product and Services

Table 156. Iluvatar CoreX Cloud AI Training Accelerator Chips Production (Million Units), Price (US\$/Unit), Production Value (USD Million), Gross Margin and Market Share (2021-2026)

Table 157. Iluvatar CoreX Recent Developments/Updates

Table 158. Iluvatar CoreX Competitive Strengths & Weaknesses

Table 159. Shanghai Biren Technology Co., Ltd. Basic Information, Manufacturing Base and Competitors

Table 160. Shanghai Biren Technology Co., Ltd. Major Business

Table 161. Shanghai Biren Technology Co., Ltd. Cloud AI Training Accelerator Chips Product and Services

Table 162. Shanghai Biren Technology Co., Ltd. Cloud AI Training Accelerator Chips Production (Million Units), Price (US\$/Unit), Production Value (USD Million), Gross Margin and Market Share (2021-2026)

Table 163. Shanghai Biren Technology Co., Ltd. Recent Developments/Updates

Table 164. Shanghai Biren Technology Co., Ltd. Competitive Strengths & Weaknesses

Table 165. Shanghai Enflame Technology Co., Ltd. Basic Information, Manufacturing Base and Competitors

Table 166. Shanghai Enflame Technology Co., Ltd. Major Business

Table 167. Shanghai Enflame Technology Co., Ltd. Cloud AI Training Accelerator Chips Product and Services

Table 168. Shanghai Enflame Technology Co., Ltd. Cloud AI Training Accelerator Chips Production (Million Units), Price (US\$/Unit), Production Value (USD Million), Gross Margin and Market Share (2021-2026)

Table 169. Shanghai Enflame Technology Co., Ltd. Recent Developments/Updates

Table 170. Shanghai Enflame Technology Co., Ltd. Competitive Strengths & Weaknesses

Table 171. Cerebras Systems, Inc. Basic Information, Manufacturing Base and Competitors

Table 172. Cerebras Systems, Inc. Major Business

Table 173. Cerebras Systems, Inc. Cloud AI Training Accelerator Chips Product and Services

Table 174. Cerebras Systems, Inc. Cloud AI Training Accelerator Chips Production (Million Units), Price (US\$/Unit), Production Value (USD Million), Gross Margin and Market Share (2021-2026)

Table 175. Cerebras Systems, Inc. Recent Developments/Updates

Table 176. Cerebras Systems, Inc. Competitive Strengths & Weaknesses

Table 177. Graphcore Limited Basic Information, Manufacturing Base and Competitors

Table 178. Graphcore Limited Major Business

Table 179. Graphcore Limited Cloud AI Training Accelerator Chips Product and Services

Table 180. Graphcore Limited Cloud AI Training Accelerator Chips Production (Million Units), Price (US\$/Unit), Production Value (USD Million), Gross Margin and Market Share (2021-2026)

Table 181. Graphcore Limited Recent Developments/Updates

Table 182. Graphcore Limited Competitive Strengths & Weaknesses

Table 183. Preferred Networks, Inc. Basic Information, Manufacturing Base and Competitors

Table 184. Preferred Networks, Inc. Major Business

Table 185. Preferred Networks, Inc. Cloud AI Training Accelerator Chips Product and Services

Table 186. Preferred Networks, Inc. Cloud AI Training Accelerator Chips Production (Million Units), Price (US\$/Unit), Production Value (USD Million), Gross Margin and Market Share (2021-2026)

Table 187. Preferred Networks, Inc. Recent Developments/Updates

Table 188. Preferred Networks, Inc. Competitive Strengths & Weaknesses

Table 189. SambaNova Systems, Inc. Basic Information, Manufacturing Base and Competitors

Table 190. SambaNova Systems, Inc. Major Business

Table 191. SambaNova Systems, Inc. Cloud AI Training Accelerator Chips Product and Services

Table 192. SambaNova Systems, Inc. Cloud AI Training Accelerator Chips Production (Million Units), Price (US\$/Unit), Production Value (USD Million), Gross Margin and Market Share (2021-2026)

Table 193. SambaNova Systems, Inc. Recent Developments/Updates

Table 194. SambaNova Systems, Inc. Competitive Strengths & Weaknesses

Table 195. Tenstorrent Inc. Basic Information, Manufacturing Base and Competitors

Table 196. Tenstorrent Inc. Major Business

Table 197. Tenstorrent Inc. Cloud AI Training Accelerator Chips Product and Services

Table 198. Tenstorrent Inc. Cloud AI Training Accelerator Chips Production (Million Units), Price (US\$/Unit), Production Value (USD Million), Gross Margin and Market Share (2021-2026)

Table 199. Tenstorrent Inc. Recent Developments/Updates

Table 200. Tenstorrent Inc. Competitive Strengths & Weaknesses

Table 201. Microsoft Corporation Basic Information, Manufacturing Base and Competitors

Table 202. Microsoft Corporation Major Business

Table 203. Microsoft Corporation Cloud AI Training Accelerator Chips Product and Services

Table 204. Microsoft Corporation Cloud AI Training Accelerator Chips Production (Million Units), Price (US\$/Unit), Production Value (USD Million), Gross Margin and Market Share (2021-2026)

Table 205. Microsoft Corporation Recent Developments/Updates

Table 206. Microsoft Corporation Competitive Strengths & Weaknesses

Table 207. Meta Platforms, Inc. Basic Information, Manufacturing Base and Competitors

Table 208. Meta Platforms, Inc. Major Business

Table 209. Meta Platforms, Inc. Cloud AI Training Accelerator Chips Product and Services

Table 210. Meta Platforms, Inc. Cloud AI Training Accelerator Chips Production (Million Units), Price (US\$/Unit), Production Value (USD Million), Gross Margin and Market Share (2021-2026)

Table 211. Meta Platforms, Inc. Recent Developments/Updates

Table 212. Meta Platforms, Inc. Competitive Strengths & Weaknesses

Table 213. Global Key Players of Cloud AI Training Accelerator Chips Upstream (Raw Materials)

Table 214. Global Cloud AI Training Accelerator Chips Typical Customers

Table 215. Cloud AI Training Accelerator Chips Typical Distributors

List Of Figures

LIST OF FIGURES

- Figure 1. Cloud AI Training Accelerator Chips Picture
- Figure 2. World Cloud AI Training Accelerator Chips Production Value: 2021 & 2025 & 2032, (USD Million)
- Figure 3. World Cloud AI Training Accelerator Chips Production Value and Forecast (2021-2032) & (USD Million)
- Figure 4. World Cloud AI Training Accelerator Chips Production (2021-2032) & (Million Units)
- Figure 5. World Cloud AI Training Accelerator Chips Average Price (2021-2032) & (US\$/Unit)
- Figure 6. World Cloud AI Training Accelerator Chips Production Value Market Share by Region (2021-2032)
- Figure 7. World Cloud AI Training Accelerator Chips Production Market Share by Region (2021-2032)
- Figure 8. North America Cloud AI Training Accelerator Chips Production (2021-2032) & (Million Units)
- Figure 9. China Cloud AI Training Accelerator Chips Production (2021-2032) & (Million Units)
- Figure 10. Japan Cloud AI Training Accelerator Chips Production (2021-2032) & (Million Units)
- Figure 11. Cloud AI Training Accelerator Chips Market Drivers
- Figure 12. Factors Affecting Demand
- Figure 13. World Cloud AI Training Accelerator Chips Consumption (2021-2032) & (Million Units)
- Figure 14. World Cloud AI Training Accelerator Chips Consumption Market Share by Region (2021-2032)
- Figure 15. United States Cloud AI Training Accelerator Chips Consumption (2021-2032) & (Million Units)
- Figure 16. China Cloud AI Training Accelerator Chips Consumption (2021-2032) & (Million Units)
- Figure 17. Europe Cloud AI Training Accelerator Chips Consumption (2021-2032) & (Million Units)
- Figure 18. Japan Cloud AI Training Accelerator Chips Consumption (2021-2032) & (Million Units)
- Figure 19. South Korea Cloud AI Training Accelerator Chips Consumption (2021-2032) & (Million Units)

Figure 20. ASEAN Cloud AI Training Accelerator Chips Consumption (2021-2032) & (Million Units)

Figure 21. India Cloud AI Training Accelerator Chips Consumption (2021-2032) & (Million Units)

Figure 22. Producer Shipments of Cloud AI Training Accelerator Chips by Manufacturer Revenue (\$MM) and Market Share (%): 2025

Figure 23. Global Four-firm Concentration Ratios (CR4) for Cloud AI Training Accelerator Chips Markets in 2025

Figure 24. Global Four-firm Concentration Ratios (CR8) for Cloud AI Training Accelerator Chips Markets in 2025

Figure 25. United States VS China: Cloud AI Training Accelerator Chips Production Value Market Share Comparison (2021 & 2025 & 2032)

Figure 26. United States VS China: Cloud AI Training Accelerator Chips Production Market Share Comparison (2021 & 2025 & 2032)

Figure 27. United States VS China: Cloud AI Training Accelerator Chips Consumption Market Share Comparison (2021 & 2025 & 2032)

Figure 28. United States Based Manufacturers Cloud AI Training Accelerator Chips Production Market Share 2025

Figure 29. China Based Manufacturers Cloud AI Training Accelerator Chips Production Market Share 2025

Figure 30. Rest of World Based Manufacturers Cloud AI Training Accelerator Chips Production Market Share 2025

Figure 31. World Cloud AI Training Accelerator Chips Production Value by Type, (USD Million), 2021 & 2025 & 2032

Figure 32. World Cloud AI Training Accelerator Chips Production Value Market Share by Type in 2025

Figure 33. Data Center GPU / GPGPU

Figure 34. AI ASIC / XPU

Figure 35. Other

Figure 36. World Cloud AI Training Accelerator Chips Production Market Share by Type (2021-2032)

Figure 37. World Cloud AI Training Accelerator Chips Production Value Market Share by Type (2021-2032)

Figure 38. World Cloud AI Training Accelerator Chips Average Price by Type (2021-2032) & (US\$/Unit)

Figure 39. World Cloud AI Training Accelerator Chips Production Value by Numeric Precision, (USD Million), 2021 & 2025 & 2032

Figure 40. World Cloud AI Training Accelerator Chips Production Value Market Share by Numeric Precision in 2025

Figure 41. FP64 / FP32 Focused

Figure 42. TF32 / BF16 Training

Figure 43. FP16 Training

Figure 44. FP8 / Low-precision Training

Figure 45. INT8 / Mixed Precision

Figure 46. Other Precision Formats

Figure 47. World Cloud AI Training Accelerator Chips Production Market Share by Numeric Precision (2021-2032)

Figure 48. World Cloud AI Training Accelerator Chips Production Value Market Share by Numeric Precision (2021-2032)

Figure 49. World Cloud AI Training Accelerator Chips Average Price by Numeric Precision (2021-2032) & (US\$/Unit)

Figure 50. World Cloud AI Training Accelerator Chips Production Value by Interconnect Capability, (USD Million), 2021 & 2025 & 2032

Figure 51. World Cloud AI Training Accelerator Chips Production Value Market Share by Interconnect Capability in 2025

Figure 52. Single-card / Standalone Accelerator

Figure 53. Multi-card Server Interconnect

Figure 54. Other

Figure 55. World Cloud AI Training Accelerator Chips Production Market Share by Interconnect Capability (2021-2032)

Figure 56. World Cloud AI Training Accelerator Chips Production Value Market Share by Interconnect Capability (2021-2032)

Figure 57. World Cloud AI Training Accelerator Chips Average Price by Interconnect Capability (2021-2032) & (US\$/Unit)

Figure 58. World Cloud AI Training Accelerator Chips Production Value by Application, (USD Million), 2021 & 2025 & 2032

Figure 59. World Cloud AI Training Accelerator Chips Production Value Market Share by Application in 2025

Figure 60. Cloud & AI Infrastructure

Figure 61. Scientific Research & Healthcare

Figure 62. Automotive & Industrial AI

Figure 63. Other

Figure 64. World Cloud AI Training Accelerator Chips Production Market Share by Application (2021-2032)

Figure 65. World Cloud AI Training Accelerator Chips Production Value Market Share by Application (2021-2032)

Figure 66. World Cloud AI Training Accelerator Chips Average Price by Application (2021-2032) & (US\$/Unit)

Figure 67. Cloud AI Training Accelerator Chips Industry Chain

Figure 68. Cloud AI Training Accelerator Chips Procurement Model

Figure 69. Cloud AI Training Accelerator Chips Sales Model

Figure 70. Cloud AI Training Accelerator Chips Sales Channels, Direct Sales, and Distribution

Figure 71. Methodology

Figure 72. Research Process and Data Source

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