

Global Cloud AI Training Accelerator Chips Market 2026 by Manufacturers, Regions, Type and Application, Forecast to 2032

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Abstracts

According to our (Global Info Research) latest study, the global Cloud AI Training Accelerator Chips market size was valued at US\$ 183668 million in 2025 and is forecast to a readjusted size of US\$ 797078 million by 2032 with a CAGR of 18.1% during review period.

Cloud AI Training Accelerator Chips refer to high-performance semiconductor devices, accelerator cards, OAM modules and system-level compute units deployed in cloud data centers, AI supercomputers, hyperscale AI factories and enterprise-scale training clusters to accelerate pre-training, continued training, fine-tuning, reinforcement learning, mixture-of-experts training and selected training-inference converged workloads. These products are typically implemented as data center GPUs, GPGPUs, custom ASICs, TPUs, NPUs, XPU, RDUs, IPUs or wafer-scale engines, and are characterized by high matrix-compute throughput, low-precision floating-point support, high-bandwidth memory, high-speed chip-to-chip interconnect, distributed training communication, software stacks, compilers and compatibility with mainstream AI frameworks.

From an industry structure perspective, cloud AI training accelerator chips have moved beyond single-chip peak performance competition into a full-stack, system-level efficiency race. Large-scale AI training requires not only tensor throughput, but also high-bandwidth memory, reliable inter-chip communication, distributed training software, framework compatibility, cluster orchestration and power-efficient deployment. Therefore, the core scope of this market should focus on training-capable cloud accelerators, including data center GPUs, GPGPUs, TPUs, NPUs, XPU, RDUs, IPUs and wafer-scale engines. Servers, CPUs, networking chips, edge AI chips and inference-

only devices should be separated from the narrow revenue model. This definition is important because many AI accelerators are increasingly marketed as ?training and inference? products, while a meaningful market model must still distinguish chips that can support pre-training, continued training or large-scale fine-tuning from chips optimized only for inference serving.

Demand growth is being driven by hyperscale cloud capex, AI-native model companies, sovereign AI programs, AI cloud providers and enterprise/private model adoption. In 2025 and 2026, the market is still heavily weighted toward training-capable GPUs, but the mix is changing as custom ASICs, TPUs, Trainium-class chips and regional alternatives enter large-scale deployment. The line between training and inference is becoming less rigid because model providers increasingly need the same infrastructure for pre-training, post-training, fine-tuning, reinforcement learning, batch inference and model refresh cycles. As a result, the most competitive products will be those that combine high compute density, large memory capacity, high scale-up bandwidth, efficient low-precision formats, robust software migration tools and credible supply continuity.

From a policy and technology perspective, export controls on advanced computing chips have accelerated regional supply-chain divergence. They raise the cost and complexity of AI infrastructure in China, while simultaneously increasing the strategic value of domestic AI accelerators. However, local substitution is not a simple chip-for-chip replacement: software ecosystem maturity, compiler quality, collective communication performance, HBM availability, advanced packaging capacity and customer validation remain decisive. The sector remains structurally attractive, but the next phase of competition will be defined by usable platform performance, not headline FLOPS alone.

This report is a detailed and comprehensive analysis for global Cloud AI Training Accelerator Chips market. Both quantitative and qualitative analyses are presented by manufacturers, by region & country, by Type and by Application. As the market is constantly changing, this report explores the competition, supply and demand trends, as well as key factors that contribute to its changing demands across many markets. Company profiles and product examples of selected competitors, along with market share estimates of some of the selected leaders for the year 2025, are provided.

Key Features:

Global Cloud AI Training Accelerator Chips market size and forecasts, in consumption

Global Cloud AI Training Accelerator Chips Market 2026 by Manufacturers, Regions, Type and Application, Foreca...

value (\$ Million), sales quantity (Million Units), and average selling prices (US\$/Unit), 2021-2032

Global Cloud AI Training Accelerator Chips market size and forecasts by region and country, in consumption value (\$ Million), sales quantity (Million Units), and average selling prices (US\$/Unit), 2021-2032

Global Cloud AI Training Accelerator Chips market size and forecasts, by Type and by Application, in consumption value (\$ Million), sales quantity (Million Units), and average selling prices (US\$/Unit), 2021-2032

Global Cloud AI Training Accelerator Chips market shares of main players, shipments in revenue (\$ Million), sales quantity (Million Units), and ASP (US\$/Unit), 2021-2026

The Primary Objectives in This Report Are:

- To determine the size of the total market opportunity of global and key countries

- To assess the growth potential for Cloud AI Training Accelerator Chips

- To forecast future growth in each product and end-use market

- To assess competitive factors affecting the marketplace

This report profiles key players in the global Cloud AI Training Accelerator Chips market based on the following parameters - company overview, sales quantity, revenue, price, gross margin, product portfolio, geographical presence, and key developments. Key companies covered as a part of this study include NVIDIA Corporation, Advanced Micro Devices, Inc., Broadcom Inc., Google LLC, Amazon Web Services, Inc., Huawei Technologies Co., Ltd., Intel Corporation, Marvell Technology, Inc., Cambricon Technologies Corporation Limited, Kunlunxin Technology, etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals.

Market Segmentation

Cloud AI Training Accelerator Chips market is split by Type and by Application. For the

period 2021-2032, the growth among segments provides accurate calculations and forecasts for consumption value by Type, and by Application in terms of volume and value. This analysis can help you expand your business by targeting qualified niche markets.

Market segment by Type

Data Center GPU / GPGPU

AI ASIC / XPU

Other

Market segment by Numeric Precision

FP64 / FP32 Focused

TF32 / BF16 Training

FP16 Training

FP8 / Low-precision Training

INT8 / Mixed Precision

Other Precision Formats

Market segment by Interconnect Capability

Single-card / Standalone Accelerator

Multi-card Server Interconnect

Other

Market segment by Application

Cloud & AI Infrastructure

Scientific Research & Healthcare

Automotive & Industrial AI

Other

Major players covered

NVIDIA Corporation

Advanced Micro Devices, Inc.

Broadcom Inc.

Google LLC

Amazon Web Services, Inc.

Huawei Technologies Co., Ltd.

Intel Corporation

Marvell Technology, Inc.

Cambricon Technologies Corporation Limited

Kunlunxin Technology

MetaX Integrated Circuits Co., Ltd.

Moore Threads Technology Co., Ltd.

Hygon Information Technology Co., Ltd.

Iluvatar CoreX

Shanghai Biren Technology Co., Ltd.

Shanghai Enflame Technology Co., Ltd.

Cerebras Systems, Inc.

Graphcore Limited

Preferred Networks, Inc.

SambaNova Systems, Inc.

Tenstorrent Inc.

Microsoft Corporation

Meta Platforms, Inc.

Market segment by region, regional analysis covers

North America (United States, Canada, and Mexico)

Europe (Germany, France, United Kingdom, Russia, Italy, and Rest of Europe)

Asia-Pacific (China, Japan, Korea, India, Southeast Asia, and Australia)

South America (Brazil, Argentina, Colombia, and Rest of South America)

Middle East & Africa (Saudi Arabia, UAE, Egypt, South Africa, and Rest of Middle East & Africa)

The content of the study subjects, includes a total of 15 chapters:

Chapter 1, to describe Cloud AI Training Accelerator Chips product scope, market overview, market estimation caveats and base year.

Chapter 2, to profile the top manufacturers of Cloud AI Training Accelerator Chips, with

price, sales quantity, revenue, and global market share of Cloud AI Training Accelerator Chips from 2021 to 2026.

Chapter 3, the Cloud AI Training Accelerator Chips competitive situation, sales quantity, revenue, and global market share of top manufacturers are analyzed emphatically by landscape contrast.

Chapter 4, the Cloud AI Training Accelerator Chips breakdown data are shown at the regional level, to show the sales quantity, consumption value, and growth by regions, from 2021 to 2032.

Chapter 5 and 6, to segment the sales by Type and by Application, with sales market share and growth rate by Type, by Application, from 2021 to 2032.

Chapter 7, 8, 9, 10 and 11, to break the sales data at the country level, with sales quantity, consumption value, and market share for key countries in the world, from 2021 to 2026. and Cloud AI Training Accelerator Chips market forecast, by regions, by Type, and by Application, with sales and revenue, from 2027 to 2032.

Chapter 12, market dynamics, drivers, restraints, trends, and Porters Five Forces analysis.

Chapter 13, the key raw materials and key suppliers, and industry chain of Cloud AI Training Accelerator Chips.

Chapter 14 and 15, to describe Cloud AI Training Accelerator Chips sales channel, distributors, customers, research findings and conclusion.

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