

Global Chip Level Sintered Silver Paste Supply, Demand and Key Producers, 2026-2032

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Abstracts

The global Chip Level Sintered Silver Paste market size is expected to reach \$ 342 million by 2032, rising at a market growth of 9.0% CAGR during the forecast period (2026-2032).

Chip level sintered silver paste is a silver based low-temperature sintering material used for semiconductor chip mounting and high reliability packaging interconnection. It is usually composed of nano silver powder, micro silver powder or micro nano composite silver powder, organic carrier, dispersant, rheological modifier, surface modifier, and sintering aid components. Its core function is to form a dense silver sintered connection layer through pressure sintering or pressureless sintering at lower process temperatures, achieving high thermal conductivity, high electrical conductivity, and high reliability connection between the chip and DBC substrate, AMB substrate, metal substrate, lead frame, copper clip, or heat dissipation structure. Compared with traditional solder, the silver connection layer formed by chip level sintered silver paste has a higher melting point, lower thermal resistance, stronger high-temperature resistance, and better power cycle reliability, which is particularly suitable for SiC power devices, GaN devices, IGBT modules MOSFET? Scenarios such as power ICs, automotive grade power modules, high-power LEDs, photovoltaic energy storage inverters, AI server power supplies, and aerospace high reliability electronic packaging. In 2025, global Chip Level Sintered Silver Paste production reached approximately 62.38 MT, with an average global market price of around US\$ 2,896 per kg. The annual production capacity of chip level sintered silver paste is 100 tons, with a gross profit margin of about 45%.

The upstream mainly includes nano silver powder, silver salt precursor, organic carrier, solvent, dispersant, rheological agent, surface modifier, sintering aid material,

packaging material, low-temperature storage and transportation material, sintering equipment, printing and dispensing equipment, and reliability testing equipment.

The downstream is mainly used for SiC power modules, GaN devices, IGBT modules MOSFET? Power ICs, automotive inverters, on-board OBC, DC DC, DBC substrates, AMB substrates, high-power LEDs, photovoltaic energy storage inverters, AI server power supplies, and aerospace high reliability electronic packaging.

Nano silver powder accounts for about 55% to 70% of the total cost, organic carriers, dispersants, rheological agents, surface modifiers, and sintering aids account for about 8% to 15%, research and development verification, reliability testing, and customer certification account for about 6% to 12%, production and manufacturing, clean environment, equipment depreciation, and yield loss account for about 7% to 12%, and packaging, warehousing, low-temperature transportation, quality management, and sales management account for about 5% to 10%.

In the field of semiconductor packaging, chip heat dissipation has always been a key challenge restricting the development of high-performance devices. As a new type of lead-free interconnect material, sintered silver paste has become a 'secret weapon' for solving the heat dissipation problem of high-power chips due to its high thermal conductivity, high reliability, and low-temperature process compatibility. The sintered silver paste achieves a leapfrog improvement in chip heat dissipation performance through the solid-state diffusion mechanism of nano silver particles. Its high thermal conductivity, low-temperature process compatibility, and reliability make it an irreplaceable heat dissipation solution in fields such as new energy vehicles, 5G communication, AI chips, etc. Despite the challenges of cost and process control, with the advancement of materials science and manufacturing processes, sintered silver paste is expected to further promote the development of semiconductor packaging technology towards high power density, miniaturization, and green direction, reshaping the global semiconductor industry landscape.

This report studies the global Chip Level Sintered Silver Paste production, demand, key manufacturers, and key regions.

This report is a detailed and comprehensive analysis of the world market for Chip Level Sintered Silver Paste and provides market size (US\$ million) and Year-over-Year (YoY) Growth, considering 2025 as the base year. This report explores demand trends and competition, as well as details the characteristics of Chip Level Sintered Silver Paste

that contribute to its increasing demand across many markets.

Highlights and key features of the study

Global Chip Level Sintered Silver Paste total production and demand, 2021-2032, (Tons)

Global Chip Level Sintered Silver Paste total production value, 2021-2032, (USD Million)

Global Chip Level Sintered Silver Paste production by region & country, production, value, CAGR, 2021-2032, (USD Million) & (Tons), (based on production site)

Global Chip Level Sintered Silver Paste consumption by region & country, CAGR, 2021-2032 & (Tons)

U.S. VS China: Chip Level Sintered Silver Paste domestic production, consumption, key domestic manufacturers and share

Global Chip Level Sintered Silver Paste production by manufacturer, production, price, value and market share 2021-2026, (USD Million) & (Tons)

Global Chip Level Sintered Silver Paste production by Type, production, value, CAGR, 2021-2032, (USD Million) & (Tons)

Global Chip Level Sintered Silver Paste production by Application, production, value, CAGR, 2021-2032, (USD Million) & (Tons)

This report profiles key players in the global Chip Level Sintered Silver Paste market based on the following parameters - company overview, production, value, price, gross margin, product portfolio, geographical presence, and key developments. Key companies covered as a part of this study include Heraeus Electronics, Indium Corporation, Nihon Superior, MacDermid Alpha, Henkel, KYOCERA Corporation, NAMICS CORPORATION, TANAKA PRECIOUS METAL GROUP, Bando Chemical Industries, NBE Tech, etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals.

Stakeholders would have ease in decision-making through various strategy matrices used in analyzing the World Chip Level Sintered Silver Paste market

Detailed Segmentation:

Each section contains quantitative market data including market by value (US\$ Millions), volume (production, consumption) & (Tons) and average price (US\$/Kg) by manufacturer, by Type, and by Application. Data is given for the years 2021-2032 by

year with 2025 as the base year, 2026 as the estimate year, and 2027-2032 as the forecast year.

Global Chip Level Sintered Silver Paste Market, By Region:

United States

China

Europe

Japan

South Korea

ASEAN

India

Rest of World

Global Chip Level Sintered Silver Paste Market, Segmentation by Type:

Pressure Sintering Paste

Pressureless Sintering Paste

Global Chip Level Sintered Silver Paste Market, Segmentation by Product Form:

Printed Sintered Silver Paste

Glue Type Sintered Silver Paste

Global Chip Level Sintered Silver Paste Market, Segmentation by Sintering Time:

Sintering Time?5min

Sintering Time>5min

Global Chip Level Sintered Silver Paste Market, Segmentation by Application:

New Energy Vehicle Power Module

AI Chip

5G Communication & Data Center

Consumer Electronics

Aerospace and Military Industry

Other

Companies Profiled:

Heraeus Electronics

Indium Corporation

Nihon Superior

MacDermid Alpha

Henkel

KYOCERA Corporation

NAMICS CORPORATION

TANAKA PRECIOUS METAL GROUP

Bando Chemical Industries

NBE Tech

Shenzhen Xinyuan New Materials

Solderwell Advanced Materials

Shenzhen Jufeng Solder

Qinglian Technology

Shenzhen Advanced Joining Technology

Key Questions Answered:

1. How big is the global Chip Level Sintered Silver Paste market?
2. What is the demand of the global Chip Level Sintered Silver Paste market?
3. What is the year over year growth of the global Chip Level Sintered Silver Paste market?
4. What is the production and production value of the global Chip Level Sintered Silver Paste market?
5. Who are the key producers in the global Chip Level Sintered Silver Paste market?
6. What are the growth factors driving the market demand?

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