

Global Chip Level Sintered Copper Paste Market 2026 by Manufacturers, Regions, Type and Application, Forecast to 2032

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Abstracts

According to our (Global Info Research) latest study, the global Chip Level Sintered Copper Paste market size was valued at US\$ 53.86 million in 2025 and is forecast to a readjusted size of US\$ 94.95 million by 2032 with a CAGR of 8.4% during review period.

Chip level sintered copper paste is a copper based low-temperature sintered connection material used for power semiconductor chip mounting and high reliability packaging interconnection. It is usually composed of highly active copper powder, nano copper or micro nano composite copper powder, organic carrier, dispersant, antioxidant, reducing agent, and rheological adjustment component. Its core function is to form a dense copper interconnect layer through pressure or pressureless sintering processes at lower sintering temperatures, achieving high thermal conductivity, high electrical conductivity, and high strength connections between chips and DBC substrates, AMB substrates, copper substrates, lead frames, or heat dissipation structures. Compared with traditional soldering materials, sintered copper layers have higher melting points, better high-temperature resistance, lower thermal resistance, and stronger power cycling reliability; Compared with sintered silver paste, sintered copper paste has material cost advantages and potential for resistance to electromigration, but requires higher requirements for copper powder oxidation resistance, sintering atmosphere, interface activation, storage stability, and process window control. This product is mainly used for SiC power devices, GaN devices, IGBT modules MOSFET? Vehicle standard power module, photovoltaic energy storage inverter, AI server power module, and high power density semiconductor packaging. In 2025, global Chip Level Sintered Copper Paste production reached approximately 24.12 MT, with an average global market price of around US\$ 2,170 per kg. The annual production capacity of chip level sintered copper

paste is 35 tons, with a gross profit margin of about 40%.

The upstream mainly includes nano copper powder, copper salt precursor, reducing agent, antioxidant coating material, organic carrier, solvent, dispersant, rheological agent, interface activator, packaging material, sintering equipment and detection equipment

Downstream applications mainly include SiC power devices, GaN devices, IGBT modules MOSFET? Vehicle specification power module, DBC substrate, AMB substrate, new energy vehicle inverter, photovoltaic energy storage inverter, AI server power supply, and high power density packaging.

According to the cost structure estimation, nano copper powder and high-purity copper powder account for about 35% to 50% of the total cost, organic carriers, reducing agents, antioxidants, dispersants, and rheological agents account for about 15% to 25%, research and development verification, reliability testing, and customer certification account for about 8% to 15%, production and manufacturing, clean environment, equipment depreciation, and yield loss account for about 10% to 18%, and packaging, warehousing, low-temperature or anti-oxidation transportation and management expenses account for about 6% to 10%.

Chip level sintered copper paste is at a critical stage of transitioning from technology validation to industrialization, driven by the increasing demand for low thermal resistance, high reliability, and low-cost interconnect materials in new energy vehicle high-voltage platforms, SiC power modules, photovoltaic energy storage inverters, data center power supplies, and high-power density packaging. Sintered silver paste has gained high recognition in power semiconductor packaging, but silver material costs are relatively high, and large-area chip mounting and automotive modules are becoming increasingly sensitive to material cost control. Therefore, sintered copper paste is considered an important alternative route. The current industry challenges mainly focus on the easy oxidation of copper powder, insufficient low-temperature sintering activity, density control of pressureless sintering, matching of interface wetting and metallization layer, porosity control, long-term wet heat reliability, and long customer verification cycle for automotive specifications. The future competition will focus on the preparation of nano copper powder, micro nano particle size compounding, antioxidant coating, reduced organic system, low porosity sintering process, adaptation of pressure and non pressure equipment, compatibility of AMB and DBC substrate interfaces, and module factory certification capabilities. Overall, chip level sintered copper paste is still difficult to fully replace sintered silver paste in the short term, but it has strong growth potential

in cost sensitive and high thermal management requirements SiC modules, IGBT modules, automotive power devices, and new energy power electronics. Enterprises with integrated capabilities in powder materials, slurry formulations, packaging processes, and reliability verification are more likely to obtain opportunities for mass production and import.

This report is a detailed and comprehensive analysis for global Chip Level Sintered Copper Paste market. Both quantitative and qualitative analyses are presented by manufacturers, by region & country, by Type and by Application. As the market is constantly changing, this report explores the competition, supply and demand trends, as well as key factors that contribute to its changing demands across many markets. Company profiles and product examples of selected competitors, along with market share estimates of some of the selected leaders for the year 2025, are provided.

Key Features:

Global Chip Level Sintered Copper Paste market size and forecasts, in consumption value (\$ Million), sales quantity (Tons), and average selling prices (US\$/Kg), 2021-2032

Global Chip Level Sintered Copper Paste market size and forecasts by region and country, in consumption value (\$ Million), sales quantity (Tons), and average selling prices (US\$/Kg), 2021-2032

Global Chip Level Sintered Copper Paste market size and forecasts, by Type and by Application, in consumption value (\$ Million), sales quantity (Tons), and average selling prices (US\$/Kg), 2021-2032

Global Chip Level Sintered Copper Paste market shares of main players, shipments in revenue (\$ Million), sales quantity (Tons), and ASP (US\$/Kg), 2021-2026

The Primary Objectives in This Report Are:

To determine the size of the total market opportunity of global and key countries

To assess the growth potential for Chip Level Sintered Copper Paste

To forecast future growth in each product and end-use market

To assess competitive factors affecting the marketplace

This report profiles key players in the global Chip Level Sintered Copper Paste market based on the following parameters - company overview, sales quantity, revenue, price, gross margin, product portfolio, geographical presence, and key developments. Key companies covered as a part of this study include Heraeus Electronics, Indium Corporation, Mitsuboshi Belting, Resonac Holdings Corporation, MITSUI KINZOKU, SCHLENK, MacDermid Alpha, Elephantech, Solderwell Advanced Materials, Shenzhen Xinyuan New Materials, etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals.

Market Segmentation

Chip Level Sintered Copper Paste market is split by Type and by Application. For the period 2021-2032, the growth among segments provides accurate calculations and forecasts for consumption value by Type, and by Application in terms of volume and value. This analysis can help you expand your business by targeting qualified niche markets.

Market segment by Type

Pressureless Sintered Copper Paste

Pressure Sintered Copper Paste

Market segment by Particle Size

Particle Size?1?m

Particle Size?3?m

Market segment by Sintering Time

Sintering Time?5min

Sintering Time>5min

Market segment by Application

New Energy Vehicle

Industry and Rail Transit

RF and Optoelectronics

Other

Major players covered

Heraeus Electronics

Indium Corporation

Mitsuboshi Belting

Resonac Holdings Corporation

MITSUI KINZOKU

SCHLENK

MacDermid Alpha

Elephantech

Solderwell Advanced Materials

Shenzhen Xinyuan New Materials

Shenzhen Jufeng Solder

Qinglian Technology

Chongqing Pingchuang Institute of Semiconductors

Market segment by region, regional analysis covers

North America (United States, Canada, and Mexico)

Europe (Germany, France, United Kingdom, Russia, Italy, and Rest of Europe)

Asia-Pacific (China, Japan, Korea, India, Southeast Asia, and Australia)

South America (Brazil, Argentina, Colombia, and Rest of South America)

Middle East & Africa (Saudi Arabia, UAE, Egypt, South Africa, and Rest of Middle East & Africa)

The content of the study subjects, includes a total of 15 chapters:

Chapter 1, to describe Chip Level Sintered Copper Paste product scope, market overview, market estimation caveats and base year.

Chapter 2, to profile the top manufacturers of Chip Level Sintered Copper Paste, with price, sales quantity, revenue, and global market share of Chip Level Sintered Copper Paste from 2021 to 2026.

Chapter 3, the Chip Level Sintered Copper Paste competitive situation, sales quantity, revenue, and global market share of top manufacturers are analyzed emphatically by landscape contrast.

Chapter 4, the Chip Level Sintered Copper Paste breakdown data are shown at the regional level, to show the sales quantity, consumption value, and growth by regions, from 2021 to 2032.

Chapter 5 and 6, to segment the sales by Type and by Application, with sales market share and growth rate by Type, by Application, from 2021 to 2032.

Chapter 7, 8, 9, 10 and 11, to break the sales data at the country level, with sales

quantity, consumption value, and market share for key countries in the world, from 2021 to 2026. and Chip Level Sintered Copper Paste market forecast, by regions, by Type, and by Application, with sales and revenue, from 2027 to 2032.

Chapter 12, market dynamics, drivers, restraints, trends, and Porters Five Forces analysis.

Chapter 13, the key raw materials and key suppliers, and industry chain of Chip Level Sintered Copper Paste.

Chapter 14 and 15, to describe Chip Level Sintered Copper Paste sales channel, distributors, customers, research findings and conclusion.

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