

Global AI Wafer Defect Inspection Supply, Demand and Key Producers, 2026-2032

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Abstracts

The global AI Wafer Defect Inspection market size is expected to reach \$ 1296 million by 2032, rising at a market growth of 10.0% CAGR during the forecast period (2026-2032).

AI Wafer Defect Inspection refers to intelligent semiconductor inspection systems that utilize artificial intelligence technologies, including machine learning, deep learning and computer vision algorithms, to identify, classify and analyze wafer defects during semiconductor manufacturing processes. The research scope covers AI-enabled inspection equipment and software systems designed for wafer surface inspection, pattern defect detection, defect classification and process monitoring. These systems combine optical inspection, electron beam inspection, high-resolution imaging and automated data analysis capabilities to improve detection accuracy, inspection efficiency and manufacturing process control. AI Wafer Defect Inspection is mainly applied in wafer fabrication processes for advanced semiconductor manufacturing, including logic chips, memory devices, compound semiconductors and other semiconductor production environments requiring high precision defect detection.

Key Findings

AI Wafer Defect Inspection improves semiconductor yield management through intelligent defect detection and classification

Advanced semiconductor manufacturing is the primary application area for AI-based wafer inspection solutions

Deep learning and computer vision technologies enhance inspection accuracy for complex wafer patterns

Asia Pacific represents a major demand region supported by semiconductor manufacturing expansion

AI wafer inspection is increasingly integrated with automated process control systems

Market Trends

The AI Wafer Defect Inspection industry is developing toward higher inspection precision, automation and data-driven semiconductor manufacturing. As semiconductor process nodes continue to shrink and wafer structures become increasingly complex, traditional inspection methods face challenges in detecting small-scale defects and distinguishing process variations from actual failures. AI technologies are being integrated into inspection platforms to improve defect recognition, classification efficiency and process learning capabilities. Future development is expected to focus on combining high-resolution imaging, advanced algorithms and semiconductor manufacturing data platforms to enable faster decision-making and more intelligent process optimization.

Market Dynamics

Drivers

The continuous advancement of semiconductor technology, increasing complexity of wafer manufacturing processes and demand for higher production yield are major drivers for AI Wafer Defect Inspection adoption. Semiconductor manufacturers require more accurate and efficient inspection capabilities to control defects in advanced manufacturing processes, reduce production losses and improve overall equipment utilization.

Restraints

The market faces challenges from high equipment costs, complex technology

integration and demanding semiconductor manufacturing requirements. Developing effective AI inspection systems requires significant investment in imaging technologies, computing infrastructure, algorithm development and semiconductor process knowledge. In addition, differences between manufacturing processes create challenges for standardization and deployment.

Opportunities

Opportunities are emerging from advanced semiconductor production, artificial intelligence-driven manufacturing upgrades and increasing adoption of smart fabs. The integration of AI inspection with manufacturing execution systems, automated defect review and predictive process analytics provides new growth opportunities for suppliers offering comprehensive semiconductor intelligence solutions.

Challenges

The industry faces challenges related to algorithm reliability, large-scale data requirements and the need for continuous model optimization. AI Wafer Defect Inspection systems must maintain high accuracy under changing process conditions, while semiconductor manufacturers require strong data security, integration capability and long-term technical support.

Industry Chain Analysis

The AI Wafer Defect Inspection industry chain includes upstream semiconductor inspection components and technology suppliers, midstream inspection equipment and solution providers, and downstream semiconductor manufacturers. Upstream suppliers provide optical components, imaging sensors, semiconductor measurement technologies, computing hardware and software development resources. Midstream companies integrate AI algorithms, inspection hardware, imaging technologies and data analysis platforms to develop intelligent wafer inspection solutions. Downstream customers include wafer foundries, integrated device manufacturers and semiconductor research facilities that use inspection systems for process control and yield improvement.

The value creation of AI Wafer Defect Inspection is mainly reflected in inspection accuracy, data processing capability, process integration and manufacturing efficiency

improvement. Compared with traditional inspection systems, AI-based solutions create differentiation through advanced algorithms, automated classification capabilities and continuous learning functions. Equipment performance, software capability and semiconductor process expertise jointly determine supplier competitiveness and cost structure.

Segment Insights

AI Wafer Defect Inspection can be segmented by inspection technology, AI methodology, wafer application and defect detection capability. Optical-based AI wafer inspection represents a major technology segment because of its high throughput and suitability for large-scale semiconductor manufacturing. Deep learning-based inspection solutions are gaining importance due to their ability to analyze complex wafer patterns and improve defect classification accuracy.

By application, advanced logic semiconductor manufacturing, memory production and compound semiconductor fabrication represent key demand areas. As semiconductor manufacturers move toward smaller process nodes and more complex device structures, demand is increasingly shifting toward high-resolution inspection systems with intelligent analysis capabilities. Future opportunities are expected to concentrate on AI-powered defect review, automated classification and integrated manufacturing intelligence platforms.

Downstream Market Opportunities

The downstream market for AI Wafer Defect Inspection is closely connected with semiconductor fabrication, advanced chip manufacturing and smart factory development. Semiconductor manufacturers increasingly require inspection solutions that improve yield, reduce production variation and accelerate process optimization. Growth opportunities are expected from advanced semiconductor nodes, memory technology development, automotive semiconductor production and expanding semiconductor manufacturing capacity.

Regional Insights

Asia Pacific represents a major regional market for AI Wafer Defect Inspection due to its strong semiconductor manufacturing base, wafer fabrication capacity and continued

investment in advanced semiconductor production. The region benefits from expanding foundry activities, memory manufacturing and semiconductor supply chain development. North America and Europe maintain demand through semiconductor technology innovation, equipment development and strategic semiconductor manufacturing initiatives.

Regional market differences are mainly reflected in semiconductor ecosystem maturity, manufacturing capacity and technology investment levels. Asia Pacific focuses more on large-scale semiconductor production deployment, while North America and Europe emphasize advanced technology development, research applications and high-value semiconductor manufacturing processes.

Competitive Landscape Analysis

The AI Wafer Defect Inspection market is characterized by competition among semiconductor inspection equipment manufacturers, process control technology providers and intelligent manufacturing solution companies. Competitive advantages are primarily determined by inspection accuracy, AI algorithm capability, hardware integration, semiconductor process knowledge and customer support services. Leading participants typically focus on combining advanced imaging technologies with artificial intelligence algorithms to improve defect detection and classification performance. The market is gradually moving from standalone inspection equipment toward integrated intelligent inspection platforms connected with semiconductor manufacturing data systems.

Report Scope

This report studies the global AI Wafer Defect Inspection demand, key companies, and key regions.

This report is a detailed and comprehensive analysis of the world market for AI Wafer Defect Inspection, and provides market size (US\$ million) and Year-over-Year (YoY) growth, considering 2025 as the base year. This report explores demand trends and competition, as well as details the characteristics of AI Wafer Defect Inspection that contribute to its increasing demand across many markets.

Highlights and key features of the study

Global AI Wafer Defect Inspection total market, 2021-2032, (USD Million)

Global AI Wafer Defect Inspection total market by region & country, CAGR, 2021-2032, (USD Million)

U.S. VS China: AI Wafer Defect Inspection total market, key domestic companies, and share, (USD Million)

Global AI Wafer Defect Inspection revenue by player, revenue and market share 2021-2026, (USD Million)

Global AI Wafer Defect Inspection total market by Type, CAGR, 2021-2032, (USD Million)

Global AI Wafer Defect Inspection total market by Application, CAGR, 2021-2032, (USD Million)

This report profiles major players in the global AI Wafer Defect Inspection market based on the following parameters - company overview, revenue, gross margin, product portfolio, geographical presence, and key developments. Key companies covered as a part of this study include KLA Corporation, Cognex, Onto Innovation, ASML Holding N.V., Hitachi High-Tech Corporation, Tokyo Electron Limited, SCREEN Semiconductor Solutions Co., Ltd., Toshiba Corporation, Camtek Ltd., Shanghai Jingce Semiconductor Technology, etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals.

Stakeholders would have ease in decision-making through various strategy matrices used in analyzing the world AI Wafer Defect Inspection market

Detailed Segmentation:

Each section contains quantitative market data including market by value (US\$

Millions), by player, by regions, by Type, and by Application. Data is given for the years 2021-2032 by year with 2025 as the base year, 2026 as the estimate year, and 2027-2032 as the forecast year.

Global AI Wafer Defect Inspection Market, By Region:

United States

China

Europe

Japan

South Korea

ASEAN

India

Rest of World

Market Segmentation

Global AI Wafer Defect Inspection Market, Segmentation by Type:

Nanometer Scale Defect Inspection (<10 nm)

Sub-Micron Defect Inspection (10 nm-1 ?m)

Micron Scale Defect Inspection (>1 ?m)

Global AI Wafer Defect Inspection Market, Segmentation by Speed:

High Throughput Wafer Inspection (>100 wafers/hour)

Medium Throughput Wafer Inspection (20-100 wafers/hour)

Low Throughput Wafer Inspection (<20 wafers/hour)

Global AI Wafer Defect Inspection Market, Segmentation by Application:

Semiconductor Manufacturing

Integrated Circuit Fabrication

Memory Chip Production

Logic Chip Production

Others

Companies Profiled:

KLA Corporation

Cognex

Onto Innovation

ASML Holding N.V.

Hitachi High-Tech Corporation

Tokyo Electron Limited

SCREEN Semiconductor Solutions Co., Ltd.

Toshiba Corporation

Camtek Ltd.

Shanghai Jingce Semiconductor Technology

Okmetic

Confovis

Averian

Key Questions Answered

1. How big is the global AI Wafer Defect Inspection market?
2. What is the demand of the global AI Wafer Defect Inspection market?
3. What is the year over year growth of the global AI Wafer Defect Inspection market?
4. What is the total value of the global AI Wafer Defect Inspection market?
5. Who are the Major Players in the global AI Wafer Defect Inspection market?
6. What are the growth factors driving the market demand?

Contents

1 SUPPLY SUMMARY

- 1.1 AI Wafer Defect Inspection Introduction
- 1.2 World AI Wafer Defect Inspection Market Size & Forecast (2021 & 2025 & 2032)
- 1.3 World AI Wafer Defect Inspection Total Market by Region (by Headquarter Location)
 - 1.3.1 World AI Wafer Defect Inspection Market Size by Region (2021-2032), (by Headquarter Location)
 - 1.3.2 United States Based Company AI Wafer Defect Inspection Revenue (2021-2032)
 - 1.3.3 China Based Company AI Wafer Defect Inspection Revenue (2021-2032)
 - 1.3.4 Europe Based Company AI Wafer Defect Inspection Revenue (2021-2032)
 - 1.3.5 Japan Based Company AI Wafer Defect Inspection Revenue (2021-2032)
 - 1.3.6 South Korea Based Company AI Wafer Defect Inspection Revenue (2021-2032)
 - 1.3.7 ASEAN Based Company AI Wafer Defect Inspection Revenue (2021-2032)
 - 1.3.8 India Based Company AI Wafer Defect Inspection Revenue (2021-2032)
- 1.4 Market Drivers, Restraints and Trends
 - 1.4.1 AI Wafer Defect Inspection Market Drivers
 - 1.4.2 Factors Affecting Demand
 - 1.4.3 Major Market Trends

2 DEMAND SUMMARY

- 2.1 World AI Wafer Defect Inspection Consumption Value (2021-2032)
- 2.2 World AI Wafer Defect Inspection Consumption Value by Region
 - 2.2.1 World AI Wafer Defect Inspection Consumption Value by Region (2021-2026)
 - 2.2.2 World AI Wafer Defect Inspection Consumption Value Forecast by Region (2027-2032)
- 2.3 United States AI Wafer Defect Inspection Consumption Value (2021-2032)
- 2.4 China AI Wafer Defect Inspection Consumption Value (2021-2032)
- 2.5 Europe AI Wafer Defect Inspection Consumption Value (2021-2032)
- 2.6 Japan AI Wafer Defect Inspection Consumption Value (2021-2032)
- 2.7 South Korea AI Wafer Defect Inspection Consumption Value (2021-2032)
- 2.8 ASEAN AI Wafer Defect Inspection Consumption Value (2021-2032)
- 2.9 India AI Wafer Defect Inspection Consumption Value (2021-2032)

3 WORLD AI WAFER DEFECT INSPECTION COMPANIES COMPETITIVE ANALYSIS

- 3.1 World AI Wafer Defect Inspection Revenue by Player (2021-2026)
- 3.2 Industry Rank and Concentration Rate (CR)
 - 3.2.1 Global AI Wafer Defect Inspection Industry Rank of Major Players
 - 3.2.2 Global Concentration Ratios (CR4) for AI Wafer Defect Inspection in 2025
 - 3.2.3 Global Concentration Ratios (CR8) for AI Wafer Defect Inspection in 2025
- 3.3 AI Wafer Defect Inspection Company Evaluation Quadrant
- 3.4 AI Wafer Defect Inspection Market: Overall Company Footprint Analysis
 - 3.4.1 AI Wafer Defect Inspection Market: Region Footprint
 - 3.4.2 AI Wafer Defect Inspection Market: Company Product Type Footprint
 - 3.4.3 AI Wafer Defect Inspection Market: Company Product Application Footprint
- 3.5 Competitive Environment
 - 3.5.1 Historical Structure of the Industry
 - 3.5.2 Barriers of Market Entry
 - 3.5.3 Factors of Competition
- 3.6 Mergers & Acquisitions Activity

4 UNITED STATES VS CHINA VS REST OF WORLD (BY HEADQUARTER LOCATION)

- 4.1 United States VS China: AI Wafer Defect Inspection Revenue Comparison (by Headquarter Location)
 - 4.1.1 United States VS China: AI Wafer Defect Inspection Revenue Comparison (2021 & 2025 & 2032) (by Headquarter Location)
 - 4.1.2 United States VS China: AI Wafer Defect Inspection Revenue Market Share Comparison (2021 & 2025 & 2032)
- 4.2 United States Based Companies VS China Based Companies: AI Wafer Defect Inspection Consumption Value Comparison
 - 4.2.1 United States VS China: AI Wafer Defect Inspection Consumption Value Comparison (2021 & 2025 & 2032)
 - 4.2.2 United States VS China: AI Wafer Defect Inspection Consumption Value Market Share Comparison (2021 & 2025 & 2032)
- 4.3 United States Based AI Wafer Defect Inspection Companies and Market Share, 2021-2026
 - 4.3.1 United States Based AI Wafer Defect Inspection Companies, Headquarters (States, Country)
 - 4.3.2 United States Based Companies AI Wafer Defect Inspection Revenue, (2021-2026)
- 4.4 China Based Companies AI Wafer Defect Inspection Revenue and Market Share, 2021-2026

4.4.1 China Based AI Wafer Defect Inspection Companies, Company Headquarters (Province, Country)

4.4.2 China Based Companies AI Wafer Defect Inspection Revenue, (2021-2026)

4.5 Rest of World Based AI Wafer Defect Inspection Companies and Market Share, 2021-2026

4.5.1 Rest of World Based AI Wafer Defect Inspection Companies, Headquarters (Province, Country)

4.5.2 Rest of World Based Companies AI Wafer Defect Inspection Revenue (2021-2026)

5 MARKET ANALYSIS BY TYPE

5.1 World AI Wafer Defect Inspection Market Size Overview by Type: 2021 VS 2025 VS 2032

5.2 Segment Introduction by Type

5.2.1 Nanometer Scale Defect Inspection (<10 nm)

5.2.2 Sub-Micron Defect Inspection (10 nm-1 ?m)

5.2.3 Micron Scale Defect Inspection (>1 ?m)

5.3 Market Segment by Type

5.3.1 World AI Wafer Defect Inspection Market Size by Type (2021-2026)

5.3.2 World AI Wafer Defect Inspection Market Size by Type (2027-2032)

5.3.3 World AI Wafer Defect Inspection Market Size Market Share by Type (2027-2032)

6 MARKET ANALYSIS BY SPEED

6.1 World AI Wafer Defect Inspection Market Size Overview by Speed: 2021 VS 2025 VS 2032

6.2 Segment Introduction by Speed

6.2.1 High Throughput Wafer Inspection (>100 wafers/hour)

6.2.2 Medium Throughput Wafer Inspection (20-100 wafers/hour)

6.2.3 Low Throughput Wafer Inspection (<20 wafers/hour)

6.3 Market Segment by Speed

6.3.1 World AI Wafer Defect Inspection Market Size by Speed (2021-2026)

6.3.2 World AI Wafer Defect Inspection Market Size by Speed (2027-2032)

6.3.3 World AI Wafer Defect Inspection Market Size Market Share by Speed (2027-2032)

7 MARKET ANALYSIS BY APPLICATION

7.1 World AI Wafer Defect Inspection Market Size Overview by Application: 2021 VS 2025 VS 2032

7.2 Segment Introduction by Application

7.2.1 Semiconductor Manufacturing

7.2.2 Integrated Circuit Fabrication

7.2.3 Memory Chip Production

7.2.4 Logic Chip Production

7.2.5 Others

7.3 Market Segment by Application

7.3.1 World AI Wafer Defect Inspection Market Size by Application (2021-2026)

7.3.2 World AI Wafer Defect Inspection Market Size by Application (2027-2032)

7.3.3 World AI Wafer Defect Inspection Market Size Market Share by Application (2021-2032)

8 COMPANY PROFILES

8.1 KLA Corporation

8.1.1 KLA Corporation Details

8.1.2 KLA Corporation Major Business

8.1.3 KLA Corporation AI Wafer Defect Inspection Product and Services

8.1.4 KLA Corporation AI Wafer Defect Inspection Revenue, Gross Margin and Market Share (2021-2026)

8.1.5 KLA Corporation Recent Developments/Updates

8.1.6 KLA Corporation Competitive Strengths & Weaknesses

8.2 Cognex

8.2.1 Cognex Details

8.2.2 Cognex Major Business

8.2.3 Cognex AI Wafer Defect Inspection Product and Services

8.2.4 Cognex AI Wafer Defect Inspection Revenue, Gross Margin and Market Share (2021-2026)

8.2.5 Cognex Recent Developments/Updates

8.2.6 Cognex Competitive Strengths & Weaknesses

8.3 Onto Innovation

8.3.1 Onto Innovation Details

8.3.2 Onto Innovation Major Business

8.3.3 Onto Innovation AI Wafer Defect Inspection Product and Services

8.3.4 Onto Innovation AI Wafer Defect Inspection Revenue, Gross Margin and Market Share (2021-2026)

- 8.3.5 Onto Innovation Recent Developments/Updates
- 8.3.6 Onto Innovation Competitive Strengths & Weaknesses
- 8.4 ASML Holding N.V.
 - 8.4.1 ASML Holding N.V. Details
 - 8.4.2 ASML Holding N.V. Major Business
 - 8.4.3 ASML Holding N.V. AI Wafer Defect Inspection Product and Services
 - 8.4.4 ASML Holding N.V. AI Wafer Defect Inspection Revenue, Gross Margin and Market Share (2021-2026)
 - 8.4.5 ASML Holding N.V. Recent Developments/Updates
 - 8.4.6 ASML Holding N.V. Competitive Strengths & Weaknesses
- 8.5 Hitachi High-Tech Corporation
 - 8.5.1 Hitachi High-Tech Corporation Details
 - 8.5.2 Hitachi High-Tech Corporation Major Business
 - 8.5.3 Hitachi High-Tech Corporation AI Wafer Defect Inspection Product and Services
 - 8.5.4 Hitachi High-Tech Corporation AI Wafer Defect Inspection Revenue, Gross Margin and Market Share (2021-2026)
 - 8.5.5 Hitachi High-Tech Corporation Recent Developments/Updates
 - 8.5.6 Hitachi High-Tech Corporation Competitive Strengths & Weaknesses
- 8.6 Tokyo Electron Limited
 - 8.6.1 Tokyo Electron Limited Details
 - 8.6.2 Tokyo Electron Limited Major Business
 - 8.6.3 Tokyo Electron Limited AI Wafer Defect Inspection Product and Services
 - 8.6.4 Tokyo Electron Limited AI Wafer Defect Inspection Revenue, Gross Margin and Market Share (2021-2026)
 - 8.6.5 Tokyo Electron Limited Recent Developments/Updates
 - 8.6.6 Tokyo Electron Limited Competitive Strengths & Weaknesses
- 8.7 SCREEN Semiconductor Solutions Co., Ltd.
 - 8.7.1 SCREEN Semiconductor Solutions Co., Ltd. Details
 - 8.7.2 SCREEN Semiconductor Solutions Co., Ltd. Major Business
 - 8.7.3 SCREEN Semiconductor Solutions Co., Ltd. AI Wafer Defect Inspection Product and Services
 - 8.7.4 SCREEN Semiconductor Solutions Co., Ltd. AI Wafer Defect Inspection Revenue, Gross Margin and Market Share (2021-2026)
 - 8.7.5 SCREEN Semiconductor Solutions Co., Ltd. Recent Developments/Updates
 - 8.7.6 SCREEN Semiconductor Solutions Co., Ltd. Competitive Strengths & Weaknesses
- 8.8 Toshiba Corporation
 - 8.8.1 Toshiba Corporation Details
 - 8.8.2 Toshiba Corporation Major Business

- 8.8.3 Toshiba Corporation AI Wafer Defect Inspection Product and Services
- 8.8.4 Toshiba Corporation AI Wafer Defect Inspection Revenue, Gross Margin and Market Share (2021-2026)
- 8.8.5 Toshiba Corporation Recent Developments/Updates
- 8.8.6 Toshiba Corporation Competitive Strengths & Weaknesses
- 8.9 Camtek Ltd.
- 8.9.1 Camtek Ltd. Details
- 8.9.2 Camtek Ltd. Major Business
- 8.9.3 Camtek Ltd. AI Wafer Defect Inspection Product and Services
- 8.9.4 Camtek Ltd. AI Wafer Defect Inspection Revenue, Gross Margin and Market Share (2021-2026)
- 8.9.5 Camtek Ltd. Recent Developments/Updates
- 8.9.6 Camtek Ltd. Competitive Strengths & Weaknesses
- 8.10 Shanghai Jingce Semiconductor Technology
- 8.10.1 Shanghai Jingce Semiconductor Technology Details
- 8.10.2 Shanghai Jingce Semiconductor Technology Major Business
- 8.10.3 Shanghai Jingce Semiconductor Technology AI Wafer Defect Inspection Product and Services
- 8.10.4 Shanghai Jingce Semiconductor Technology AI Wafer Defect Inspection Revenue, Gross Margin and Market Share (2021-2026)
- 8.10.5 Shanghai Jingce Semiconductor Technology Recent Developments/Updates
- 8.10.6 Shanghai Jingce Semiconductor Technology Competitive Strengths & Weaknesses
- 8.11 Okmetic
- 8.11.1 Okmetic Details
- 8.11.2 Okmetic Major Business
- 8.11.3 Okmetic AI Wafer Defect Inspection Product and Services
- 8.11.4 Okmetic AI Wafer Defect Inspection Revenue, Gross Margin and Market Share (2021-2026)
- 8.11.5 Okmetic Recent Developments/Updates
- 8.11.6 Okmetic Competitive Strengths & Weaknesses
- 8.12 Confovis
- 8.12.1 Confovis Details
- 8.12.2 Confovis Major Business
- 8.12.3 Confovis AI Wafer Defect Inspection Product and Services
- 8.12.4 Confovis AI Wafer Defect Inspection Revenue, Gross Margin and Market Share (2021-2026)
- 8.12.5 Confovis Recent Developments/Updates
- 8.12.6 Confovis Competitive Strengths & Weaknesses

8.13 Averian

8.13.1 Averian Details

8.13.2 Averian Major Business

8.13.3 Averian AI Wafer Defect Inspection Product and Services

8.13.4 Averian AI Wafer Defect Inspection Revenue, Gross Margin and Market Share (2021-2026)

8.13.5 Averian Recent Developments/Updates

8.13.6 Averian Competitive Strengths & Weaknesses

9 INDUSTRY CHAIN ANALYSIS

9.1 AI Wafer Defect Inspection Industry Chain

9.2 AI Wafer Defect Inspection Upstream Analysis

9.3 AI Wafer Defect Inspection Midstream Analysis

9.4 AI Wafer Defect Inspection Downstream Analysis

10 RESEARCH FINDINGS AND CONCLUSION

11 APPENDIX

11.1 Methodology

11.2 Research Process and Data Source

11.3 Disclaimer

List Of Tables

LIST OF TABLES

Table 1. World AI Wafer Defect Inspection Revenue by Region (2021, 2025 and 2032) & (USD Million), (by Headquarter Location)

Table 2. World AI Wafer Defect Inspection Revenue by Region (2021-2026) & (USD Million), (by Headquarter Location)

Table 3. World AI Wafer Defect Inspection Revenue by Region (2027-2032) & (USD Million), (by Headquarter Location)

Table 4. World AI Wafer Defect Inspection Revenue Market Share by Region (2021-2026), (by Headquarter Location)

Table 5. World AI Wafer Defect Inspection Revenue Market Share by Region (2027-2032), (by Headquarter Location)

Table 6. Major Market Trends

Table 7. World AI Wafer Defect Inspection Consumption Value Growth Rate Forecast by Region (2021 & 2025 & 2032) & (USD Million)

Table 8. World AI Wafer Defect Inspection Consumption Value by Region (2021-2026) & (USD Million)

Table 9. World AI Wafer Defect Inspection Consumption Value Forecast by Region (2027-2032) & (USD Million)

Table 10. World AI Wafer Defect Inspection Revenue by Player (2021-2026) & (USD Million)

Table 11. Revenue Market Share of Key AI Wafer Defect Inspection Players in 2025

Table 12. World AI Wafer Defect Inspection Industry Rank of Major Player, Based on Revenue in 2025

Table 13. Global AI Wafer Defect Inspection Company Evaluation Quadrant

Table 14. Head Office of Key AI Wafer Defect Inspection Players

Table 15. AI Wafer Defect Inspection Market: Company Product Type Footprint

Table 16. AI Wafer Defect Inspection Market: Company Product Application Footprint

Table 17. AI Wafer Defect Inspection Mergers & Acquisitions Activity

Table 18. United States VS China AI Wafer Defect Inspection Revenue Comparison, (2021 & 2025 & 2032) & (USD Million)

Table 19. United States VS China AI Wafer Defect Inspection Consumption Value Comparison, (2021 & 2025 & 2032) & (USD Million)

Table 20. United States Based AI Wafer Defect Inspection Companies, Headquarters (States, Country)

Table 21. United States Based Companies AI Wafer Defect Inspection Revenue, (2021-2026) & (USD Million)

Table 22. United States Based Companies AI Wafer Defect Inspection Revenue Market Share (2021-2026)

Table 23. China Based AI Wafer Defect Inspection Companies, Headquarters (Province, Country)

Table 24. China Based Companies AI Wafer Defect Inspection Revenue, (2021-2026) & (USD Million)

Table 25. China Based Companies AI Wafer Defect Inspection Revenue Market Share (2021-2026)

Table 26. Rest of World Based AI Wafer Defect Inspection Companies, Headquarters (Province, Country)

Table 27. Rest of World Based Companies AI Wafer Defect Inspection Revenue (2021-2026) & (USD Million)

Table 28. Rest of World Based Companies AI Wafer Defect Inspection Revenue Market Share (2021-2026)

Table 29. World AI Wafer Defect Inspection Market Size by Type, (USD Million), 2021 & 2025 & 2032

Table 30. World AI Wafer Defect Inspection Market Size Value by Type (2021-2026) & (USD Million)

Table 31. World AI Wafer Defect Inspection Market Size by Type (2027-2032) & (USD Million)

Table 32. World AI Wafer Defect Inspection Market Size by Speed, (USD Million), 2021 & 2025 & 2032

Table 33. World AI Wafer Defect Inspection Market Size Value by Speed (2021-2026) & (USD Million)

Table 34. World AI Wafer Defect Inspection Market Size by Speed (2027-2032) & (USD Million)

Table 35. World AI Wafer Defect Inspection Market Size by Application, (USD Million), 2021 & 2025 & 2032

Table 36. World AI Wafer Defect Inspection Market Size by Application (2021-2026) & (USD Million)

Table 37. World AI Wafer Defect Inspection Market Size by Application (2027-2032) & (USD Million)

Table 38. KLA Corporation Basic Information, Manufacturing Base and Competitors

Table 39. KLA Corporation Major Business

Table 40. KLA Corporation AI Wafer Defect Inspection Product and Services

Table 41. KLA Corporation AI Wafer Defect Inspection Revenue, Gross Margin and Market Share (2021-2026) & (USD Million)

Table 42. KLA Corporation Recent Developments/Updates

Table 43. KLA Corporation Competitive Strengths & Weaknesses

Table 44. Cognex Basic Information, Manufacturing Base and Competitors
Table 45. Cognex Major Business
Table 46. Cognex AI Wafer Defect Inspection Product and Services
Table 47. Cognex AI Wafer Defect Inspection Revenue, Gross Margin and Market Share (2021-2026) & (USD Million)
Table 48. Cognex Recent Developments/Updates
Table 49. Cognex Competitive Strengths & Weaknesses
Table 50. Onto Innovation Basic Information, Manufacturing Base and Competitors
Table 51. Onto Innovation Major Business
Table 52. Onto Innovation AI Wafer Defect Inspection Product and Services
Table 53. Onto Innovation AI Wafer Defect Inspection Revenue, Gross Margin and Market Share (2021-2026) & (USD Million)
Table 54. Onto Innovation Recent Developments/Updates
Table 55. Onto Innovation Competitive Strengths & Weaknesses
Table 56. ASML Holding N.V. Basic Information, Manufacturing Base and Competitors
Table 57. ASML Holding N.V. Major Business
Table 58. ASML Holding N.V. AI Wafer Defect Inspection Product and Services
Table 59. ASML Holding N.V. AI Wafer Defect Inspection Revenue, Gross Margin and Market Share (2021-2026) & (USD Million)
Table 60. ASML Holding N.V. Recent Developments/Updates
Table 61. ASML Holding N.V. Competitive Strengths & Weaknesses
Table 62. Hitachi High-Tech Corporation Basic Information, Manufacturing Base and Competitors
Table 63. Hitachi High-Tech Corporation Major Business
Table 64. Hitachi High-Tech Corporation AI Wafer Defect Inspection Product and Services
Table 65. Hitachi High-Tech Corporation AI Wafer Defect Inspection Revenue, Gross Margin and Market Share (2021-2026) & (USD Million)
Table 66. Hitachi High-Tech Corporation Recent Developments/Updates
Table 67. Hitachi High-Tech Corporation Competitive Strengths & Weaknesses
Table 68. Tokyo Electron Limited Basic Information, Manufacturing Base and Competitors
Table 69. Tokyo Electron Limited Major Business
Table 70. Tokyo Electron Limited AI Wafer Defect Inspection Product and Services
Table 71. Tokyo Electron Limited AI Wafer Defect Inspection Revenue, Gross Margin and Market Share (2021-2026) & (USD Million)
Table 72. Tokyo Electron Limited Recent Developments/Updates
Table 73. Tokyo Electron Limited Competitive Strengths & Weaknesses
Table 74. SCREEN Semiconductor Solutions Co., Ltd. Basic Information, Manufacturing

Base and Competitors

Table 75. SCREEN Semiconductor Solutions Co., Ltd. Major Business

Table 76. SCREEN Semiconductor Solutions Co., Ltd. AI Wafer Defect Inspection Product and Services

Table 77. SCREEN Semiconductor Solutions Co., Ltd. AI Wafer Defect Inspection Revenue, Gross Margin and Market Share (2021-2026) & (USD Million)

Table 78. SCREEN Semiconductor Solutions Co., Ltd. Recent Developments/Updates

Table 79. SCREEN Semiconductor Solutions Co., Ltd. Competitive Strengths & Weaknesses

Table 80. Toshiba Corporation Basic Information, Manufacturing Base and Competitors

Table 81. Toshiba Corporation Major Business

Table 82. Toshiba Corporation AI Wafer Defect Inspection Product and Services

Table 83. Toshiba Corporation AI Wafer Defect Inspection Revenue, Gross Margin and Market Share (2021-2026) & (USD Million)

Table 84. Toshiba Corporation Recent Developments/Updates

Table 85. Toshiba Corporation Competitive Strengths & Weaknesses

Table 86. Camtek Ltd. Basic Information, Manufacturing Base and Competitors

Table 87. Camtek Ltd. Major Business

Table 88. Camtek Ltd. AI Wafer Defect Inspection Product and Services

Table 89. Camtek Ltd. AI Wafer Defect Inspection Revenue, Gross Margin and Market Share (2021-2026) & (USD Million)

Table 90. Camtek Ltd. Recent Developments/Updates

Table 91. Camtek Ltd. Competitive Strengths & Weaknesses

Table 92. Shanghai Jingce Semiconductor Technology Basic Information, Manufacturing Base and Competitors

Table 93. Shanghai Jingce Semiconductor Technology Major Business

Table 94. Shanghai Jingce Semiconductor Technology AI Wafer Defect Inspection Product and Services

Table 95. Shanghai Jingce Semiconductor Technology AI Wafer Defect Inspection Revenue, Gross Margin and Market Share (2021-2026) & (USD Million)

Table 96. Shanghai Jingce Semiconductor Technology Recent Developments/Updates

Table 97. Shanghai Jingce Semiconductor Technology Competitive Strengths & Weaknesses

Table 98. Okmetic Basic Information, Manufacturing Base and Competitors

Table 99. Okmetic Major Business

Table 100. Okmetic AI Wafer Defect Inspection Product and Services

Table 101. Okmetic AI Wafer Defect Inspection Revenue, Gross Margin and Market Share (2021-2026) & (USD Million)

Table 102. Okmetic Recent Developments/Updates

Table 103. Okmetic Competitive Strengths & Weaknesses
Table 104. Confovis Basic Information, Manufacturing Base and Competitors
Table 105. Confovis Major Business
Table 106. Confovis AI Wafer Defect Inspection Product and Services
Table 107. Confovis AI Wafer Defect Inspection Revenue, Gross Margin and Market Share (2021-2026) & (USD Million)
Table 108. Confovis Recent Developments/Updates
Table 109. Confovis Competitive Strengths & Weaknesses
Table 110. Averian Basic Information, Manufacturing Base and Competitors
Table 111. Averian Major Business
Table 112. Averian AI Wafer Defect Inspection Product and Services
Table 113. Averian AI Wafer Defect Inspection Revenue, Gross Margin and Market Share (2021-2026) & (USD Million)
Table 114. Averian Recent Developments/Updates
Table 115. Averian Competitive Strengths & Weaknesses
Table 116. Global Key Players of AI Wafer Defect Inspection Upstream (Raw Materials)
Table 117. Global AI Wafer Defect Inspection Typical Customers

List Of Figures

LIST OF FIGURES

Figure 1. AI Wafer Defect Inspection Picture

Figure 2. World AI Wafer Defect Inspection Total Revenue: 2021 & 2025 & 2032, (USD Million)

Figure 3. World AI Wafer Defect Inspection Total Revenue (2021-2032) & (USD Million)

Figure 4. World AI Wafer Defect Inspection Revenue by Region (2021, 2025 and 2032) & (USD Million), (by Headquarter Location)

Figure 5. World AI Wafer Defect Inspection Revenue Market Share by Region (2021-2032), (by Headquarter Location)

Figure 6. United States Based Company AI Wafer Defect Inspection Revenue (2021-2032) & (USD Million)

Figure 7. China Based Company AI Wafer Defect Inspection Revenue (2021-2032) & (USD Million)

Figure 8. Europe Based Company AI Wafer Defect Inspection Revenue (2021-2032) & (USD Million)

Figure 9. Japan Based Company AI Wafer Defect Inspection Revenue (2021-2032) & (USD Million)

Figure 10. South Korea Based Company AI Wafer Defect Inspection Revenue (2021-2032) & (USD Million)

Figure 11. ASEAN Based Company AI Wafer Defect Inspection Revenue (2021-2032) & (USD Million)

Figure 12. India Based Company AI Wafer Defect Inspection Revenue (2021-2032) & (USD Million)

Figure 13. AI Wafer Defect Inspection Market Drivers

Figure 14. Factors Affecting Demand

Figure 15. World AI Wafer Defect Inspection Consumption Value (2021-2032) & (USD Million)

Figure 16. World AI Wafer Defect Inspection Consumption Value Market Share by Region (2021-2032)

Figure 17. United States AI Wafer Defect Inspection Consumption Value (2021-2032) & (USD Million)

Figure 18. China AI Wafer Defect Inspection Consumption Value (2021-2032) & (USD Million)

Figure 19. Europe AI Wafer Defect Inspection Consumption Value (2021-2032) & (USD Million)

Figure 20. Japan AI Wafer Defect Inspection Consumption Value (2021-2032) & (USD Million)

Million)

Figure 21. South Korea AI Wafer Defect Inspection Consumption Value (2021-2032) & (USD Million)

Figure 22. ASEAN AI Wafer Defect Inspection Consumption Value (2021-2032) & (USD Million)

Figure 23. India AI Wafer Defect Inspection Consumption Value (2021-2032) & (USD Million)

Figure 24. Producer Shipments of AI Wafer Defect Inspection by Player Revenue (\$MM) and Market Share (%): 2025

Figure 25. Global Four-firm Concentration Ratios (CR4) for AI Wafer Defect Inspection Markets in 2025

Figure 26. Global Four-firm Concentration Ratios (CR8) for AI Wafer Defect Inspection Markets in 2025

Figure 27. United States VS China: AI Wafer Defect Inspection Revenue Market Share Comparison (2021 & 2025 & 2032)

Figure 28. United States VS China: AI Wafer Defect Inspection Consumption Value Market Share Comparison (2021 & 2025 & 2032)

Figure 29. World AI Wafer Defect Inspection Market Size by Type, (USD Million), 2021 & 2025 & 2032

Figure 30. World AI Wafer Defect Inspection Market Size Market Share by Type in 2025

Figure 31. Nanometer Scale Defect Inspection (<10 nm)

Figure 32. Sub-Micron Defect Inspection (10 nm-1 ?m)

Figure 33. Micron Scale Defect Inspection (>1 ?m)

Figure 34. World AI Wafer Defect Inspection Market Size Market Share by Type (2021-2032)

Figure 35. World AI Wafer Defect Inspection Market Size by Speed, (USD Million), 2021 & 2025 & 2032

Figure 36. World AI Wafer Defect Inspection Market Size Market Share by Speed in 2025

Figure 37. High Throughput Wafer Inspection (>100 wafers/hour)

Figure 38. Medium Throughput Wafer Inspection (20-100 wafers/hour)

Figure 39. Low Throughput Wafer Inspection (<20 wafers/hour)

Figure 40. World AI Wafer Defect Inspection Market Size Market Share by Speed (2021-2032)

Figure 41. World AI Wafer Defect Inspection Market Size by Application, (USD Million), 2021 & 2025 & 2032

Figure 42. World AI Wafer Defect Inspection Market Size Market Share by Application in 2025

Figure 43. Semiconductor Manufacturing

Figure 44. Integrated Circuit Fabrication

Figure 45. Memory Chip Production

Figure 46. Logic Chip Production

Figure 47. Others

Figure 48. World AI Wafer Defect Inspection Market Size Market Share by Application (2021-2032)

Figure 49. AI Wafer Defect Inspection Industrial Chain

Figure 50. Methodology

Figure 51. Research Process and Data Source

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