

# Global AI-Driven Chip Design Verification Platforms Market 2026 by Company, Regions, Type and Application, Forecast to 2032

<https://marketpublishers.com/r/G864CAABC5F8EN.html>

Date: July 2026

Pages: 117

Price: US\$ 3,480.00 (Single User License)

ID: G864CAABC5F8EN

## Abstracts

According to our (Global Info Research) latest study, the global AI-Driven Chip Design Verification Platforms market size was valued at US\$ 236 million in 2025 and is forecast to a readjusted size of US\$ 886 million by 2032 with a CAGR of 19.1% during review period.

An AI-driven chip design verification platform is a software platform or engineering service that integrates generative AI, machine learning, formal verification, simulation acceleration, and cloud-based regression management. Its purpose is to automate the generation of testbenches and assertions, pinpoint design defects, compress verification cycles, and enhance the efficiency of coverage convergence. This platform primarily targets design workflows for complex SoCs, AI accelerators, automotive-grade chips, and high-end ASICs. The overall market trend is driven collectively by advancements in process nodes, Chiplet technology, multi-IP integration, and a shortage of engineering talent; the period between 2025 and 2026 marks a transitional phase, shifting from the use of standalone auxiliary tools toward the adoption of process-level intelligent agents. Industry gross margin levels typically range from 70%?85% for software subscriptions and 35%?55% for engineering services, resulting in a blended gross margin of approximately 55%?75%.

This report is a detailed and comprehensive analysis for global AI-Driven Chip Design Verification Platforms market. Both quantitative and qualitative analyses are presented by company, by region & country, by Type and by Application. As the market is constantly changing, this report explores the competition, supply and demand trends, as well as key factors that contribute to its changing demands across many markets. Company profiles and product examples of selected competitors, along with market

share estimates of some of the selected leaders for the year 2025, are provided.

#### Key Features:

Global AI-Driven Chip Design Verification Platforms market size and forecasts, in consumption value (\$ Million), 2021-2032

Global AI-Driven Chip Design Verification Platforms market size and forecasts by region and country, in consumption value (\$ Million), 2021-2032

Global AI-Driven Chip Design Verification Platforms market size and forecasts, by Type and by Application, in consumption value (\$ Million), 2021-2032

Global AI-Driven Chip Design Verification Platforms market shares of main players, in revenue (\$ Million), 2021-2026

#### The Primary Objectives in This Report Are:

To determine the size of the total market opportunity of global and key countries

To assess the growth potential for AI-Driven Chip Design Verification Platforms

To forecast future growth in each product and end-use market

To assess competitive factors affecting the marketplace

This report profiles key players in the global AI-Driven Chip Design Verification Platforms market based on the following parameters - company overview, revenue, gross margin, product portfolio, geographical presence, and key developments. Key companies covered as a part of this study include Synopsys<sup>US?</sup>, Cadence Design Systems<sup>US?</sup>, Siemens EDA<sup>DE?</sup>, Keysight Technologies<sup>US?</sup>, Silvaco<sup>US?</sup>, Aldec<sup>US?</sup>, Breker Verification Systems<sup>US?</sup>, Cyprium<sup>US?</sup>, Mirabilis Design<sup>US?</sup>, Emptrean Technology<sup>CN?</sup>, etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals.

## Market segmentation

AI-Driven Chip Design Verification Platforms market is split by Type and by Application. For the period 2021-2032, the growth among segments provides accurate calculations and forecasts for Consumption Value by Type and by Application. This analysis can help you expand your business by targeting qualified niche markets.

### Market segment by Type

RTL Verification AI Assistant

Formal Verification Agent

Emulation and Prototyping Automation

Mixed-Signal Verification AI

Signoff Verification Copilot

### Market segment by Technology

Generative HDL and Assertion Technology

ML-Based Bug Localization

LLM EDA Workflow Orchestration

Cloud Verification Farm

Knowledge Graph Constraint Management

### Market segment by Functional Category

Testbench Generation

Coverage Closure

Assertion Generation

Debug Root Cause Analysis

Regression Prioritization

## Market segment by Application

AI Accelerators

Automotive SoC

Data Center ASIC

Communication Chips

RISC-V and IP Design

Others

## Market segment by players, this report covers

Synopsys?US?

Cadence Design Systems?US?

Siemens EDA?DE?

Keysight Technologies?US?

Silvaco?US?

Aldec?US?

Breker Verification Systems?US?

Cycuity?US?

Mirabilis Design?US?

Empyrean Technology?CN?

Primarius Technologies?CN?

Xpeedic Technology?CN?

Zuken?JP?

Amiq EDA?RO?

Market segment by regions, regional analysis covers

North America (United States, Canada and Mexico)

Europe (Germany, France, UK, Russia, Italy and Rest of Europe)

Asia-Pacific (China, Japan, South Korea, India, Southeast Asia and Rest of Asia-Pacific)

South America (Brazil, Rest of South America)

Middle East & Africa (Turkey, Saudi Arabia, UAE, Rest of Middle East & Africa)

The content of the study subjects, includes a total of 13 chapters:

Chapter 1, to describe AI-Driven Chip Design Verification Platforms product scope, market overview, market estimation caveats and base year.

Chapter 2, to profile the top players of AI-Driven Chip Design Verification Platforms, with revenue, gross margin, and global market share of AI-Driven Chip Design Verification Platforms from 2021 to 2026.

Chapter 3, the AI-Driven Chip Design Verification Platforms competitive situation, revenue, and global market share of top players are analyzed emphatically by landscape contrast.

Chapter 4 and 5, to segment the market size by Type and by Application, with consumption value and growth rate by Type, by Application, from 2021 to 2032.

Chapter 6, 7, 8, 9, and 10, to break the market size data at the country level, with revenue and market share for key countries in the world, from 2021 to 2026. and AI-Driven Chip Design Verification Platforms market forecast, by regions, by Type and by Application, with consumption value, from 2027 to 2032.

Chapter 11, market dynamics, drivers, restraints, trends, Porters Five Forces analysis.

Chapter 12, the key raw materials and key suppliers, and industry chain of AI-Driven Chip Design Verification Platforms.

Chapter 13, to describe AI-Driven Chip Design Verification Platforms research findings and conclusion.

## Contents

### 1 MARKET OVERVIEW

#### 1.1 Product Overview and Scope

#### 1.2 Market Estimation Caveats and Base Year

#### 1.3 Classification of AI-Driven Chip Design Verification Platforms by Type

##### 1.3.1 Overview: Global AI-Driven Chip Design Verification Platforms Market Size by Type: 2021 Versus 2025 Versus 2032

##### 1.3.2 Global AI-Driven Chip Design Verification Platforms Consumption Value Market Share by Type in 2025

##### 1.3.3 RTL Verification AI Assistant

##### 1.3.4 Formal Verification Agent

##### 1.3.5 Emulation and Prototyping Automation

##### 1.3.6 Mixed-Signal Verification AI

##### 1.3.7 Signoff Verification Copilot

#### 1.4 Classification of AI-Driven Chip Design Verification Platforms by Technology

##### 1.4.1 Overview: Global AI-Driven Chip Design Verification Platforms Market Size by Technology: 2021 Versus 2025 Versus 2032

##### 1.4.2 Global AI-Driven Chip Design Verification Platforms Consumption Value Market Share by Technology in 2025

##### 1.4.3 Generative HDL and Assertion Technology

##### 1.4.4 ML-Based Bug Localization

##### 1.4.5 LLM EDA Workflow Orchestration

##### 1.4.6 Cloud Verification Farm

##### 1.4.7 Knowledge Graph Constraint Management

#### 1.5 Classification of AI-Driven Chip Design Verification Platforms by Functional Category

##### 1.5.1 Overview: Global AI-Driven Chip Design Verification Platforms Market Size by Functional Category: 2021 Versus 2025 Versus 2032

##### 1.5.2 Global AI-Driven Chip Design Verification Platforms Consumption Value Market Share by Functional Category in 2025

##### 1.5.3 Testbench Generation

##### 1.5.4 Coverage Closure

##### 1.5.5 Assertion Generation

##### 1.5.6 Debug Root Cause Analysis

##### 1.5.7 Regression Prioritization

#### 1.6 Global AI-Driven Chip Design Verification Platforms Market by Application

##### 1.6.1 Overview: Global AI-Driven Chip Design Verification Platforms Market Size by

Application: 2021 Versus 2025 Versus 2032

1.6.2 AI Accelerators

1.6.3 Automotive SoC

1.6.4 Data Center ASIC

1.6.5 Communication Chips

1.6.6 RISC-V and IP Design

1.6.7 Others

1.7 Global AI-Driven Chip Design Verification Platforms Market Size & Forecast

1.8 Global AI-Driven Chip Design Verification Platforms Market Size and Forecast by Region

1.8.1 Global AI-Driven Chip Design Verification Platforms Market Size by Region: 2021 VS 2025 VS 2032

1.8.2 Global AI-Driven Chip Design Verification Platforms Market Size by Region, (2021-2032)

1.8.3 North America AI-Driven Chip Design Verification Platforms Market Size and Prospect (2021-2032)

1.8.4 Europe AI-Driven Chip Design Verification Platforms Market Size and Prospect (2021-2032)

1.8.5 Asia-Pacific AI-Driven Chip Design Verification Platforms Market Size and Prospect (2021-2032)

1.8.6 South America AI-Driven Chip Design Verification Platforms Market Size and Prospect (2021-2032)

1.8.7 Middle East & Africa AI-Driven Chip Design Verification Platforms Market Size and Prospect (2021-2032)

## **2 COMPANY PROFILES**

2.1 Synopsys?US?

2.1.1 Synopsys?US? Details

2.1.2 Synopsys?US? Major Business

2.1.3 Synopsys?US? AI-Driven Chip Design Verification Platforms Product and Solutions

2.1.4 Synopsys?US? AI-Driven Chip Design Verification Platforms Revenue, Gross Margin and Market Share (2021-2026)

2.1.5 Synopsys?US? Recent Developments and Future Plans

2.2 Cadence Design Systems?US?

2.2.1 Cadence Design Systems?US? Details

2.2.2 Cadence Design Systems?US? Major Business

2.2.3 Cadence Design Systems?US? AI-Driven Chip Design Verification Platforms

## Product and Solutions

2.2.4 Cadence Design Systems?US? AI-Driven Chip Design Verification Platforms Revenue, Gross Margin and Market Share (2021-2026)

2.2.5 Cadence Design Systems?US? Recent Developments and Future Plans

## 2.3 Siemens EDA?DE?

2.3.1 Siemens EDA?DE? Details

2.3.2 Siemens EDA?DE? Major Business

2.3.3 Siemens EDA?DE? AI-Driven Chip Design Verification Platforms Product and Solutions

2.3.4 Siemens EDA?DE? AI-Driven Chip Design Verification Platforms Revenue, Gross Margin and Market Share (2021-2026)

2.3.5 Siemens EDA?DE? Recent Developments and Future Plans

## 2.4 Keysight Technologies?US?

2.4.1 Keysight Technologies?US? Details

2.4.2 Keysight Technologies?US? Major Business

2.4.3 Keysight Technologies?US? AI-Driven Chip Design Verification Platforms Product and Solutions

2.4.4 Keysight Technologies?US? AI-Driven Chip Design Verification Platforms Revenue, Gross Margin and Market Share (2021-2026)

2.4.5 Keysight Technologies?US? Recent Developments and Future Plans

## 2.5 Silvaco?US?

2.5.1 Silvaco?US? Details

2.5.2 Silvaco?US? Major Business

2.5.3 Silvaco?US? AI-Driven Chip Design Verification Platforms Product and Solutions

2.5.4 Silvaco?US? AI-Driven Chip Design Verification Platforms Revenue, Gross Margin and Market Share (2021-2026)

2.5.5 Silvaco?US? Recent Developments and Future Plans

## 2.6 Aldec?US?

2.6.1 Aldec?US? Details

2.6.2 Aldec?US? Major Business

2.6.3 Aldec?US? AI-Driven Chip Design Verification Platforms Product and Solutions

2.6.4 Aldec?US? AI-Driven Chip Design Verification Platforms Revenue, Gross Margin and Market Share (2021-2026)

2.6.5 Aldec?US? Recent Developments and Future Plans

## 2.7 Breker Verification Systems?US?

2.7.1 Breker Verification Systems?US? Details

2.7.2 Breker Verification Systems?US? Major Business

2.7.3 Breker Verification Systems?US? AI-Driven Chip Design Verification Platforms Product and Solutions

2.7.4 Breker Verification Systems?US? AI-Driven Chip Design Verification Platforms Revenue, Gross Margin and Market Share (2021-2026)

2.7.5 Breker Verification Systems?US? Recent Developments and Future Plans  
2.8 Cycuity?US?

2.8.1 Cycuity?US? Details  
2.8.2 Cycuity?US? Major Business  
2.8.3 Cycuity?US? AI-Driven Chip Design Verification Platforms Product and Solutions  
2.8.4 Cycuity?US? AI-Driven Chip Design Verification Platforms Revenue, Gross Margin and Market Share (2021-2026)

2.8.5 Cycuity?US? Recent Developments and Future Plans  
2.9 Mirabilis Design?US?

2.9.1 Mirabilis Design?US? Details  
2.9.2 Mirabilis Design?US? Major Business  
2.9.3 Mirabilis Design?US? AI-Driven Chip Design Verification Platforms Product and Solutions

2.9.4 Mirabilis Design?US? AI-Driven Chip Design Verification Platforms Revenue, Gross Margin and Market Share (2021-2026)

2.9.5 Mirabilis Design?US? Recent Developments and Future Plans

2.10 Empyrean Technology?CN?

2.10.1 Empyrean Technology?CN? Details  
2.10.2 Empyrean Technology?CN? Major Business  
2.10.3 Empyrean Technology?CN? AI-Driven Chip Design Verification Platforms Product and Solutions

2.10.4 Empyrean Technology?CN? AI-Driven Chip Design Verification Platforms Revenue, Gross Margin and Market Share (2021-2026)

2.10.5 Empyrean Technology?CN? Recent Developments and Future Plans

2.11 Primarius Technologies?CN?

2.11.1 Primarius Technologies?CN? Details  
2.11.2 Primarius Technologies?CN? Major Business  
2.11.3 Primarius Technologies?CN? AI-Driven Chip Design Verification Platforms Product and Solutions

2.11.4 Primarius Technologies?CN? AI-Driven Chip Design Verification Platforms Revenue, Gross Margin and Market Share (2021-2026)

2.11.5 Primarius Technologies?CN? Recent Developments and Future Plans

2.12 Xpeedic Technology?CN?

2.12.1 Xpeedic Technology?CN? Details  
2.12.2 Xpeedic Technology?CN? Major Business  
2.12.3 Xpeedic Technology?CN? AI-Driven Chip Design Verification Platforms Product and Solutions

2.12.4 Xpeedic Technology?CN? AI-Driven Chip Design Verification Platforms Revenue, Gross Margin and Market Share (2021-2026)

2.12.5 Xpeedic Technology?CN? Recent Developments and Future Plans

2.13 Zuken?JP?

2.13.1 Zuken?JP? Details

2.13.2 Zuken?JP? Major Business

2.13.3 Zuken?JP? AI-Driven Chip Design Verification Platforms Product and Solutions

2.13.4 Zuken?JP? AI-Driven Chip Design Verification Platforms Revenue, Gross Margin and Market Share (2021-2026)

2.13.5 Zuken?JP? Recent Developments and Future Plans

2.14 Amiq EDA?RO?

2.14.1 Amiq EDA?RO? Details

2.14.2 Amiq EDA?RO? Major Business

2.14.3 Amiq EDA?RO? AI-Driven Chip Design Verification Platforms Product and Solutions

2.14.4 Amiq EDA?RO? AI-Driven Chip Design Verification Platforms Revenue, Gross Margin and Market Share (2021-2026)

2.14.5 Amiq EDA?RO? Recent Developments and Future Plans

### **3 MARKET COMPETITION, BY PLAYERS**

3.1 Global AI-Driven Chip Design Verification Platforms Revenue and Share by Players (2021-2026)

3.2 Market Share Analysis (2025)

3.2.1 Market Share of AI-Driven Chip Design Verification Platforms by Company Revenue

3.2.2 Top 3 AI-Driven Chip Design Verification Platforms Players Market Share in 2025

3.2.3 Top 6 AI-Driven Chip Design Verification Platforms Players Market Share in 2025

3.3 AI-Driven Chip Design Verification Platforms Market: Overall Company Footprint Analysis

3.3.1 AI-Driven Chip Design Verification Platforms Market: Region Footprint

3.3.2 AI-Driven Chip Design Verification Platforms Market: Company Product Type Footprint

3.3.3 AI-Driven Chip Design Verification Platforms Market: Company Product Application Footprint

3.4 New Market Entrants and Barriers to Market Entry

3.5 Mergers, Acquisition, Agreements, and Collaborations

### **4 MARKET SIZE SEGMENT BY TYPE**

4.1 Global AI-Driven Chip Design Verification Platforms Consumption Value and Market Share by Type (2021-2026)

4.2 Global AI-Driven Chip Design Verification Platforms Market Forecast by Type (2027-2032)

## **5 MARKET SIZE SEGMENT BY APPLICATION**

5.1 Global AI-Driven Chip Design Verification Platforms Consumption Value Market Share by Application (2021-2026)

5.2 Global AI-Driven Chip Design Verification Platforms Market Forecast by Application (2027-2032)

## **6 NORTH AMERICA**

6.1 North America AI-Driven Chip Design Verification Platforms Consumption Value by Type (2021-2032)

6.2 North America AI-Driven Chip Design Verification Platforms Market Size by Application (2021-2032)

6.3 North America AI-Driven Chip Design Verification Platforms Market Size by Country

6.3.1 North America AI-Driven Chip Design Verification Platforms Consumption Value by Country (2021-2032)

6.3.2 United States AI-Driven Chip Design Verification Platforms Market Size and Forecast (2021-2032)

6.3.3 Canada AI-Driven Chip Design Verification Platforms Market Size and Forecast (2021-2032)

6.3.4 Mexico AI-Driven Chip Design Verification Platforms Market Size and Forecast (2021-2032)

## **7 EUROPE**

7.1 Europe AI-Driven Chip Design Verification Platforms Consumption Value by Type (2021-2032)

7.2 Europe AI-Driven Chip Design Verification Platforms Consumption Value by Application (2021-2032)

7.3 Europe AI-Driven Chip Design Verification Platforms Market Size by Country

7.3.1 Europe AI-Driven Chip Design Verification Platforms Consumption Value by Country (2021-2032)

7.3.2 Germany AI-Driven Chip Design Verification Platforms Market Size and Forecast

(2021-2032)

7.3.3 France AI-Driven Chip Design Verification Platforms Market Size and Forecast (2021-2032)

7.3.4 United Kingdom AI-Driven Chip Design Verification Platforms Market Size and Forecast (2021-2032)

7.3.5 Russia AI-Driven Chip Design Verification Platforms Market Size and Forecast (2021-2032)

7.3.6 Italy AI-Driven Chip Design Verification Platforms Market Size and Forecast (2021-2032)

## **8 ASIA-PACIFIC**

8.1 Asia-Pacific AI-Driven Chip Design Verification Platforms Consumption Value by Type (2021-2032)

8.2 Asia-Pacific AI-Driven Chip Design Verification Platforms Consumption Value by Application (2021-2032)

8.3 Asia-Pacific AI-Driven Chip Design Verification Platforms Market Size by Region

8.3.1 Asia-Pacific AI-Driven Chip Design Verification Platforms Consumption Value by Region (2021-2032)

8.3.2 China AI-Driven Chip Design Verification Platforms Market Size and Forecast (2021-2032)

8.3.3 Japan AI-Driven Chip Design Verification Platforms Market Size and Forecast (2021-2032)

8.3.4 South Korea AI-Driven Chip Design Verification Platforms Market Size and Forecast (2021-2032)

8.3.5 India AI-Driven Chip Design Verification Platforms Market Size and Forecast (2021-2032)

8.3.6 Southeast Asia AI-Driven Chip Design Verification Platforms Market Size and Forecast (2021-2032)

8.3.7 Australia AI-Driven Chip Design Verification Platforms Market Size and Forecast (2021-2032)

## **9 SOUTH AMERICA**

9.1 South America AI-Driven Chip Design Verification Platforms Consumption Value by Type (2021-2032)

9.2 South America AI-Driven Chip Design Verification Platforms Consumption Value by Application (2021-2032)

9.3 South America AI-Driven Chip Design Verification Platforms Market Size by Country

9.3.1 South America AI-Driven Chip Design Verification Platforms Consumption Value by Country (2021-2032)

9.3.2 Brazil AI-Driven Chip Design Verification Platforms Market Size and Forecast (2021-2032)

9.3.3 Argentina AI-Driven Chip Design Verification Platforms Market Size and Forecast (2021-2032)

## **10 MIDDLE EAST & AFRICA**

10.1 Middle East & Africa AI-Driven Chip Design Verification Platforms Consumption Value by Type (2021-2032)

10.2 Middle East & Africa AI-Driven Chip Design Verification Platforms Consumption Value by Application (2021-2032)

10.3 Middle East & Africa AI-Driven Chip Design Verification Platforms Market Size by Country

10.3.1 Middle East & Africa AI-Driven Chip Design Verification Platforms Consumption Value by Country (2021-2032)

10.3.2 Turkey AI-Driven Chip Design Verification Platforms Market Size and Forecast (2021-2032)

10.3.3 Saudi Arabia AI-Driven Chip Design Verification Platforms Market Size and Forecast (2021-2032)

10.3.4 UAE AI-Driven Chip Design Verification Platforms Market Size and Forecast (2021-2032)

## **11 MARKET DYNAMICS**

11.1 AI-Driven Chip Design Verification Platforms Market Drivers

11.2 AI-Driven Chip Design Verification Platforms Market Restraints

11.3 AI-Driven Chip Design Verification Platforms Trends Analysis

11.4 Porters Five Forces Analysis

11.4.1 Threat of New Entrants

11.4.2 Bargaining Power of Suppliers

11.4.3 Bargaining Power of Buyers

11.4.4 Threat of Substitutes

11.4.5 Competitive Rivalry

## **12 INDUSTRY CHAIN ANALYSIS**

12.1 AI-Driven Chip Design Verification Platforms Industry Chain

12.2 AI-Driven Chip Design Verification Platforms Upstream Analysis

12.3 AI-Driven Chip Design Verification Platforms Midstream Analysis

12.4 AI-Driven Chip Design Verification Platforms Downstream Analysis

## **13 RESEARCH FINDINGS AND CONCLUSION**

## **14 APPENDIX**

14.1 Methodology

14.2 Research Process and Data Source

14.3 Disclaimer

## List Of Tables

### LIST OF TABLES

Table 1. Global AI-Driven Chip Design Verification Platforms Consumption Value by Type, (USD Million), 2021 & 2025 & 2032

Table 2. Global AI-Driven Chip Design Verification Platforms Consumption Value by Technology, (USD Million), 2021 & 2025 & 2032

Table 3. Global AI-Driven Chip Design Verification Platforms Consumption Value by Functional Category, (USD Million), 2021 & 2025 & 2032

Table 4. Global AI-Driven Chip Design Verification Platforms Consumption Value by Application, (USD Million), 2021 & 2025 & 2032

Table 5. Global AI-Driven Chip Design Verification Platforms Consumption Value by Region (2021-2026) & (USD Million)

Table 6. Global AI-Driven Chip Design Verification Platforms Consumption Value by Region (2027-2032) & (USD Million)

Table 7. Synopsys?US? Company Information, Head Office, and Major Competitors

Table 8. Synopsys?US? Major Business

Table 9. Synopsys?US? AI-Driven Chip Design Verification Platforms Product and Solutions

Table 10. Synopsys?US? AI-Driven Chip Design Verification Platforms Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 11. Synopsys?US? Recent Developments and Future Plans

Table 12. Cadence Design Systems?US? Company Information, Head Office, and Major Competitors

Table 13. Cadence Design Systems?US? Major Business

Table 14. Cadence Design Systems?US? AI-Driven Chip Design Verification Platforms Product and Solutions

Table 15. Cadence Design Systems?US? AI-Driven Chip Design Verification Platforms Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 16. Cadence Design Systems?US? Recent Developments and Future Plans

Table 17. Siemens EDA?DE? Company Information, Head Office, and Major Competitors

Table 18. Siemens EDA?DE? Major Business

Table 19. Siemens EDA?DE? AI-Driven Chip Design Verification Platforms Product and Solutions

Table 20. Siemens EDA?DE? AI-Driven Chip Design Verification Platforms Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 21. Keysight Technologies?US? Company Information, Head Office, and Major

## Competitors

Table 22. Keysight Technologies?US? Major Business

Table 23. Keysight Technologies?US? AI-Driven Chip Design Verification Platforms Product and Solutions

Table 24. Keysight Technologies?US? AI-Driven Chip Design Verification Platforms Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 25. Keysight Technologies?US? Recent Developments and Future Plans

Table 26. Silvaco?US? Company Information, Head Office, and Major Competitors

Table 27. Silvaco?US? Major Business

Table 28. Silvaco?US? AI-Driven Chip Design Verification Platforms Product and Solutions

Table 29. Silvaco?US? AI-Driven Chip Design Verification Platforms Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 30. Silvaco?US? Recent Developments and Future Plans

Table 31. Aldec?US? Company Information, Head Office, and Major Competitors

Table 32. Aldec?US? Major Business

Table 33. Aldec?US? AI-Driven Chip Design Verification Platforms Product and Solutions

Table 34. Aldec?US? AI-Driven Chip Design Verification Platforms Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 35. Aldec?US? Recent Developments and Future Plans

Table 36. Breker Verification Systems?US? Company Information, Head Office, and Major Competitors

Table 37. Breker Verification Systems?US? Major Business

Table 38. Breker Verification Systems?US? AI-Driven Chip Design Verification Platforms Product and Solutions

Table 39. Breker Verification Systems?US? AI-Driven Chip Design Verification Platforms Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 40. Breker Verification Systems?US? Recent Developments and Future Plans

Table 41. Cycuity?US? Company Information, Head Office, and Major Competitors

Table 42. Cycuity?US? Major Business

Table 43. Cycuity?US? AI-Driven Chip Design Verification Platforms Product and Solutions

Table 44. Cycuity?US? AI-Driven Chip Design Verification Platforms Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 45. Cycuity?US? Recent Developments and Future Plans

Table 46. Mirabilis Design?US? Company Information, Head Office, and Major Competitors

Table 47. Mirabilis Design?US? Major Business

Table 48. Mirabilis Design?US? AI-Driven Chip Design Verification Platforms Product and Solutions

Table 49. Mirabilis Design?US? AI-Driven Chip Design Verification Platforms Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 50. Mirabilis Design?US? Recent Developments and Future Plans

Table 51. Empyrean Technology?CN? Company Information, Head Office, and Major Competitors

Table 52. Empyrean Technology?CN? Major Business

Table 53. Empyrean Technology?CN? AI-Driven Chip Design Verification Platforms Product and Solutions

Table 54. Empyrean Technology?CN? AI-Driven Chip Design Verification Platforms Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 55. Empyrean Technology?CN? Recent Developments and Future Plans

Table 56. Primarius Technologies?CN? Company Information, Head Office, and Major Competitors

Table 57. Primarius Technologies?CN? Major Business

Table 58. Primarius Technologies?CN? AI-Driven Chip Design Verification Platforms Product and Solutions

Table 59. Primarius Technologies?CN? AI-Driven Chip Design Verification Platforms Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 60. Primarius Technologies?CN? Recent Developments and Future Plans

Table 61. Xpeedic Technology?CN? Company Information, Head Office, and Major Competitors

Table 62. Xpeedic Technology?CN? Major Business

Table 63. Xpeedic Technology?CN? AI-Driven Chip Design Verification Platforms Product and Solutions

Table 64. Xpeedic Technology?CN? AI-Driven Chip Design Verification Platforms Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 65. Xpeedic Technology?CN? Recent Developments and Future Plans

Table 66. Zuken?JP? Company Information, Head Office, and Major Competitors

Table 67. Zuken?JP? Major Business

Table 68. Zuken?JP? AI-Driven Chip Design Verification Platforms Product and Solutions

Table 69. Zuken?JP? AI-Driven Chip Design Verification Platforms Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 70. Zuken?JP? Recent Developments and Future Plans

Table 71. Amiq EDA?RO? Company Information, Head Office, and Major Competitors

Table 72. Amiq EDA?RO? Major Business

Table 73. Amiq EDA?RO? AI-Driven Chip Design Verification Platforms Product and

## Solutions

Table 74. Amiq EDA?RO? AI-Driven Chip Design Verification Platforms Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 75. Amiq EDA?RO? Recent Developments and Future Plans

Table 76. Global AI-Driven Chip Design Verification Platforms Revenue (USD Million) by Players (2021-2026)

Table 77. Global AI-Driven Chip Design Verification Platforms Revenue Share by Players (2021-2026)

Table 78. Breakdown of AI-Driven Chip Design Verification Platforms by Company Type (Tier 1, Tier 2, and Tier 3)

Table 79. Market Position of Players in AI-Driven Chip Design Verification Platforms, (Tier 1, Tier 2, and Tier 3), Based on Revenue in 2025

Table 80. Head Office of Key AI-Driven Chip Design Verification Platforms Players

Table 81. AI-Driven Chip Design Verification Platforms Market: Company Product Type Footprint

Table 82. AI-Driven Chip Design Verification Platforms Market: Company Product Application Footprint

Table 83. AI-Driven Chip Design Verification Platforms New Market Entrants and Barriers to Market Entry

Table 84. AI-Driven Chip Design Verification Platforms Mergers, Acquisition, Agreements, and Collaborations

Table 85. Global AI-Driven Chip Design Verification Platforms Consumption Value (USD Million) by Type (2021-2026)

Table 86. Global AI-Driven Chip Design Verification Platforms Consumption Value Share by Type (2021-2026)

Table 87. Global AI-Driven Chip Design Verification Platforms Consumption Value Forecast by Type (2027-2032)

Table 88. Global AI-Driven Chip Design Verification Platforms Consumption Value by Application (2021-2026)

Table 89. Global AI-Driven Chip Design Verification Platforms Consumption Value Forecast by Application (2027-2032)

Table 90. North America AI-Driven Chip Design Verification Platforms Consumption Value by Type (2021-2026) & (USD Million)

Table 91. North America AI-Driven Chip Design Verification Platforms Consumption Value by Type (2027-2032) & (USD Million)

Table 92. North America AI-Driven Chip Design Verification Platforms Consumption Value by Application (2021-2026) & (USD Million)

Table 93. North America AI-Driven Chip Design Verification Platforms Consumption Value by Application (2027-2032) & (USD Million)

Table 94. North America AI-Driven Chip Design Verification Platforms Consumption Value by Country (2021-2026) & (USD Million)

Table 95. North America AI-Driven Chip Design Verification Platforms Consumption Value by Country (2027-2032) & (USD Million)

Table 96. Europe AI-Driven Chip Design Verification Platforms Consumption Value by Type (2021-2026) & (USD Million)

Table 97. Europe AI-Driven Chip Design Verification Platforms Consumption Value by Type (2027-2032) & (USD Million)

Table 98. Europe AI-Driven Chip Design Verification Platforms Consumption Value by Application (2021-2026) & (USD Million)

Table 99. Europe AI-Driven Chip Design Verification Platforms Consumption Value by Application (2027-2032) & (USD Million)

Table 100. Europe AI-Driven Chip Design Verification Platforms Consumption Value by Country (2021-2026) & (USD Million)

Table 101. Europe AI-Driven Chip Design Verification Platforms Consumption Value by Country (2027-2032) & (USD Million)

Table 102. Asia-Pacific AI-Driven Chip Design Verification Platforms Consumption Value by Type (2021-2026) & (USD Million)

Table 103. Asia-Pacific AI-Driven Chip Design Verification Platforms Consumption Value by Type (2027-2032) & (USD Million)

Table 104. Asia-Pacific AI-Driven Chip Design Verification Platforms Consumption Value by Application (2021-2026) & (USD Million)

Table 105. Asia-Pacific AI-Driven Chip Design Verification Platforms Consumption Value by Application (2027-2032) & (USD Million)

Table 106. Asia-Pacific AI-Driven Chip Design Verification Platforms Consumption Value by Region (2021-2026) & (USD Million)

Table 107. Asia-Pacific AI-Driven Chip Design Verification Platforms Consumption Value by Region (2027-2032) & (USD Million)

Table 108. South America AI-Driven Chip Design Verification Platforms Consumption Value by Type (2021-2026) & (USD Million)

Table 109. South America AI-Driven Chip Design Verification Platforms Consumption Value by Type (2027-2032) & (USD Million)

Table 110. South America AI-Driven Chip Design Verification Platforms Consumption Value by Application (2021-2026) & (USD Million)

Table 111. South America AI-Driven Chip Design Verification Platforms Consumption Value by Application (2027-2032) & (USD Million)

Table 112. South America AI-Driven Chip Design Verification Platforms Consumption Value by Country (2021-2026) & (USD Million)

Table 113. South America AI-Driven Chip Design Verification Platforms Consumption

Value by Country (2027-2032) & (USD Million)

Table 114. Middle East & Africa AI-Driven Chip Design Verification Platforms

Consumption Value by Type (2021-2026) & (USD Million)

Table 115. Middle East & Africa AI-Driven Chip Design Verification Platforms

Consumption Value by Type (2027-2032) & (USD Million)

Table 116. Middle East & Africa AI-Driven Chip Design Verification Platforms

Consumption Value by Application (2021-2026) & (USD Million)

Table 117. Middle East & Africa AI-Driven Chip Design Verification Platforms

Consumption Value by Application (2027-2032) & (USD Million)

Table 118. Middle East & Africa AI-Driven Chip Design Verification Platforms

Consumption Value by Country (2021-2026) & (USD Million)

Table 119. Middle East & Africa AI-Driven Chip Design Verification Platforms

Consumption Value by Country (2027-2032) & (USD Million)

Table 120. Global Key Players of AI-Driven Chip Design Verification Platforms

Upstream (Raw Materials)

Table 121. Global AI-Driven Chip Design Verification Platforms Typical Customers

## List Of Figures

### LIST OF FIGURES

- Figure 1. AI-Driven Chip Design Verification Platforms Picture
- Figure 2. Global AI-Driven Chip Design Verification Platforms Consumption Value by Type, (USD Million), 2021 & 2025 & 2032
- Figure 3. Global AI-Driven Chip Design Verification Platforms Consumption Value Market Share by Type in 2025
- Figure 4. RTL Verification AI Assistant
- Figure 5. Formal Verification Agent
- Figure 6. Emulation and Prototyping Automation
- Figure 7. Mixed-Signal Verification AI
- Figure 8. Signoff Verification Copilot
- Figure 9. Global AI-Driven Chip Design Verification Platforms Consumption Value by Technology, (USD Million), 2021 & 2025 & 2032
- Figure 10. Global AI-Driven Chip Design Verification Platforms Consumption Value Market Share by Technology in 2025
- Figure 11. Generative HDL and Assertion Technology
- Figure 12. ML-Based Bug Localization
- Figure 13. LLM EDA Workflow Orchestration
- Figure 14. Cloud Verification Farm
- Figure 15. Knowledge Graph Constraint Management
- Figure 16. Global AI-Driven Chip Design Verification Platforms Consumption Value by Functional Category, (USD Million), 2021 & 2025 & 2032
- Figure 17. Global AI-Driven Chip Design Verification Platforms Consumption Value Market Share by Functional Category in 2025
- Figure 18. Testbench Generation
- Figure 19. Coverage Closure
- Figure 20. Assertion Generation
- Figure 21. Debug Root Cause Analysis
- Figure 22. Regression Prioritization
- Figure 23. Global AI-Driven Chip Design Verification Platforms Consumption Value by Application, (USD Million), 2021 & 2025 & 2032
- Figure 24. AI-Driven Chip Design Verification Platforms Consumption Value Market Share by Application in 2025
- Figure 25. AI Accelerators Picture
- Figure 26. Automotive SoC Picture
- Figure 27. Data Center ASIC Picture

Figure 28. Communication Chips Picture

Figure 29. RISC-V and IP Design Picture

Figure 30. Others Picture

Figure 31. Global AI-Driven Chip Design Verification Platforms Consumption Value, (USD Million): 2021 & 2025 & 2032

Figure 32. Global AI-Driven Chip Design Verification Platforms Consumption Value and Forecast (2021-2032) & (USD Million)

Figure 33. Global Market AI-Driven Chip Design Verification Platforms Consumption Value (USD Million) Comparison by Region (2021 VS 2025 VS 2032)

Figure 34. Global AI-Driven Chip Design Verification Platforms Consumption Value Market Share by Region (2021-2032)

Figure 35. Global AI-Driven Chip Design Verification Platforms Consumption Value Market Share by Region in 2025

Figure 36. North America AI-Driven Chip Design Verification Platforms Consumption Value (2021-2032) & (USD Million)

Figure 37. Europe AI-Driven Chip Design Verification Platforms Consumption Value (2021-2032) & (USD Million)

Figure 38. Asia-Pacific AI-Driven Chip Design Verification Platforms Consumption Value (2021-2032) & (USD Million)

Figure 39. South America AI-Driven Chip Design Verification Platforms Consumption Value (2021-2032) & (USD Million)

Figure 40. Middle East & Africa AI-Driven Chip Design Verification Platforms Consumption Value (2021-2032) & (USD Million)

Figure 41. Company Three Recent Developments and Future Plans

Figure 42. Global AI-Driven Chip Design Verification Platforms Revenue Share by Players in 2025

Figure 43. AI-Driven Chip Design Verification Platforms Market Share by Company Type (Tier 1, Tier 2, and Tier 3) in 2025

Figure 44. Market Share of AI-Driven Chip Design Verification Platforms by Player Revenue in 2025

Figure 45. Top 3 AI-Driven Chip Design Verification Platforms Players Market Share in 2025

Figure 46. Top 6 AI-Driven Chip Design Verification Platforms Players Market Share in 2025

Figure 47. Global AI-Driven Chip Design Verification Platforms Consumption Value Share by Type (2021-2026)

Figure 48. Global AI-Driven Chip Design Verification Platforms Market Share Forecast by Type (2027-2032)

Figure 49. Global AI-Driven Chip Design Verification Platforms Consumption Value

Share by Application (2021-2026)

Figure 50. Global AI-Driven Chip Design Verification Platforms Market Share Forecast by Application (2027-2032)

Figure 51. North America AI-Driven Chip Design Verification Platforms Consumption Value Market Share by Type (2021-2032)

Figure 52. North America AI-Driven Chip Design Verification Platforms Consumption Value Market Share by Application (2021-2032)

Figure 53. North America AI-Driven Chip Design Verification Platforms Consumption Value Market Share by Country (2021-2032)

Figure 54. United States AI-Driven Chip Design Verification Platforms Consumption Value (2021-2032) & (USD Million)

Figure 55. Canada AI-Driven Chip Design Verification Platforms Consumption Value (2021-2032) & (USD Million)

Figure 56. Mexico AI-Driven Chip Design Verification Platforms Consumption Value (2021-2032) & (USD Million)

Figure 57. Europe AI-Driven Chip Design Verification Platforms Consumption Value Market Share by Type (2021-2032)

Figure 58. Europe AI-Driven Chip Design Verification Platforms Consumption Value Market Share by Application (2021-2032)

Figure 59. Europe AI-Driven Chip Design Verification Platforms Consumption Value Market Share by Country (2021-2032)

Figure 60. Germany AI-Driven Chip Design Verification Platforms Consumption Value (2021-2032) & (USD Million)

Figure 61. France AI-Driven Chip Design Verification Platforms Consumption Value (2021-2032) & (USD Million)

Figure 62. United Kingdom AI-Driven Chip Design Verification Platforms Consumption Value (2021-2032) & (USD Million)

Figure 63. Russia AI-Driven Chip Design Verification Platforms Consumption Value (2021-2032) & (USD Million)

Figure 64. Italy AI-Driven Chip Design Verification Platforms Consumption Value (2021-2032) & (USD Million)

Figure 65. Asia-Pacific AI-Driven Chip Design Verification Platforms Consumption Value Market Share by Type (2021-2032)

Figure 66. Asia-Pacific AI-Driven Chip Design Verification Platforms Consumption Value Market Share by Application (2021-2032)

Figure 67. Asia-Pacific AI-Driven Chip Design Verification Platforms Consumption Value Market Share by Region (2021-2032)

Figure 68. China AI-Driven Chip Design Verification Platforms Consumption Value (2021-2032) & (USD Million)

Figure 69. Japan AI-Driven Chip Design Verification Platforms Consumption Value (2021-2032) & (USD Million)

Figure 70. South Korea AI-Driven Chip Design Verification Platforms Consumption Value (2021-2032) & (USD Million)

Figure 71. India AI-Driven Chip Design Verification Platforms Consumption Value (2021-2032) & (USD Million)

Figure 72. Southeast Asia AI-Driven Chip Design Verification Platforms Consumption Value (2021-2032) & (USD Million)

Figure 73. Australia AI-Driven Chip Design Verification Platforms Consumption Value (2021-2032) & (USD Million)

Figure 74. South America AI-Driven Chip Design Verification Platforms Consumption Value Market Share by Type (2021-2032)

Figure 75. South America AI-Driven Chip Design Verification Platforms Consumption Value Market Share by Application (2021-2032)

Figure 76. South America AI-Driven Chip Design Verification Platforms Consumption Value Market Share by Country (2021-2032)

Figure 77. Brazil AI-Driven Chip Design Verification Platforms Consumption Value (2021-2032) & (USD Million)

Figure 78. Argentina AI-Driven Chip Design Verification Platforms Consumption Value (2021-2032) & (USD Million)

Figure 79. Middle East & Africa AI-Driven Chip Design Verification Platforms Consumption Value Market Share by Type (2021-2032)

Figure 80. Middle East & Africa AI-Driven Chip Design Verification Platforms Consumption Value Market Share by Application (2021-2032)

Figure 81. Middle East & Africa AI-Driven Chip Design Verification Platforms Consumption Value Market Share by Country (2021-2032)

Figure 82. Turkey AI-Driven Chip Design Verification Platforms Consumption Value (2021-2032) & (USD Million)

Figure 83. Saudi Arabia AI-Driven Chip Design Verification Platforms Consumption Value (2021-2032) & (USD Million)

Figure 84. UAE AI-Driven Chip Design Verification Platforms Consumption Value (2021-2032) & (USD Million)

Figure 85. AI-Driven Chip Design Verification Platforms Market Drivers

Figure 86. AI-Driven Chip Design Verification Platforms Market Restraints

Figure 87. AI-Driven Chip Design Verification Platforms Market Trends

Figure 88. Porters Five Forces Analysis

Figure 89. AI-Driven Chip Design Verification Platforms Industrial Chain

Figure 90. Methodology

Figure 91. Research Process and Data Source

## I would like to order

Product name: Global AI-Driven Chip Design Verification Platforms Market 2026 by Company, Regions, Type and Application, Forecast to 2032

Product link: <https://marketpublishers.com/r/G864CAABC5F8EN.html>

Price: US\$ 3,480.00 (Single User License / Electronic Delivery)

If you want to order Corporate License or Hard Copy, please, contact our Customer Service:

[info@marketpublishers.com](mailto:info@marketpublishers.com)

## Payment

To pay by Credit Card (Visa, MasterCard, American Express, PayPal), please, click button on product page <https://marketpublishers.com/r/G864CAABC5F8EN.html>