

Global 8-inch Wafer Market 2024 by Manufacturers, Regions, Type and Application, Forecast to 2030

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Abstracts

According to our (Global Info Research) latest study, the global 8-inch Wafer market size was valued at USD 4223.6 million in 2023 and is forecast to a readjusted size of USD 4645.2 million by 2030 with a CAGR of 1.4% during review period.

Semiconductor silicon wafers are key component of integrated circuits such as those used to power computers, cell phones, and a wide variety of other devices. A silicon wafer consists of a thin slice of silicon which can be treated in various ways, depending on the type of electronics that is being used. Silicon has a very high quality semiconductor, making it ideal for the production of such circuits.

Shin-Etsu Chemical, Sumco and Global Wafers are the top 3 companies in global 8-inch wafer market and account for about 60% of total market share. Geographically speaking, Asia-Pacific holds about 70% of global market share, followed by North America with about 20% share. In terms of type, 8-inch polished segment holds an main share of over 50%. In terms of application, memory segment account for over 40% of the global share.

The Global Info Research report includes an overview of the development of the 8-inch Wafer industry chain, the market status of Memory (8-inch Polished Wafer, 8-inch Epitaxial Wafer), Logic/MPU (8-inch Polished Wafer, 8-inch Epitaxial Wafer), and key enterprises in developed and developing market, and analysed the cutting-edge technology, patent, hot applications and market trends of 8-inch Wafer.

Regionally, the report analyzes the 8-inch Wafer markets in key regions. North America and Europe are experiencing steady growth, driven by government initiatives and increasing consumer awareness. Asia-Pacific, particularly China, leads the global 8-inch



Wafer market, with robust domestic demand, supportive policies, and a strong manufacturing base.

Key Features:

The report presents comprehensive understanding of the 8-inch Wafer market. It provides a holistic view of the industry, as well as detailed insights into individual components and stakeholders. The report analysis market dynamics, trends, challenges, and opportunities within the 8-inch Wafer industry.

The report involves analyzing the market at a macro level:

Market Sizing and Segmentation: Report collect data on the overall market size, including the sales quantity (K Pcs), revenue generated, and market share of different by Type (e.g., 8-inch Polished Wafer, 8-inch Epitaxial Wafer).

Industry Analysis: Report analyse the broader industry trends, such as government policies and regulations, technological advancements, consumer preferences, and market dynamics. This analysis helps in understanding the key drivers and challenges influencing the 8-inch Wafer market.

Regional Analysis: The report involves examining the 8-inch Wafer market at a regional or national level. Report analyses regional factors such as government incentives, infrastructure development, economic conditions, and consumer behaviour to identify variations and opportunities within different markets.

Market Projections: Report covers the gathered data and analysis to make future projections and forecasts for the 8-inch Wafer market. This may include estimating market growth rates, predicting market demand, and identifying emerging trends.

The report also involves a more granular approach to 8-inch Wafer:

Company Analysis: Report covers individual 8-inch Wafer manufacturers, suppliers, and other relevant industry players. This analysis includes studying their financial performance, market positioning, product portfolios, partnerships, and strategies.

Consumer Analysis: Report covers data on consumer behaviour, preferences, and attitudes towards 8-inch Wafer This may involve surveys, interviews, and analysis of consumer reviews and feedback from different by Application (Memory, Logic/MPU).



Technology Analysis: Report covers specific technologies relevant to 8-inch Wafer. It assesses the current state, advancements, and potential future developments in 8-inch Wafer areas.

Competitive Landscape: By analyzing individual companies, suppliers, and consumers, the report present insights into the competitive landscape of the 8-inch Wafer market. This analysis helps understand market share, competitive advantages, and potential areas for differentiation among industry players.

Market Validation: The report involves validating findings and projections through primary research, such as surveys, interviews, and focus groups.

Market Segmentation

8-inch Wafer market is split by Type and by Application. For the period 2019-2030, the growth among segments provides accurate calculations and forecasts for consumption value by Type, and by Application in terms of volume and value.

Market segment by Type

8-inch Polished Wafer

8-inch Epitaxial Wafer

8-inch Annealed Wafer

Other

Market segment by Application

Memory

Logic/MPU

Analog

Discrete Device&Sensor



Other	
Major players covered	
Shin-Etsu Chemical	
Sumco	
Global Wafers	
Siltronic	
SK Siltron	
Waferworks	
Ferrotec	
AST	
Gritek	
Guosheng	
QL Electronics	
MCL	
National Silicon Industry Group	
Poshing	

Market segment by region, regional analysis covers

North America (United States, Canada and Mexico)



Europe (Germany, France, United Kingdom, Russia, Italy, and Rest of Europe)

Asia-Pacific (China, Japan, Korea, India, Southeast Asia, and Australia)

South America (Brazil, Argentina, Colombia, and Rest of South America)

Middle East & Africa (Saudi Arabia, UAE, Egypt, South Africa, and Rest of Middle East & Africa)

The content of the study subjects, includes a total of 15 chapters:

Chapter 1, to describe 8-inch Wafer product scope, market overview, market estimation caveats and base year.

Chapter 2, to profile the top manufacturers of 8-inch Wafer, with price, sales, revenue and global market share of 8-inch Wafer from 2019 to 2024.

Chapter 3, the 8-inch Wafer competitive situation, sales quantity, revenue and global market share of top manufacturers are analyzed emphatically by landscape contrast.

Chapter 4, the 8-inch Wafer breakdown data are shown at the regional level, to show the sales quantity, consumption value and growth by regions, from 2019 to 2030.

Chapter 5 and 6, to segment the sales by Type and application, with sales market share and growth rate by type, application, from 2019 to 2030.

Chapter 7, 8, 9, 10 and 11, to break the sales data at the country level, with sales quantity, consumption value and market share for key countries in the world, from 2017 to 2023.and 8-inch Wafer market forecast, by regions, type and application, with sales and revenue, from 2025 to 2030.

Chapter 12, market dynamics, drivers, restraints, trends and Porters Five Forces analysis.

Chapter 13, the key raw materials and key suppliers, and industry chain of 8-inch Wafer.

Chapter 14 and 15, to describe 8-inch Wafer sales channel, distributors, customers,



research findings and conclusion.



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