

# Global Bidirectional Logic Bus Buffer Market 2026 by Manufacturers, Regions, Type and Application, Forecast to 2032

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## Abstracts

According to our (Global Info Research) latest study, the global Bidirectional Logic Bus Buffer market size was valued at US\$ 4522 million in 2025 and is forecast to a readjusted size of US\$ 7621 million by 2032 with a CAGR of 7.7% during review period.

In 2025, global sales of bidirectional logic bus buffers across various application scenarios were estimated at 5.86 billion units. The average selling price was approximately USD 0.75-0.85 per unit, and the overall gross profit margin was approximately 28%-40%, with automotive and industrial bus transceivers such as CAN/LIN/RS-485 being the main contributors. Bidirectional logic bus buffers, as the physical layer interface chip between MCUs/SoCs and fieldbuses/vehicle buses, are fundamental components for reliable differential communication and bus protection in systems such as automotive electronics, motor drives, industrial control, and building/energy management. Their core value lies in solving the pain points of traditional single-ended communication in long-distance, multi-node, and electromagnetically interference-prone environments, such as easy bit errors, easy lock-up, difficulty in suppressing common-mode interference, and the ability of node power failure/short circuit to bring down the entire network. In typical automotive CAN/LIN networks, industrial RS-485 buses, and building and energy management fieldbuses, without highly robust bus transceivers, the controller side cannot withstand common-mode interference of -7 to +12 V or even higher, cable misconnections, and transient surges, making it difficult to guarantee system reliability and security. A typical bidirectional logic bus buffer structure includes: TXD/RXD or differential I/O pins connected to the controller side, a bus-side differential driver/receiver stage (such as CANH/CANL, A/B lines), current limiting and overvoltage protection networks, ESD/surge protection circuitry, fault protection and bus fail-safe circuitry, low-

power/standby/wake-up logic, power supply and reference circuitry, and package pin/heat dissipation structure. Common parameters include: support for bus standards (CAN FD/LIN/RS-485/RS-422, etc.), data rates from 20 kbit/s (LIN) to 1 Mbit/s, 2–5 Mbit/s (CAN FD) and even 50 Mbit/s, supply voltage of 3.3 V or 5 V, common-mode voltage range of  $\pm 7$  to +12 V or even  $\pm 12$  to +12 V, bus-side ESD protection of  $\pm 8$ – $\pm 16$  kV, and operating temperature of  $\pm 40$  to +125 °C. In terms of typical system usage: a gasoline-powered vehicle requires a total of 15–30 CAN/LIN bus buffers, while a mid-to-high-end new energy vehicle can have 30–60; a medium-sized PLC/distributed I/O station requires approximately 2–6 RS-485/field bidirectional logic bus buffers; a photovoltaic inverter/energy storage BMS system requires 4–10 buffers; and industrial motors/servo drives typically require 1–3 buffers. The upstream mainly relies on mature process logic and high-voltage wafers (8/16/32 nm and above), packaging substrates and molding materials, precision resistors, capacitors and protection devices, and lead frames; the downstream focuses on automotive ECU manufacturers, industrial control and PLC manufacturers, motor and drive manufacturers, photovoltaic and energy storage system integrators, and building/energy management and rail transit control system suppliers.

This report is a detailed and comprehensive analysis for global Bidirectional Logic Bus Buffer market. Both quantitative and qualitative analyses are presented by manufacturers, by region & country, by Voltage and by Application. As the market is constantly changing, this report explores the competition, supply and demand trends, as well as key factors that contribute to its changing demands across many markets. Company profiles and product examples of selected competitors, along with market share estimates of some of the selected leaders for the year 2025, are provided.

### **Key Features:**

Global Bidirectional Logic Bus Buffer market size and forecasts, in consumption value (\$ Million), sales quantity (Million Units), and average selling prices (US\$/Unit), 2021-2032

Global Bidirectional Logic Bus Buffer market size and forecasts by region and country, in consumption value (\$ Million), sales quantity (Million Units), and average selling prices (US\$/Unit), 2021-2032

Global Bidirectional Logic Bus Buffer market size and forecasts, by Voltage and by Application, in consumption value (\$ Million), sales quantity (Million Units), and average selling prices (US\$/Unit), 2021-2032

Global Bidirectional Logic Bus Buffer market shares of main players, shipments in revenue (\$ Million), sales quantity (Million Units), and ASP (US\$/Unit), 2021-2026

### **The Primary Objectives in This Report Are:**

- To determine the size of the total market opportunity of global and key countries
- To assess the growth potential for Bidirectional Logic Bus Buffer
- To forecast future growth in each product and end-use market
- To assess competitive factors affecting the marketplace

This report profiles key players in the global Bidirectional Logic Bus Buffer market based on the following parameters - company overview, sales quantity, revenue, price, gross margin, product portfolio, geographical presence, and key developments. Key companies covered as a part of this study include Onsemi, TI, Vector Informatik, Toshiba, Infineon, Microchip Technology, Exar, STMicroelectronics, SG MICRO, Nexperia, etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals.

### **Market Segmentation**

Bidirectional Logic Bus Buffer market is split by Voltage and by Application. For the period 2021-2032, the growth among segments provides accurate calculations and forecasts for consumption value by Voltage, and by Application in terms of volume and value. This analysis can help you expand your business by targeting qualified niche markets.

#### Market segment by Voltage

3.6V

5.5V

6V

#### Market segment by Current Sinking Capability

8 mA

24 mA

Others

#### Market segment by Packaging Method

DW Package

N Package

#### Market segment by Application

Industrial Control

Automotive Electronics

Smart Home

Others

#### Major players covered

Onsemi

TI

Vector Informatik

Toshiba

Infineon

Microchip Technology

Exar

STMicroelectronics

SG MICRO

Nexperia

Adafruit

Analog Devices

NTE Electronics

Diodes Incorporated

Renesas Electronics

Teledyne

Market segment by region, regional analysis covers  
North America (United States, Canada, and Mexico)  
Europe (Germany, France, United Kingdom, Russia, Italy, and Rest of Europe)  
Asia-Pacific (China, Japan, Korea, India, Southeast Asia, and Australia)  
South America (Brazil, Argentina, Colombia, and Rest of South America)  
Middle East & Africa (Saudi Arabia, UAE, Egypt, South Africa, and Rest of Middle East & Africa)

**The content of the study subjects, includes a total of 15 chapters:**

Chapter 1, to describe Bidirectional Logic Bus Buffer product scope, market overview, market estimation caveats and base year.

Chapter 2, to profile the top manufacturers of Bidirectional Logic Bus Buffer, with price, sales quantity, revenue, and global market share of Bidirectional Logic Bus Buffer from 2021 to 2026.

Chapter 3, the Bidirectional Logic Bus Buffer competitive situation, sales quantity,

revenue, and global market share of top manufacturers are analyzed emphatically by landscape contrast.

Chapter 4, the Bidirectional Logic Bus Buffer breakdown data are shown at the regional level, to show the sales quantity, consumption value, and growth by regions, from 2021 to 2032.

Chapter 5 and 6, to segment the sales by Voltage and by Application, with sales market share and growth rate by Voltage, by Application, from 2021 to 2032.

Chapter 7, 8, 9, 10 and 11, to break the sales data at the country level, with sales quantity, consumption value, and market share for key countries in the world, from 2021 to 2026. and Bidirectional Logic Bus Buffer market forecast, by regions, by Voltage, and by Application, with sales and revenue, from 2027 to 2032.

Chapter 12, market dynamics, drivers, restraints, trends, and Porters Five Forces analysis.

Chapter 13, the key raw materials and key suppliers, and industry chain of Bidirectional Logic Bus Buffer.

Chapter 14 and 15, to describe Bidirectional Logic Bus Buffer sales channel, distributors, customers, research findings and conclusion.

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