

Global Bidirectional Bus Buffer Market 2026 by Manufacturers, Regions, Type and Application, Forecast to 2032

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Abstracts

According to our (Global Info Research) latest study, the global Bidirectional Bus Buffer market size was valued at US\$ 4522 million in 2025 and is forecast to a readjusted size of US\$ 7621 million by 2032 with a CAGR of 7.7% during review period.

In 2025, global sales of bidirectional bus buffers across various application scenarios were estimated at 5.86 billion units. The average selling price was approximately USD 0.75-0.85 per unit, and the overall gross profit margin was approximately 28%-40%, with automotive and industrial bus transceivers such as CAN/LIN/RS-485 being the main contributors. Bidirectional bus buffers, as the physical layer interface chip between MCUs/SoCs and fieldbuses/vehicle buses, are fundamental components for reliable differential communication and bus protection in systems such as automotive electronics, motor drives, industrial control, and building/energy management. Their core value lies in solving the pain points of traditional single-ended communication in long-distance, multi-node, and electromagnetically interference-prone environments, such as easy bit errors, easy lock-up, difficulty in suppressing common-mode interference, and the ability of node power failure/short circuit to bring down the entire network. In typical automotive CAN/LIN networks, industrial RS-485 buses, and building and energy management fieldbuses, without highly robust bus transceivers, the controller side cannot withstand common-mode interference of -7 to +12 V or even higher, cable misconnections, and transient surges, making it difficult to guarantee system reliability and security. A typical bidirectional bus buffer structure includes: TXD/RXD or differential I/O pins connected to the controller side, a bus-side differential driver/receiver stage (such as CANH/CANL, A/B lines), current limiting and overvoltage protection networks, ESD/surge protection circuitry, fault protection and bus fail-safe circuitry, low-power/standby/wake-up logic, power supply and reference circuitry, and

package pin/heat dissipation structure. Common parameters include: support for bus standards (CAN FD/LIN/RS-485/RS-422, etc.), data rates from 20 kbit/s (LIN) to 1 Mbit/s, 2–5 Mbit/s (CAN FD) and even 50 Mbit/s, supply voltage of 3.3 V or 5 V, common-mode voltage range of ± 7 to +12 V or even ± 12 to +12 V, bus-side ESD protection of ± 8 – ± 16 kV, and operating temperature of ± 40 to +125 °C. In terms of typical system usage: a gasoline-powered vehicle requires a total of 15–30 CAN/LIN bus transceivers, while a mid-to-high-end new energy vehicle can have 30–60; a medium-sized PLC/distributed I/O station requires approximately 2–6 RS-485/fieldbus transceivers; a photovoltaic inverter/energy storage BMS system requires 4–10 transceivers; and industrial motors/servo drives typically require 1–3 buffers. The upstream mainly relies on mature process logic and high-voltage wafers (8/16/32 nm and above), packaging substrates and molding materials, precision resistors, capacitors and protection devices, and lead frames; the downstream focuses on automotive ECU manufacturers, industrial control and PLC manufacturers, motor and drive manufacturers, photovoltaic and energy storage system integrators, and building/energy management and rail transit control system suppliers.

This report is a detailed and comprehensive analysis for global Bidirectional Bus Buffer market. Both quantitative and qualitative analyses are presented by manufacturers, by region & country, by Voltage and by Application. As the market is constantly changing, this report explores the competition, supply and demand trends, as well as key factors that contribute to its changing demands across many markets. Company profiles and product examples of selected competitors, along with market share estimates of some of the selected leaders for the year 2025, are provided.

Key Features:

Global Bidirectional Bus Buffer market size and forecasts, in consumption value (\$ Million), sales quantity (Million Units), and average selling prices (US\$/Unit), 2021-2032

Global Bidirectional Bus Buffer market size and forecasts by region and country, in consumption value (\$ Million), sales quantity (Million Units), and average selling prices (US\$/Unit), 2021-2032

Global Bidirectional Bus Buffer market size and forecasts, by Voltage and by Application, in consumption value (\$ Million), sales quantity (Million Units), and average selling prices (US\$/Unit), 2021-2032

Global Bidirectional Bus Buffer market shares of main players, shipments in revenue (\$

Million), sales quantity (Million Units), and ASP (US\$/Unit), 2021-2026

The Primary Objectives in This Report Are:

To determine the size of the total market opportunity of global and key countries

To assess the growth potential for Bidirectional Bus Buffer

To forecast future growth in each product and end-use market

To assess competitive factors affecting the marketplace

This report profiles key players in the global Bidirectional Bus Buffer market based on the following parameters - company overview, sales quantity, revenue, price, gross margin, product portfolio, geographical presence, and key developments. Key companies covered as a part of this study include Onsemi, TI, Vector Informatik, Toshiba, Infineon, Microchip Technology, Exar, STMicroelectronics, SG MICRO, Nexperia, etc.

This report also provides key insights about market drivers, restraints, opportunities, new product launches or approvals.

Market Segmentation

Bidirectional Bus Buffer market is split by Voltage and by Application. For the period 2021-2032, the growth among segments provides accurate calculations and forecasts for consumption value by Voltage, and by Application in terms of volume and value. This analysis can help you expand your business by targeting qualified niche markets.

Market segment by Voltage

3.6V

5.5V

6V

Market segment by Current Sinking Capability

8 mA

24 mA

Others

Market segment by Packaging Method

DW Package

N Package

Market segment by Application

Industrial Control

Automotive Electronics

Smart Home

Others

Major players covered

Onsemi

TI

Vector Informatik

Toshiba

Infineon

Microchip Technology

Exar

STMicroelectronics

SG MICRO

Nexperia

Adafruit

Analog Devices

NTE Electronics

Diodes Incorporated

Renesas Electronics

Teledyne

Market segment by region, regional analysis covers
North America (United States, Canada, and Mexico)
Europe (Germany, France, United Kingdom, Russia, Italy, and Rest of Europe)
Asia-Pacific (China, Japan, Korea, India, Southeast Asia, and Australia)
South America (Brazil, Argentina, Colombia, and Rest of South America)
Middle East & Africa (Saudi Arabia, UAE, Egypt, South Africa, and Rest of Middle East & Africa)

The content of the study subjects, includes a total of 15 chapters:

Chapter 1, to describe Bidirectional Bus Buffer product scope, market overview, market estimation caveats and base year.

Chapter 2, to profile the top manufacturers of Bidirectional Bus Buffer, with price, sales quantity, revenue, and global market share of Bidirectional Bus Buffer from 2021 to 2026.

Chapter 3, the Bidirectional Bus Buffer competitive situation, sales quantity, revenue, and global market share of top manufacturers are analyzed emphatically by landscape contrast.

Chapter 4, the Bidirectional Bus Buffer breakdown data are shown at the regional level, to show the sales quantity, consumption value, and growth by regions, from 2021 to 2032.

Chapter 5 and 6, to segment the sales by Voltage and by Application, with sales market share and growth rate by Voltage, by Application, from 2021 to 2032.

Chapter 7, 8, 9, 10 and 11, to break the sales data at the country level, with sales quantity, consumption value, and market share for key countries in the world, from 2021 to 2026. and Bidirectional Bus Buffer market forecast, by regions, by Voltage, and by Application, with sales and revenue, from 2027 to 2032.

Chapter 12, market dynamics, drivers, restraints, trends, and Porters Five Forces analysis.

Chapter 13, the key raw materials and key suppliers, and industry chain of Bidirectional Bus Buffer.

Chapter 14 and 15, to describe Bidirectional Bus Buffer sales channel, distributors, customers, research findings and conclusion.

Contents

1 MARKET OVERVIEW

1.1 Product Overview and Scope

1.2 Market Estimation Caveats and Base Year

1.3 Market Analysis by Voltage

1.3.1 Overview: Global Bidirectional Bus Buffer Consumption Value by Voltage: 2021 Versus 2025 Versus 2032

1.3.2 3.6V

1.3.3 5.5V

1.3.4 6V

1.4 Market Analysis by Current Sinking Capability

1.4.1 Overview: Global Bidirectional Bus Buffer Consumption Value by Current Sinking Capability: 2021 Versus 2025 Versus 2032

1.4.2 8 mA

1.4.3 24 mA

1.4.4 Others

1.5 Market Analysis by Packaging Method

1.5.1 Overview: Global Bidirectional Bus Buffer Consumption Value by Packaging Method: 2021 Versus 2025 Versus 2032

1.5.2 DW Package

1.5.3 N Package

1.6 Market Analysis by Application

1.6.1 Overview: Global Bidirectional Bus Buffer Consumption Value by Application: 2021 Versus 2025 Versus 2032

1.6.2 Industrial Control

1.6.3 Automotive Electronics

1.6.4 Smart Home

1.6.5 Others

1.7 Global Bidirectional Bus Buffer Market Size & Forecast

1.7.1 Global Bidirectional Bus Buffer Consumption Value (2021 & 2025 & 2032)

1.7.2 Global Bidirectional Bus Buffer Sales Quantity (2021-2032)

1.7.3 Global Bidirectional Bus Buffer Average Price (2021-2032)

2 MANUFACTURERS PROFILES

2.1 Onsemi

2.1.1 Onsemi Details

- 2.1.2 Onsemi Major Business
- 2.1.3 Onsemi Bidirectional Bus Buffer Product and Services
- 2.1.4 Onsemi Bidirectional Bus Buffer Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
- 2.1.5 Onsemi Recent Developments/Updates
- 2.2 TI
 - 2.2.1 TI Details
 - 2.2.2 TI Major Business
 - 2.2.3 TI Bidirectional Bus Buffer Product and Services
 - 2.2.4 TI Bidirectional Bus Buffer Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
 - 2.2.5 TI Recent Developments/Updates
- 2.3 Vector Informatik
 - 2.3.1 Vector Informatik Details
 - 2.3.2 Vector Informatik Major Business
 - 2.3.3 Vector Informatik Bidirectional Bus Buffer Product and Services
 - 2.3.4 Vector Informatik Bidirectional Bus Buffer Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
 - 2.3.5 Vector Informatik Recent Developments/Updates
- 2.4 Toshiba
 - 2.4.1 Toshiba Details
 - 2.4.2 Toshiba Major Business
 - 2.4.3 Toshiba Bidirectional Bus Buffer Product and Services
 - 2.4.4 Toshiba Bidirectional Bus Buffer Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
 - 2.4.5 Toshiba Recent Developments/Updates
- 2.5 Infineon
 - 2.5.1 Infineon Details
 - 2.5.2 Infineon Major Business
 - 2.5.3 Infineon Bidirectional Bus Buffer Product and Services
 - 2.5.4 Infineon Bidirectional Bus Buffer Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
 - 2.5.5 Infineon Recent Developments/Updates
- 2.6 Microchip Technology
 - 2.6.1 Microchip Technology Details
 - 2.6.2 Microchip Technology Major Business
 - 2.6.3 Microchip Technology Bidirectional Bus Buffer Product and Services
 - 2.6.4 Microchip Technology Bidirectional Bus Buffer Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)

- 2.6.5 Microchip Technology Recent Developments/Updates
- 2.7 Exar
 - 2.7.1 Exar Details
 - 2.7.2 Exar Major Business
 - 2.7.3 Exar Bidirectional Bus Buffer Product and Services
 - 2.7.4 Exar Bidirectional Bus Buffer Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
 - 2.7.5 Exar Recent Developments/Updates
- 2.8 STMicroelectronics
 - 2.8.1 STMicroelectronics Details
 - 2.8.2 STMicroelectronics Major Business
 - 2.8.3 STMicroelectronics Bidirectional Bus Buffer Product and Services
 - 2.8.4 STMicroelectronics Bidirectional Bus Buffer Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
 - 2.8.5 STMicroelectronics Recent Developments/Updates
- 2.9 SG MICRO
 - 2.9.1 SG MICRO Details
 - 2.9.2 SG MICRO Major Business
 - 2.9.3 SG MICRO Bidirectional Bus Buffer Product and Services
 - 2.9.4 SG MICRO Bidirectional Bus Buffer Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
 - 2.9.5 SG MICRO Recent Developments/Updates
- 2.10 Nexperia
 - 2.10.1 Nexperia Details
 - 2.10.2 Nexperia Major Business
 - 2.10.3 Nexperia Bidirectional Bus Buffer Product and Services
 - 2.10.4 Nexperia Bidirectional Bus Buffer Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
 - 2.10.5 Nexperia Recent Developments/Updates
- 2.11 Adafruit
 - 2.11.1 Adafruit Details
 - 2.11.2 Adafruit Major Business
 - 2.11.3 Adafruit Bidirectional Bus Buffer Product and Services
 - 2.11.4 Adafruit Bidirectional Bus Buffer Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
 - 2.11.5 Adafruit Recent Developments/Updates
- 2.12 Analog Devices
 - 2.12.1 Analog Devices Details
 - 2.12.2 Analog Devices Major Business

- 2.12.3 Analog Devices Bidirectional Bus Buffer Product and Services
- 2.12.4 Analog Devices Bidirectional Bus Buffer Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
- 2.12.5 Analog Devices Recent Developments/Updates
- 2.13 NTE Electronics
 - 2.13.1 NTE Electronics Details
 - 2.13.2 NTE Electronics Major Business
 - 2.13.3 NTE Electronics Bidirectional Bus Buffer Product and Services
 - 2.13.4 NTE Electronics Bidirectional Bus Buffer Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
 - 2.13.5 NTE Electronics Recent Developments/Updates
- 2.14 Diodes Incorporated
 - 2.14.1 Diodes Incorporated Details
 - 2.14.2 Diodes Incorporated Major Business
 - 2.14.3 Diodes Incorporated Bidirectional Bus Buffer Product and Services
 - 2.14.4 Diodes Incorporated Bidirectional Bus Buffer Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
 - 2.14.5 Diodes Incorporated Recent Developments/Updates
- 2.15 Renesas Electronics
 - 2.15.1 Renesas Electronics Details
 - 2.15.2 Renesas Electronics Major Business
 - 2.15.3 Renesas Electronics Bidirectional Bus Buffer Product and Services
 - 2.15.4 Renesas Electronics Bidirectional Bus Buffer Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
 - 2.15.5 Renesas Electronics Recent Developments/Updates
- 2.16 Teledyne
 - 2.16.1 Teledyne Details
 - 2.16.2 Teledyne Major Business
 - 2.16.3 Teledyne Bidirectional Bus Buffer Product and Services
 - 2.16.4 Teledyne Bidirectional Bus Buffer Sales Quantity, Average Price, Revenue, Gross Margin and Market Share (2021-2026)
 - 2.16.5 Teledyne Recent Developments/Updates

3 COMPETITIVE ENVIRONMENT: BIDIRECTIONAL BUS BUFFER BY MANUFACTURER

- 3.1 Global Bidirectional Bus Buffer Sales Quantity by Manufacturer (2021-2026)
- 3.2 Global Bidirectional Bus Buffer Revenue by Manufacturer (2021-2026)
- 3.3 Global Bidirectional Bus Buffer Average Price by Manufacturer (2021-2026)

3.4 Market Share Analysis (2025)

3.4.1 Producer Shipments of Bidirectional Bus Buffer by Manufacturer Revenue (\$MM) and Market Share (%): 2025

3.4.2 Top 3 Bidirectional Bus Buffer Manufacturer Market Share in 2025

3.4.3 Top 6 Bidirectional Bus Buffer Manufacturer Market Share in 2025

3.5 Bidirectional Bus Buffer Market: Overall Company Footprint Analysis

3.5.1 Bidirectional Bus Buffer Market: Region Footprint

3.5.2 Bidirectional Bus Buffer Market: Company Product Type Footprint

3.5.3 Bidirectional Bus Buffer Market: Company Product Application Footprint

3.6 New Market Entrants and Barriers to Market Entry

3.7 Mergers, Acquisition, Agreements, and Collaborations

4 CONSUMPTION ANALYSIS BY REGION

4.1 Global Bidirectional Bus Buffer Market Size by Region

4.1.1 Global Bidirectional Bus Buffer Sales Quantity by Region (2021-2032)

4.1.2 Global Bidirectional Bus Buffer Consumption Value by Region (2021-2032)

4.1.3 Global Bidirectional Bus Buffer Average Price by Region (2021-2032)

4.2 North America Bidirectional Bus Buffer Consumption Value (2021-2032)

4.3 Europe Bidirectional Bus Buffer Consumption Value (2021-2032)

4.4 Asia-Pacific Bidirectional Bus Buffer Consumption Value (2021-2032)

4.5 South America Bidirectional Bus Buffer Consumption Value (2021-2032)

4.6 Middle East & Africa Bidirectional Bus Buffer Consumption Value (2021-2032)

5 MARKET SEGMENT BY VOLTAGE

5.1 Global Bidirectional Bus Buffer Sales Quantity by Voltage (2021-2032)

5.2 Global Bidirectional Bus Buffer Consumption Value by Voltage (2021-2032)

5.3 Global Bidirectional Bus Buffer Average Price by Voltage (2021-2032)

6 MARKET SEGMENT BY APPLICATION

6.1 Global Bidirectional Bus Buffer Sales Quantity by Application (2021-2032)

6.2 Global Bidirectional Bus Buffer Consumption Value by Application (2021-2032)

6.3 Global Bidirectional Bus Buffer Average Price by Application (2021-2032)

7 NORTH AMERICA

7.1 North America Bidirectional Bus Buffer Sales Quantity by Voltage (2021-2032)

- 7.2 North America Bidirectional Bus Buffer Sales Quantity by Application (2021-2032)
- 7.3 North America Bidirectional Bus Buffer Market Size by Country
 - 7.3.1 North America Bidirectional Bus Buffer Sales Quantity by Country (2021-2032)
 - 7.3.2 North America Bidirectional Bus Buffer Consumption Value by Country (2021-2032)
 - 7.3.3 United States Market Size and Forecast (2021-2032)
 - 7.3.4 Canada Market Size and Forecast (2021-2032)
 - 7.3.5 Mexico Market Size and Forecast (2021-2032)

8 EUROPE

- 8.1 Europe Bidirectional Bus Buffer Sales Quantity by Voltage (2021-2032)
- 8.2 Europe Bidirectional Bus Buffer Sales Quantity by Application (2021-2032)
- 8.3 Europe Bidirectional Bus Buffer Market Size by Country
 - 8.3.1 Europe Bidirectional Bus Buffer Sales Quantity by Country (2021-2032)
 - 8.3.2 Europe Bidirectional Bus Buffer Consumption Value by Country (2021-2032)
 - 8.3.3 Germany Market Size and Forecast (2021-2032)
 - 8.3.4 France Market Size and Forecast (2021-2032)
 - 8.3.5 United Kingdom Market Size and Forecast (2021-2032)
 - 8.3.6 Russia Market Size and Forecast (2021-2032)
 - 8.3.7 Italy Market Size and Forecast (2021-2032)

9 ASIA-PACIFIC

- 9.1 Asia-Pacific Bidirectional Bus Buffer Sales Quantity by Voltage (2021-2032)
- 9.2 Asia-Pacific Bidirectional Bus Buffer Sales Quantity by Application (2021-2032)
- 9.3 Asia-Pacific Bidirectional Bus Buffer Market Size by Region
 - 9.3.1 Asia-Pacific Bidirectional Bus Buffer Sales Quantity by Region (2021-2032)
 - 9.3.2 Asia-Pacific Bidirectional Bus Buffer Consumption Value by Region (2021-2032)
 - 9.3.3 China Market Size and Forecast (2021-2032)
 - 9.3.4 Japan Market Size and Forecast (2021-2032)
 - 9.3.5 South Korea Market Size and Forecast (2021-2032)
 - 9.3.6 India Market Size and Forecast (2021-2032)
 - 9.3.7 Southeast Asia Market Size and Forecast (2021-2032)
 - 9.3.8 Australia Market Size and Forecast (2021-2032)

10 SOUTH AMERICA

- 10.1 South America Bidirectional Bus Buffer Sales Quantity by Voltage (2021-2032)

- 10.2 South America Bidirectional Bus Buffer Sales Quantity by Application (2021-2032)
- 10.3 South America Bidirectional Bus Buffer Market Size by Country
 - 10.3.1 South America Bidirectional Bus Buffer Sales Quantity by Country (2021-2032)
 - 10.3.2 South America Bidirectional Bus Buffer Consumption Value by Country (2021-2032)
 - 10.3.3 Brazil Market Size and Forecast (2021-2032)
 - 10.3.4 Argentina Market Size and Forecast (2021-2032)

11 MIDDLE EAST & AFRICA

- 11.1 Middle East & Africa Bidirectional Bus Buffer Sales Quantity by Voltage (2021-2032)
- 11.2 Middle East & Africa Bidirectional Bus Buffer Sales Quantity by Application (2021-2032)
- 11.3 Middle East & Africa Bidirectional Bus Buffer Market Size by Country
 - 11.3.1 Middle East & Africa Bidirectional Bus Buffer Sales Quantity by Country (2021-2032)
 - 11.3.2 Middle East & Africa Bidirectional Bus Buffer Consumption Value by Country (2021-2032)
 - 11.3.3 Turkey Market Size and Forecast (2021-2032)
 - 11.3.4 Egypt Market Size and Forecast (2021-2032)
 - 11.3.5 Saudi Arabia Market Size and Forecast (2021-2032)
 - 11.3.6 South Africa Market Size and Forecast (2021-2032)

12 MARKET DYNAMICS

- 12.1 Bidirectional Bus Buffer Market Drivers
- 12.2 Bidirectional Bus Buffer Market Restraints
- 12.3 Bidirectional Bus Buffer Trends Analysis
- 12.4 Porters Five Forces Analysis
 - 12.4.1 Threat of New Entrants
 - 12.4.2 Bargaining Power of Suppliers
 - 12.4.3 Bargaining Power of Buyers
 - 12.4.4 Threat of Substitutes
 - 12.4.5 Competitive Rivalry

13 RAW MATERIAL AND INDUSTRY CHAIN

- 13.1 Raw Material of Bidirectional Bus Buffer and Key Manufacturers

- 13.2 Manufacturing Costs Percentage of Bidirectional Bus Buffer
- 13.3 Bidirectional Bus Buffer Production Process
- 13.4 Industry Value Chain Analysis

14 SHIPMENTS BY DISTRIBUTION CHANNEL

- 14.1 Sales Channel
 - 14.1.1 Direct to End-User
 - 14.1.2 Distributors
- 14.2 Bidirectional Bus Buffer Typical Distributors
- 14.3 Bidirectional Bus Buffer Typical Customers

15 RESEARCH FINDINGS AND CONCLUSION

16 APPENDIX

- 16.1 Methodology
- 16.2 Research Process and Data Source
- 16.3 Disclaimer

List Of Tables

LIST OF TABLES

- Table 1. Global Bidirectional Bus Buffer Consumption Value by Voltage, (USD Million), 2021 & 2025 & 2032
- Table 2. Global Bidirectional Bus Buffer Consumption Value by Current Sinking Capability, (USD Million), 2021 & 2025 & 2032
- Table 3. Global Bidirectional Bus Buffer Consumption Value by Packaging Method, (USD Million), 2021 & 2025 & 2032
- Table 4. Global Bidirectional Bus Buffer Consumption Value by Application, (USD Million), 2021 & 2025 & 2032
- Table 5. Onsemi Basic Information, Manufacturing Base and Competitors
- Table 6. Onsemi Major Business
- Table 7. Onsemi Bidirectional Bus Buffer Product and Services
- Table 8. Onsemi Bidirectional Bus Buffer Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)
- Table 9. Onsemi Recent Developments/Updates
- Table 10. TI Basic Information, Manufacturing Base and Competitors
- Table 11. TI Major Business
- Table 12. TI Bidirectional Bus Buffer Product and Services
- Table 13. TI Bidirectional Bus Buffer Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)
- Table 14. TI Recent Developments/Updates
- Table 15. Vector Informatik Basic Information, Manufacturing Base and Competitors
- Table 16. Vector Informatik Major Business
- Table 17. Vector Informatik Bidirectional Bus Buffer Product and Services
- Table 18. Vector Informatik Bidirectional Bus Buffer Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)
- Table 19. Vector Informatik Recent Developments/Updates
- Table 20. Toshiba Basic Information, Manufacturing Base and Competitors
- Table 21. Toshiba Major Business
- Table 22. Toshiba Bidirectional Bus Buffer Product and Services
- Table 23. Toshiba Bidirectional Bus Buffer Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)
- Table 24. Toshiba Recent Developments/Updates
- Table 25. Infineon Basic Information, Manufacturing Base and Competitors
- Table 26. Infineon Major Business

Table 27. Infineon Bidirectional Bus Buffer Product and Services

Table 28. Infineon Bidirectional Bus Buffer Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 29. Infineon Recent Developments/Updates

Table 30. Microchip Technology Basic Information, Manufacturing Base and Competitors

Table 31. Microchip Technology Major Business

Table 32. Microchip Technology Bidirectional Bus Buffer Product and Services

Table 33. Microchip Technology Bidirectional Bus Buffer Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 34. Microchip Technology Recent Developments/Updates

Table 35. Exar Basic Information, Manufacturing Base and Competitors

Table 36. Exar Major Business

Table 37. Exar Bidirectional Bus Buffer Product and Services

Table 38. Exar Bidirectional Bus Buffer Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 39. Exar Recent Developments/Updates

Table 40. STMicroelectronics Basic Information, Manufacturing Base and Competitors

Table 41. STMicroelectronics Major Business

Table 42. STMicroelectronics Bidirectional Bus Buffer Product and Services

Table 43. STMicroelectronics Bidirectional Bus Buffer Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 44. STMicroelectronics Recent Developments/Updates

Table 45. SG MICRO Basic Information, Manufacturing Base and Competitors

Table 46. SG MICRO Major Business

Table 47. SG MICRO Bidirectional Bus Buffer Product and Services

Table 48. SG MICRO Bidirectional Bus Buffer Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 49. SG MICRO Recent Developments/Updates

Table 50. Nexperia Basic Information, Manufacturing Base and Competitors

Table 51. Nexperia Major Business

Table 52. Nexperia Bidirectional Bus Buffer Product and Services

Table 53. Nexperia Bidirectional Bus Buffer Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)

Table 54. Nexperia Recent Developments/Updates

Table 55. Adafruit Basic Information, Manufacturing Base and Competitors

Table 56. Adafruit Major Business

- Table 57. Adafruit Bidirectional Bus Buffer Product and Services
- Table 58. Adafruit Bidirectional Bus Buffer Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)
- Table 59. Adafruit Recent Developments/Updates
- Table 60. Analog Devices Basic Information, Manufacturing Base and Competitors
- Table 61. Analog Devices Major Business
- Table 62. Analog Devices Bidirectional Bus Buffer Product and Services
- Table 63. Analog Devices Bidirectional Bus Buffer Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)
- Table 64. Analog Devices Recent Developments/Updates
- Table 65. NTE Electronics Basic Information, Manufacturing Base and Competitors
- Table 66. NTE Electronics Major Business
- Table 67. NTE Electronics Bidirectional Bus Buffer Product and Services
- Table 68. NTE Electronics Bidirectional Bus Buffer Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)
- Table 69. NTE Electronics Recent Developments/Updates
- Table 70. Diodes Incorporated Basic Information, Manufacturing Base and Competitors
- Table 71. Diodes Incorporated Major Business
- Table 72. Diodes Incorporated Bidirectional Bus Buffer Product and Services
- Table 73. Diodes Incorporated Bidirectional Bus Buffer Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)
- Table 74. Diodes Incorporated Recent Developments/Updates
- Table 75. Renesas Electronics Basic Information, Manufacturing Base and Competitors
- Table 76. Renesas Electronics Major Business
- Table 77. Renesas Electronics Bidirectional Bus Buffer Product and Services
- Table 78. Renesas Electronics Bidirectional Bus Buffer Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)
- Table 79. Renesas Electronics Recent Developments/Updates
- Table 80. Teledyne Basic Information, Manufacturing Base and Competitors
- Table 81. Teledyne Major Business
- Table 82. Teledyne Bidirectional Bus Buffer Product and Services
- Table 83. Teledyne Bidirectional Bus Buffer Sales Quantity (Million Units), Average Price (US\$/Unit), Revenue (USD Million), Gross Margin and Market Share (2021-2026)
- Table 84. Teledyne Recent Developments/Updates
- Table 85. Global Bidirectional Bus Buffer Sales Quantity by Manufacturer (2021-2026) &

(Million Units)

Table 86. Global Bidirectional Bus Buffer Revenue by Manufacturer (2021-2026) & (USD Million)

Table 87. Global Bidirectional Bus Buffer Average Price by Manufacturer (2021-2026) & (US\$/Unit)

Table 88. Market Position of Manufacturers in Bidirectional Bus Buffer, (Tier 1, Tier 2, and Tier 3), Based on Revenue in 2025

Table 89. Head Office and Bidirectional Bus Buffer Production Site of Key Manufacturer

Table 90. Bidirectional Bus Buffer Market: Company Product Type Footprint

Table 91. Bidirectional Bus Buffer Market: Company Product Application Footprint

Table 92. Bidirectional Bus Buffer New Market Entrants and Barriers to Market Entry

Table 93. Bidirectional Bus Buffer Mergers, Acquisition, Agreements, and Collaborations

Table 94. Global Bidirectional Bus Buffer Consumption Value by Region (2021-2025-2032) & (USD Million) & CAGR

Table 95. Global Bidirectional Bus Buffer Sales Quantity by Region (2021-2026) & (Million Units)

Table 96. Global Bidirectional Bus Buffer Sales Quantity by Region (2027-2032) & (Million Units)

Table 97. Global Bidirectional Bus Buffer Consumption Value by Region (2021-2026) & (USD Million)

Table 98. Global Bidirectional Bus Buffer Consumption Value by Region (2027-2032) & (USD Million)

Table 99. Global Bidirectional Bus Buffer Average Price by Region (2021-2026) & (US\$/Unit)

Table 100. Global Bidirectional Bus Buffer Average Price by Region (2027-2032) & (US\$/Unit)

Table 101. Global Bidirectional Bus Buffer Sales Quantity by Voltage (2021-2026) & (Million Units)

Table 102. Global Bidirectional Bus Buffer Sales Quantity by Voltage (2027-2032) & (Million Units)

Table 103. Global Bidirectional Bus Buffer Consumption Value by Voltage (2021-2026) & (USD Million)

Table 104. Global Bidirectional Bus Buffer Consumption Value by Voltage (2027-2032) & (USD Million)

Table 105. Global Bidirectional Bus Buffer Average Price by Voltage (2021-2026) & (US\$/Unit)

Table 106. Global Bidirectional Bus Buffer Average Price by Voltage (2027-2032) & (US\$/Unit)

Table 107. Global Bidirectional Bus Buffer Sales Quantity by Application (2021-2026) & (Million Units)

Table 108. Global Bidirectional Bus Buffer Sales Quantity by Application (2027-2032) & (Million Units)

Table 109. Global Bidirectional Bus Buffer Consumption Value by Application (2021-2026) & (USD Million)

Table 110. Global Bidirectional Bus Buffer Consumption Value by Application (2027-2032) & (USD Million)

Table 111. Global Bidirectional Bus Buffer Average Price by Application (2021-2026) & (US\$/Unit)

Table 112. Global Bidirectional Bus Buffer Average Price by Application (2027-2032) & (US\$/Unit)

Table 113. North America Bidirectional Bus Buffer Sales Quantity by Voltage (2021-2026) & (Million Units)

Table 114. North America Bidirectional Bus Buffer Sales Quantity by Voltage (2027-2032) & (Million Units)

Table 115. North America Bidirectional Bus Buffer Sales Quantity by Application (2021-2026) & (Million Units)

Table 116. North America Bidirectional Bus Buffer Sales Quantity by Application (2027-2032) & (Million Units)

Table 117. North America Bidirectional Bus Buffer Sales Quantity by Country (2021-2026) & (Million Units)

Table 118. North America Bidirectional Bus Buffer Sales Quantity by Country (2027-2032) & (Million Units)

Table 119. North America Bidirectional Bus Buffer Consumption Value by Country (2021-2026) & (USD Million)

Table 120. North America Bidirectional Bus Buffer Consumption Value by Country (2027-2032) & (USD Million)

Table 121. Europe Bidirectional Bus Buffer Sales Quantity by Voltage (2021-2026) & (Million Units)

Table 122. Europe Bidirectional Bus Buffer Sales Quantity by Voltage (2027-2032) & (Million Units)

Table 123. Europe Bidirectional Bus Buffer Sales Quantity by Application (2021-2026) & (Million Units)

Table 124. Europe Bidirectional Bus Buffer Sales Quantity by Application (2027-2032) & (Million Units)

Table 125. Europe Bidirectional Bus Buffer Sales Quantity by Country (2021-2026) & (Million Units)

Table 126. Europe Bidirectional Bus Buffer Sales Quantity by Country (2027-2032) &

(Million Units)

Table 127. Europe Bidirectional Bus Buffer Consumption Value by Country (2021-2026) & (USD Million)

Table 128. Europe Bidirectional Bus Buffer Consumption Value by Country (2027-2032) & (USD Million)

Table 129. Asia-Pacific Bidirectional Bus Buffer Sales Quantity by Voltage (2021-2026) & (Million Units)

Table 130. Asia-Pacific Bidirectional Bus Buffer Sales Quantity by Voltage (2027-2032) & (Million Units)

Table 131. Asia-Pacific Bidirectional Bus Buffer Sales Quantity by Application (2021-2026) & (Million Units)

Table 132. Asia-Pacific Bidirectional Bus Buffer Sales Quantity by Application (2027-2032) & (Million Units)

Table 133. Asia-Pacific Bidirectional Bus Buffer Sales Quantity by Region (2021-2026) & (Million Units)

Table 134. Asia-Pacific Bidirectional Bus Buffer Sales Quantity by Region (2027-2032) & (Million Units)

Table 135. Asia-Pacific Bidirectional Bus Buffer Consumption Value by Region (2021-2026) & (USD Million)

Table 136. Asia-Pacific Bidirectional Bus Buffer Consumption Value by Region (2027-2032) & (USD Million)

Table 137. South America Bidirectional Bus Buffer Sales Quantity by Voltage (2021-2026) & (Million Units)

Table 138. South America Bidirectional Bus Buffer Sales Quantity by Voltage (2027-2032) & (Million Units)

Table 139. South America Bidirectional Bus Buffer Sales Quantity by Application (2021-2026) & (Million Units)

Table 140. South America Bidirectional Bus Buffer Sales Quantity by Application (2027-2032) & (Million Units)

Table 141. South America Bidirectional Bus Buffer Sales Quantity by Country (2021-2026) & (Million Units)

Table 142. South America Bidirectional Bus Buffer Sales Quantity by Country (2027-2032) & (Million Units)

Table 143. South America Bidirectional Bus Buffer Consumption Value by Country (2021-2026) & (USD Million)

Table 144. South America Bidirectional Bus Buffer Consumption Value by Country (2027-2032) & (USD Million)

Table 145. Middle East & Africa Bidirectional Bus Buffer Sales Quantity by Voltage (2021-2026) & (Million Units)

Table 146. Middle East & Africa Bidirectional Bus Buffer Sales Quantity by Voltage (2027-2032) & (Million Units)

Table 147. Middle East & Africa Bidirectional Bus Buffer Sales Quantity by Application (2021-2026) & (Million Units)

Table 148. Middle East & Africa Bidirectional Bus Buffer Sales Quantity by Application (2027-2032) & (Million Units)

Table 149. Middle East & Africa Bidirectional Bus Buffer Sales Quantity by Country (2021-2026) & (Million Units)

Table 150. Middle East & Africa Bidirectional Bus Buffer Sales Quantity by Country (2027-2032) & (Million Units)

Table 151. Middle East & Africa Bidirectional Bus Buffer Consumption Value by Country (2021-2026) & (USD Million)

Table 152. Middle East & Africa Bidirectional Bus Buffer Consumption Value by Country (2027-2032) & (USD Million)

Table 153. Bidirectional Bus Buffer Raw Material

Table 154. Key Manufacturers of Bidirectional Bus Buffer Raw Materials

Table 155. Bidirectional Bus Buffer Typical Distributors

Table 156. Bidirectional Bus Buffer Typical Customers

List Of Figures

LIST OF FIGURES

Figure 1. Bidirectional Bus Buffer Picture

Figure 2. Global Bidirectional Bus Buffer Revenue by Voltage, (USD Million), 2021 & 2025 & 2032

Figure 3. Global Bidirectional Bus Buffer Revenue Market Share by Voltage in 2025

Figure 4. 3.6V Examples

Figure 5. 5.5V Examples

Figure 6. 6V Examples

Figure 7. Global Bidirectional Bus Buffer Revenue by Current Sinking Capability, (USD Million), 2021 & 2025 & 2032

Figure 8. Global Bidirectional Bus Buffer Revenue Market Share by Current Sinking Capability in 2025

Figure 9. 8 mA Examples

Figure 10. 24 mA Examples

Figure 11. Others Examples

Figure 12. Global Bidirectional Bus Buffer Revenue by Packaging Method, (USD Million), 2021 & 2025 & 2032

Figure 13. Global Bidirectional Bus Buffer Revenue Market Share by Packaging Method in 2025

Figure 14. DW Package Examples

Figure 15. N Package Examples

Figure 16. Global Bidirectional Bus Buffer Consumption Value by Application, (USD Million), 2021 & 2025 & 2032

Figure 17. Global Bidirectional Bus Buffer Revenue Market Share by Application in 2025

Figure 18. Industrial Control Examples

Figure 19. Automotive Electronics Examples

Figure 20. Smart Home Examples

Figure 21. Others Examples

Figure 22. Global Bidirectional Bus Buffer Consumption Value, (USD Million): 2021 & 2025 & 2032

Figure 23. Global Bidirectional Bus Buffer Consumption Value and Forecast (2021-2032) & (USD Million)

Figure 24. Global Bidirectional Bus Buffer Sales Quantity (2021-2032) & (Million Units)

Figure 25. Global Bidirectional Bus Buffer Price (2021-2032) & (US\$/Unit)

Figure 26. Global Bidirectional Bus Buffer Sales Quantity Market Share by Manufacturer in 2025

Figure 27. Global Bidirectional Bus Buffer Revenue Market Share by Manufacturer in 2025

Figure 28. Producer Shipments of Bidirectional Bus Buffer by Manufacturer Sales (\$MM) and Market Share (%): 2025

Figure 29. Top 3 Bidirectional Bus Buffer Manufacturer (Revenue) Market Share in 2025

Figure 30. Top 6 Bidirectional Bus Buffer Manufacturer (Revenue) Market Share in 2025

Figure 31. Global Bidirectional Bus Buffer Sales Quantity Market Share by Region (2021-2032)

Figure 32. Global Bidirectional Bus Buffer Consumption Value Market Share by Region (2021-2032)

Figure 33. North America Bidirectional Bus Buffer Consumption Value (2021-2032) & (USD Million)

Figure 34. Europe Bidirectional Bus Buffer Consumption Value (2021-2032) & (USD Million)

Figure 35. Asia-Pacific Bidirectional Bus Buffer Consumption Value (2021-2032) & (USD Million)

Figure 36. South America Bidirectional Bus Buffer Consumption Value (2021-2032) & (USD Million)

Figure 37. Middle East & Africa Bidirectional Bus Buffer Consumption Value (2021-2032) & (USD Million)

Figure 38. Global Bidirectional Bus Buffer Sales Quantity Market Share by Voltage (2021-2032)

Figure 39. Global Bidirectional Bus Buffer Consumption Value Market Share by Voltage (2021-2032)

Figure 40. Global Bidirectional Bus Buffer Average Price by Voltage (2021-2032) & (US\$/Unit)

Figure 41. Global Bidirectional Bus Buffer Sales Quantity Market Share by Application (2021-2032)

Figure 42. Global Bidirectional Bus Buffer Revenue Market Share by Application (2021-2032)

Figure 43. Global Bidirectional Bus Buffer Average Price by Application (2021-2032) & (US\$/Unit)

Figure 44. North America Bidirectional Bus Buffer Sales Quantity Market Share by Voltage (2021-2032)

Figure 45. North America Bidirectional Bus Buffer Sales Quantity Market Share by Application (2021-2032)

Figure 46. North America Bidirectional Bus Buffer Sales Quantity Market Share by Country (2021-2032)

Figure 47. North America Bidirectional Bus Buffer Consumption Value Market Share by

Country (2021-2032)

Figure 48. United States Bidirectional Bus Buffer Consumption Value (2021-2032) & (USD Million)

Figure 49. Canada Bidirectional Bus Buffer Consumption Value (2021-2032) & (USD Million)

Figure 50. Mexico Bidirectional Bus Buffer Consumption Value (2021-2032) & (USD Million)

Figure 51. Europe Bidirectional Bus Buffer Sales Quantity Market Share by Voltage (2021-2032)

Figure 52. Europe Bidirectional Bus Buffer Sales Quantity Market Share by Application (2021-2032)

Figure 53. Europe Bidirectional Bus Buffer Sales Quantity Market Share by Country (2021-2032)

Figure 54. Europe Bidirectional Bus Buffer Consumption Value Market Share by Country (2021-2032)

Figure 55. Germany Bidirectional Bus Buffer Consumption Value (2021-2032) & (USD Million)

Figure 56. France Bidirectional Bus Buffer Consumption Value (2021-2032) & (USD Million)

Figure 57. United Kingdom Bidirectional Bus Buffer Consumption Value (2021-2032) & (USD Million)

Figure 58. Russia Bidirectional Bus Buffer Consumption Value (2021-2032) & (USD Million)

Figure 59. Italy Bidirectional Bus Buffer Consumption Value (2021-2032) & (USD Million)

Figure 60. Asia-Pacific Bidirectional Bus Buffer Sales Quantity Market Share by Voltage (2021-2032)

Figure 61. Asia-Pacific Bidirectional Bus Buffer Sales Quantity Market Share by Application (2021-2032)

Figure 62. Asia-Pacific Bidirectional Bus Buffer Sales Quantity Market Share by Region (2021-2032)

Figure 63. Asia-Pacific Bidirectional Bus Buffer Consumption Value Market Share by Region (2021-2032)

Figure 64. China Bidirectional Bus Buffer Consumption Value (2021-2032) & (USD Million)

Figure 65. Japan Bidirectional Bus Buffer Consumption Value (2021-2032) & (USD Million)

Figure 66. South Korea Bidirectional Bus Buffer Consumption Value (2021-2032) & (USD Million)

Figure 67. India Bidirectional Bus Buffer Consumption Value (2021-2032) & (USD Million)

Figure 68. Southeast Asia Bidirectional Bus Buffer Consumption Value (2021-2032) & (USD Million)

Figure 69. Australia Bidirectional Bus Buffer Consumption Value (2021-2032) & (USD Million)

Figure 70. South America Bidirectional Bus Buffer Sales Quantity Market Share by Voltage (2021-2032)

Figure 71. South America Bidirectional Bus Buffer Sales Quantity Market Share by Application (2021-2032)

Figure 72. South America Bidirectional Bus Buffer Sales Quantity Market Share by Country (2021-2032)

Figure 73. South America Bidirectional Bus Buffer Consumption Value Market Share by Country (2021-2032)

Figure 74. Brazil Bidirectional Bus Buffer Consumption Value (2021-2032) & (USD Million)

Figure 75. Argentina Bidirectional Bus Buffer Consumption Value (2021-2032) & (USD Million)

Figure 76. Middle East & Africa Bidirectional Bus Buffer Sales Quantity Market Share by Voltage (2021-2032)

Figure 77. Middle East & Africa Bidirectional Bus Buffer Sales Quantity Market Share by Application (2021-2032)

Figure 78. Middle East & Africa Bidirectional Bus Buffer Sales Quantity Market Share by Country (2021-2032)

Figure 79. Middle East & Africa Bidirectional Bus Buffer Consumption Value Market Share by Country (2021-2032)

Figure 80. Turkey Bidirectional Bus Buffer Consumption Value (2021-2032) & (USD Million)

Figure 81. Egypt Bidirectional Bus Buffer Consumption Value (2021-2032) & (USD Million)

Figure 82. Saudi Arabia Bidirectional Bus Buffer Consumption Value (2021-2032) & (USD Million)

Figure 83. South Africa Bidirectional Bus Buffer Consumption Value (2021-2032) & (USD Million)

Figure 84. Bidirectional Bus Buffer Market Drivers

Figure 85. Bidirectional Bus Buffer Market Restraints

Figure 86. Bidirectional Bus Buffer Market Trends

Figure 87. Porters Five Forces Analysis

Figure 88. Manufacturing Cost Structure Analysis of Bidirectional Bus Buffer in 2025

Figure 89. Manufacturing Process Analysis of Bidirectional Bus Buffer

Figure 90. Bidirectional Bus Buffer Industrial Chain

Figure 91. Sales Channel: Direct to End-User vs Distributors

Figure 92. Direct Channel Pros & Cons

Figure 93. Indirect Channel Pros & Cons

Figure 94. Methodology

Figure 95. Research Process and Data Source

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