

Error Correction Technology Trends: Market Research Report

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Abstracts

The report analyzes and presents an overview of Error Correction Technology market worldwide. 'Error Correction works as a defined protocol, which sets agreements on the data handling way between modems. This involves calculating a checksum on a data block. This checksum is used as a data description and reflects changes, if any, in the block quite accurately. A modem calculates the data checksums during data flow. On a regular basis Checksum numbers are sent to the other side. On completion of a data block, the other side, also keeping a checksum, compares both the checksums. In case of any error, a request to resend the entire data block is generated.'

The report further discusses about practical purposes, applications, and future prospects of the technology along with the strategic industry activities of major companies worldwide. In addition, major companies operating in the Error Correction Technology arena worldwide including Altera Corporation, Arasan Chip Systems Inc., Ekinops, Intel Corporation, Mitsubishi Electric Corporation, PMC-Sierra Inc., Vitesse Semiconductor Corporation, Xrio Ltd., and others are profiled.

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A Prelude

Forward Error Correction - Introduction

Forward Error Correction - Applications

Practical Purposes

Error Correction Protocol

2. MARKET TRENDS

Burgeoning Bandwidth Demand Driving Deployment of High-Speed OTN Links

3. RECENT INDUSTRY ACTIVITY

Mitsubishi Electric Unveils New Error-Correction Technology for Long-Haul Transmissions

Comtech Systems Receives Order for Additional Modem Upgrade Kits

Ekinops Introduces Tunable Transponder

TAEC Unveils SLC-based BENAND Flash Memory

TVU Networks Develops Enhanced TVUPack Portable Cellular Uplink Solution

Altera Introduces Integrated EFEC IP Cores for High-Performance Stratix IV and Stratix V FPGA Series

Bezeq Selects Ekinops for Providing Optical Transport Equipment

PMC-Sierra to Takeover RAD3 Communications

PMC-Sierra Introduces Sizzle FEC Technology for Cost-Effective OTN Deployments

Vitesse and Applied Micro Announce Collaboration on Standard Approach for eFEC Technology for Emerging OTN Applications

Life Technologies Inks Exclusive License Agreement for Enzymatic Technology

Lyric Semiconductor Unveils New Lyric Error Correction Technology

4. MARKET PARTICIPANTS

Advantest America Corporation (USA)

AHA Products Group (USA)

Altera Corporation (USA)

Analogies SA (Greece)

Applied Micro Circuits Corporation (USA)

Arasan Chip Systems, Inc. (USA)
Cirrus Logic, Inc. (USA)
ECC Technologies, Inc. (USA)
Ekinops (France)
Harmonic, Inc. (USA)
Huawei Technologies Co. , Ltd. (China)
IMEC (Belgium)
Infinera Corporation (USA)
Intel Corporation (USA)
KenCast, Inc. (USA)
MAYAH Communications GmbH (Germany)
Micron Technology, Inc. (USA)
Mitsubishi Electric Corporation (Japan)
Noesis Technologies LP (Greece)
Novici Biotech LLC (USA)
PMC-Sierra, Inc. (USA)
Qualcomm Technologies, Inc. (USA)

REF TEK (USA)

ROMO Wind (Switzerland)
Siglead, Inc. (Japan)
Sony Corporation (Japan)
Tera Microelectronics Ltd. (Turkey)
Toshiba America Electronic Components, Inc. (USA)
Toshiba Microelectronics Corporation (Japan)
TrellisWare Technologies, Inc. (USA)
Turboconcept SAS (France)
TVU Networks, Inc. (USA)
Verayo, Inc. (USA)
ViaSat, Inc. (USA)
Vitesse Semiconductor Corporation (USA)
Xilinx, Inc. (USA)
Xrio Ltd. (UK)

5. APPENDIX

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