

# The Global Advanced IC Substrate Market 2025-2035

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# **Abstracts**

The global advanced IC substrate market is undergoing a significant transformation, driven by the increasing complexity of semiconductor designs and the rise of artificial intelligence applications. IC substrates have become a critical component in the semiconductor manufacturing ecosystem, particularly as 3D packaging technologies continue to evolve. Advanced IC substrates serve as the critical interface between semiconductor chips and printed circuit boards, providing electrical connections, mechanical support, and thermal management. The market encompasses several key technologies: organic core substrates, glass core substrates (GCS), substrate-like PCBs (SLP), and embedded die technologies. Each of these platforms addresses specific requirements across applications ranging from high-performance computing to mobile devices and automotive electronics.

The market is experiencing robust growth. This growth trajectory is primarily fueled by the increasing demands of AI accelerators, data center applications, and advanced mobile processors, all of which require increasingly sophisticated substrate solutions. IC substrates are advancing along four critical dimensions: increased height (layer count), larger sizes, greater precision, and improved flatness. Substrate dimensions, which measured approximately 75x60mm in 2020, are projected to reach 150x150mm by 2026—representing a dramatic increase in area to accommodate larger, more complex chips. Simultaneously, layer counts are expected to increase from 20 to 28 layers by 2026, a 40% growth that reflects the increasing interconnect density requirements of next-generation semiconductors.

The technical requirements are becoming increasingly stringent across all substrate types. For organic substrates, manufacturers are pushing toward line/space dimensions below 5/5?m, with leading-edge products already implementing 8/8?m in production volumes. Glass core substrates are emerging as a potential solution for ultra-high-density applications, though they remain in early commercialization stages. Substrate-like PCB technology continues to penetrate the mobile and consumer segments, while embedded die technology finds growing applications in automotive and industrial



markets. The global supply chain for advanced IC substrates remains concentrated in East Asia, with Japan, Taiwan, South Korea, and increasingly China hosting major manufacturing capabilities.

Investment in advanced IC substrate manufacturing capacity has accelerated significantly since 2022, with major expansions announced across Taiwan, Japan, South Korea, and China. These investments are being driven both by market growth projections and by supply chain resilience concerns, which have prompted geographic diversification of manufacturing capabilities. Looking forward, technological innovation will continue to reshape the market. Key development areas include ultra-fine line/space formation, warpage control for larger substrates, new materials with improved electrical and thermal properties, and manufacturing processes capable of higher precision at larger panel sizes. The integration of glass core substrates for high-performance applications and the evolution of embedded die technologies will further expand the capabilities of advanced IC substrates.

The Global Advanced IC Substrate Market 2025-2035 provides an in-depth analysis of the rapidly evolving advanced IC substrate industry from 2025 to 2035. As semiconductor packaging becomes increasingly critical to system performance, advanced IC substrates have emerged as a cornerstone technology enabling next-generation computing, AI acceleration, automotive electronics, and mobile devices. The report examines the transition from traditional organic substrates to emerging glass core substrates and embedded die technologies, analyzing how these platforms will reshape semiconductor packaging capabilities. Covering line/space evolution from current 8/8?m to sub-2?m, substrate form factor expansion to 150?150mm, and layer count increases to 28+, this analysis provides essential strategic intelligence for stakeholders throughout the semiconductor supply chain.

Report contents include:

Complete Market Sizing and Forecasting: Detailed revenue projections, production volumes, and compound annual growth rates across all substrate technologies from 2025-2035

Technology Evolution Roadmaps: Comprehensive analysis of organic, glass core, substrate-like PCB, and embedded die technology developments with clear migration paths

Application-Specific Requirements: Detailed specifications for AI accelerators, data center, automotive, mobile, and consumer electronics applications

Manufacturing Process Innovation: Analysis of advanced process technologies



including amSAP, TGV formation, and ultra-fine line/space fabrication

Supply Chain Mapping: Complete ecosystem analysis covering raw materials, equipment, manufacturing capabilities, and regional strengths/vulnerabilities

Competitive Landscape: Detailed profiles of 115+ companies across the substrate manufacturing, materials, equipment, and semiconductor design ecosystem

Sustainability Analysis: Environmental impact assessment, carbon footprint comparison, and ESG roadmaps for the substrate industry

The report covers:

Technical evolution of line/space capabilities, via sizes, form factors, and layer counts

Glass core substrate emergence and commercialization timeline

Substrate-like PCB expansion beyond mobile into automotive and IoT

Embedded die technology integration strategies for active and passive components

Co-packaged optics substrate requirements and implementation approaches

Next-generation manufacturing technologies including AI-assisted design and additive fabrication

Regional manufacturing capability development and reshoring initiatives

Supply chain vulnerabilities, mitigation strategies, and diversification approaches

Long-term sustainability considerations including water usage, carbon footprint, and circular economy strategies

This definitive industry report provides detailed profiles of 115+ companies across the



advanced IC substrate ecosystem, including: 3DGSinc, Aavco, Absolics, ACE-Pillar, Achilles, AGC, AKC, Ajinomoto, AMD, Anano, AP-Solution, Applied Materials, ASE, AT&S Austria Technologie & Systemtechnik, BOE, CGP Materials, CHD Tech, Chemtronics, Ckplas, Coherent, Corning, Covinc, DMS, DNP, Dupont, E&R, Evatec, Extolchem, F&S Tech, Fujikura, Fujitsu, GigaPhoton, Gpline, Google, Guihua, Hanbit-Laser, HB Technology, Hoemyeong-Industry, Ibiden, Infineon, Innometry, Intel, JoongWoo, JTNC, Jusung-Engineering, KCC-Glass, KLA, Kinsus, Koto, Lam Research, Lante, LG Innotek, Lincotec, LPKF, LTC, Mactech, Man, MediaTek, Micro-technology, Mirae, Mitsui, Mosaic Microsystems, Murata, Nan Ya PCB, Nanya, Neontech, NEG, NVIDIA, NSG-Group, Onto Innovation, Pengcheng, Philoptics, PlanOptik, Qorvo, Qualcomm, Rena, Samsung Electro-Mechanics and more.....

This market report is essential for IC substrate manufacturers, semiconductor companies, materials suppliers, equipment providers, packaging houses, investment firms, and technology strategists seeking to navigate the rapidly evolving advanced IC substrate landscape. With substrate technology becoming increasingly critical to semiconductor performance and system integration, this analysis provides the strategic intelligence needed to identify opportunities, mitigate risks, and capitalize on the next decade of advanced packaging innovation.



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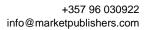


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+357 96 030922 info@marketpublishers.com

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