

Global Wafer and Polishing Pad Competitive Landscape Professional Research Report 2025

<https://marketpublishers.com/r/WA2262EFA07AEN.html>

Date: June 2025

Pages: 165

Price: US\$ 3,500.00 (Single User License)

ID: WA2262EFA07AEN

Abstracts

Market Overview

According to DIResearch's in-depth investigation and research, the global Wafer and Polishing Pad market size will reach Million USD in 2025 and is projected to reach Million USD by 2032, with a CAGR of % (2025-2032). Notably, the China Wafer and Polishing Pad market has changed rapidly in the past few years. By 2025, China's market size is expected to be Million USD, representing approximately % of the global market share.

Research Summary

A wafer is a thin, flat slice of semiconductor material, usually silicon, used as a substrate for fabricating integrated circuits (ICs) and other electronic components. These wafers are meticulously processed and polished to have a smooth, flat surface onto which layers of semiconductor materials and conductive traces are deposited during the fabrication process. Polishing pads are integral tools employed in the semiconductor manufacturing process for planarizing and refining the surface of these wafers. Composed of soft, resilient materials such as polyurethane or porous materials embedded with abrasive particles, polishing pads facilitate a process called chemical mechanical planarization (CMP). During CMP, the wafer is pressed against the polishing pad and moved in a controlled manner while an abrasive slurry is applied. This slurry gradually removes imperfections and irregularities from the wafer's surface, ensuring uniformity and precision in the fabrication of semiconductor devices.

The major global manufacturers of Wafer and Polishing Pad include Shin-Etsu Chemical, Sumco, Global Wafers, Siltronic, SK siltron, Waferworks, Ferrotec, AST,

Gritek, Guosheng, QL Electronics, MCL, National Silicon Industry Group, Poshing, Zhonghuan, DuPont, Cabot, FUJIBO, etc. The global players competition landscape in this report is divided into three tiers. The first tier comprises global leading enterprises that command a substantial market share, hold a dominant industry position, possess strong competitiveness and influence, and generate significant revenue. The second tier includes companies with a notable market presence and reputation; these firms actively follow industry leaders in product, service, or technological innovation and maintain a moderate revenue scale. The third tier consists of smaller companies with limited market share and lower brand recognition, primarily focused on local markets and generating comparatively lower revenue.

This report studies the market size, price trends and future development prospects of Wafer and Polishing Pad. Focus on analysing the market share, product portfolio, prices, sales, revenue and gross profit margin of global major manufacturers, as well as the market status and trends of different product types and applications in the global Wafer and Polishing Pad market. The report data covers historical data from 2020 to 2024, based year in 2025 and forecast data from 2026 to 2032.

The regions and countries in the report include North America, Europe, China, APAC (excl. China), Latin America and Middle East and Africa, covering the Wafer and Polishing Pad market conditions and future development trends of key regions and countries, combined with industry-related policies and the latest technological developments, analyze the development characteristics of Wafer and Polishing Pad industries in various regions and countries, help companies understand the development characteristics of each region, help companies formulate business strategies, and achieve the ultimate goal of the company's global development strategy.

The data sources of this report mainly include the National Bureau of Statistics, customs databases, industry associations, corporate financial reports, third-party databases, etc. Among them, macroeconomic data mainly comes from the National Bureau of Statistics, International Economic Research Organization; industry statistical data mainly come from industry associations; company data mainly comes from interviews, public information collection, third-party reliable databases, and price data mainly comes from various markets monitoring database.

Global Key Manufacturers of Wafer and Polishing Pad Include:

Shin-Etsu Chemical

Sumco

Global Wafers

Siltronic

SK siltron

Waferworks

Ferrotec

AST

Gritek

Guosheng

QL Electronics

MCL

National Silicon Industry Group

Poshing

Zhonghuan

DuPont

Cabot

FUJIBO

Wafer and Polishing Pad Product Segment Include:

Carbide Wafer

Carbide Wafer Polishing Pad

Wafer and Polishing Pad Product Application Include:

Memory

Logic and MPU

Analog

Discrete Device and Sensor

Other

Chapter Scope

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