

Global PCIe IP Competitive Landscape Professional Research Report 2025

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Abstracts

Market Overview

According to DIResearch's in-depth investigation and research, the global PCIe IP market size will reach Million USD in 2025 and is projected to reach Million USD by 2032, with a CAGR of % (2025-2032). Notably, the China PCIe IP market has changed rapidly in the past few years. By 2025, China's market size is expected to be Million USD, representing approximately % of the global market share.

Research Summary

PCIe IP (Peripheral Component Interconnect Express Intellectual Property) is a pre-designed digital module used in semiconductor design to integrate PCIe communication capabilities into custom chips or devices. PCIe is a high-speed interface standard widely utilized for connecting hardware components like processors, GPUs, and storage devices. PCIe IP provides the core functionality required for implementing PCIe protocols, including data transfer, link management, and error handling, without requiring developers to design these features from scratch. By using PCIe IP, manufacturers can reduce development time, ensure protocol compliance, and achieve reliable, high-performance connectivity in applications ranging from computing and networking to storage and embedded systems.

The major global suppliers of PCIe IP include Synopsys, ARM, CEVA, Cadence, Rambus, Actt, Arasan, Alphawave, etc. The global players competition landscape in this report is divided into three tiers. The first tier comprises global leading enterprises that command a substantial market share, hold a dominant industry position, possess strong competitiveness and influence, and generate significant revenue. The second tier

includes companies with a notable market presence and reputation; these firms actively follow industry leaders in product, service, or technological innovation and maintain a moderate revenue scale. The third tier consists of smaller companies with limited market share and lower brand recognition, primarily focused on local markets and generating comparatively lower revenue.

This report studies the market size, price trends and future development prospects of PCIe IP. Focus on analysing the market share, product portfolio, prices, sales, revenue and gross profit margin of global major suppliers, as well as the market status and trends of different product types and applications in the global PCIe IP market. The report data covers historical data from 2020 to 2024, based year in 2025 and forecast data from 2026 to 2032.

The regions and countries in the report include North America, Europe, China, APAC (excl. China), Latin America and Middle East and Africa, covering the PCIe IP market conditions and future development trends of key regions and countries, combined with industry-related policies and the latest technological developments, analyze the development characteristics of PCIe IP industries in various regions and countries, help companies understand the development characteristics of each region, help companies formulate business strategies, and achieve the ultimate goal of the company's global development strategy.

The data sources of this report mainly include the National Bureau of Statistics, customs databases, industry associations, corporate financial reports, third-party databases, etc. Among them, macroeconomic data mainly comes from the National Bureau of Statistics, International Economic Research Organization; industry statistical data mainly come from industry associations; company data mainly comes from interviews, public information collection, third-party reliable databases, and price data mainly comes from various markets monitoring database.

Global Key Suppliers of PCIe IP Include:

Synopsys

ARM

CEVA

Cadence

Rambus

Actt

Arasan

Alphawave

PCIe IP Product Segment Include:

PCIe 3/4

PCIe 5

Other

PCIe IP Product Application Include:

Automotive

Mobile Device

Data Center

Other

Chapter Scope

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