

Global LiDAR-on-Chip Market Research Report 2026(Status and Outlook)

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Abstracts

The 2025 U.S. tariff policies introduce profound uncertainty into the global economic landscape. This report critically examines the implications of recent tariff adjustments and international strategic countermeasures on LiDAR-on-Chip competitive dynamics, regional economic interdependencies, and supply chain reconfigurations. LiDAR-on-Chip (LoC) refers to the integration of Light Detection and Ranging (LiDAR) systems onto a single semiconductor chip, typically using silicon photonics or CMOS-compatible processes. Unlike traditional bulky LiDAR systems that rely on discrete mechanical components and optics, LiDAR-on-Chip devices use miniaturized, solid-state architectures to perform the same functions—emitting laser pulses, detecting reflections, and computing distances—within a much smaller, cost-effective, and power-efficient footprint. LiDAR-on-Chip (LoC) systems typically operate at wavelengths of 905 nm or 1550 nm, offering detection ranges from 10 to over 300 meters depending on application, with range accuracy as fine as 2 cm and frame rates between 10 Hz to 30 Hz. Advanced LoC models such as those from Aeva and SiLC can detect velocity directly using FMCW technology and achieve angular resolutions under 0.1°, with point clouds exceeding 1 million points per second. Field of view (FoV) ranges from narrow 30° × 30° for focused detection to wide 120° × 90° for panoramic coverage. Power consumption is typically under 5 W (often below 1 W in miniaturized chips), and packages can be as compact as

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